# UTC UNISONIC TECHNOLOGIES CO., LTD

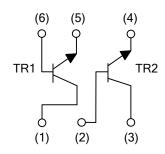
IMX2 **DUAL TRANSISTOR** 

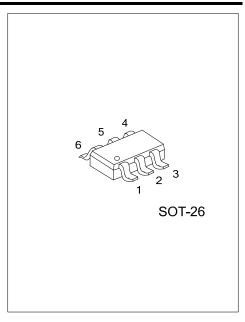
## **NPN GENERAL PURPOSE DUAL TRANSISTOR**

#### **FEATURES**

\* Two independently operating NPN transistors.

#### **EQUIVALENT CIRCUITS**

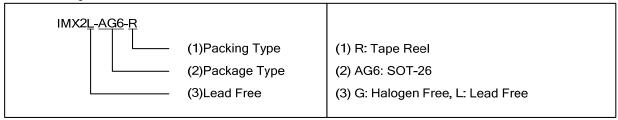




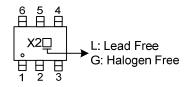
#### **ORDERING INFORMATION**

Ordering Number		Doolsono	Pin Assignment					Doolsing		
Lead Free	Halogen Free	Package	1	2	3	4	5	6	Packing	
IMX2L-AG6 -R	IMX2G-AG6-R	SOT-26	C1	B2	C2	E2	E1	B1	Tape Reel	

Note: Pin Assignment: B: Base C: Collector E: Emitter



#### **MARKING**



Chunnital Balling 1688.com

#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Collector-Base Voltage	$V_{CBO}$	60	V
Collector-Emitter Voltage	$V_{CEO}$	50	V
Emitter-Base Voltage	$V_{EBO}$	7	V
Collector Current	Ic	150	mA
Collector Power Dissipation	Pc	300 (Note1)	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C

Note: 1. 200mW per element must not be exceeded.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> = 50μA	60			V
Collector-Emitter Breakdown Voltage	$BV_CEO$	I <sub>C</sub> = 1mA	50			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> = 50μA	7			V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 60V			0.1	μΑ
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 7V			0.1	μΑ
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C / I_B = 50 \text{mA/5mA}$			0.4	V
DC Current Transfer Ratio	$h_{FE}$	V <sub>CE</sub> = 6V, I <sub>C</sub> = 1mA	120		560	
Transition Frequency (Note)	$f_T$	V <sub>CE</sub> =12V, I <sub>E</sub> =-2mA, f=100MHz		180		MHz
Output Capacitance	Сов	V <sub>CB</sub> = 12V, I <sub>E</sub> =0A, f=1KHz		2	3.5	pF

Note: Transition frequency of the device.



<sup>2.</sup> Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

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