

**UNISONIC TECHNOLOGIES CO., LTD** 

L8312

Preliminary

LINEAR INTEGRATED CIRCUIT

# DUAL POLARIZATION AND TONE SWITCH CONTROLLER

# DESCRIPTION

The UTC **L8312** is specially designed integrated circuit for control satellite receiver front-end block switch and provides customers with dual independent channel of two-control signal: polarization and tone switch controller for LNB switch signal path selection.

The UTC **L8312** is characterized by its two independent channels and each provides logical outputs to show status of LNB switch polarization selection and 22 KHz tone signal.

Minimum number of external discrete components and the wide range of supply voltage make this device suitable in Twin, Quad and multiple feed universal LNB switch to get lower cost and minimum PCB size.

In order to satisfy the special LNB switch model, a tighter specification has been taken into consideration for the polarization switch characteristics. The number of external components is also reduced. The UTC **L8312** includes separate 22K tone detection for each channel. In addition, the built-in tone amplifier circuit is designed to protect transient signal by an external resistor and capacitor simply, and it also provides good bandwidth and sensitivity.

The UTC **L8312** is generally applied in multi output LNB and multi switch.

## FEATURES

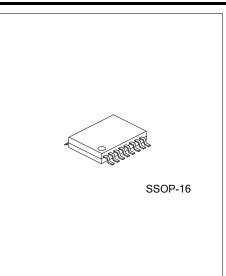
\* Direct Drive PIN Diode and Multiplexer IC

\* High Supply Voltage

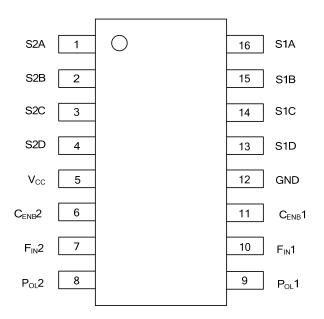
#### ORDERING INFORMATION

Ordering Number	Package	Packing	
L8312G-R16-R	SSOP-16	Tape Reel	





# PIN CONFIGURATION

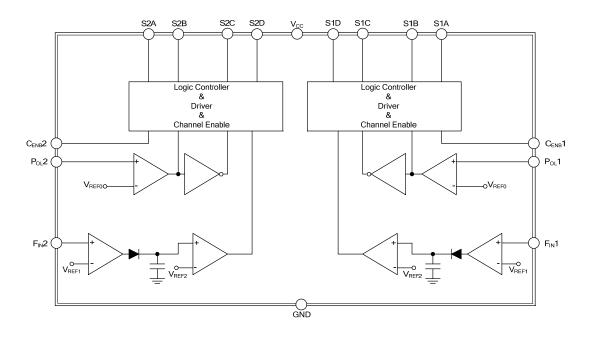


#### PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	S2A	Channel 2 Switch Output A
2	S2B	Channel 2 Switch Output B
3	S2C	Channel 2 Switch Output C
4	S2D	Channel 2 Switch Output D
5	Vcc	Supply Voltage Pin
6	C <sub>ENB</sub> 2	Channel 2 Enable
7	F <sub>IN</sub> 2	Channel 2 22KHz Tone Input Pin
8	P <sub>OL</sub> 2	Channel 2 Polarization Input Pin
9	Po∟1	Channel 1 Polarization Input Pin
10	F <sub>IN</sub> 1	Channel 1 22KHz Tone Input Pin
11	C <sub>ENB</sub> 1	Channel 1 Enable
12	GND	Ground Pin
13	S1D	Channel 1 Switch Output A
14	S1C	Channel 1 Switch Output B
15	S1B	Channel 1 Switch Output C
16	S1A	Channel 1 Switch Output D



# BLOCK DIAGRAM





### ■ ABSOLUTE MAXIMUM RATING (T<sub>A</sub>=-25 ~ +85°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	Vs	-0.6 ~ 12	V
Supply Current	I <sub>S</sub>	100	mA
Input Voltage (P <sub>OL</sub> Pin)	V <sub>IN</sub>	25	V
Power Dissipation (T <sub>A</sub> =25°C)	PD	600	mW
Operating Junction Temperature	TJ	-40 ~ 70	°C
Storage Temperature	T <sub>STG</sub>	-50 ~ 85	°C

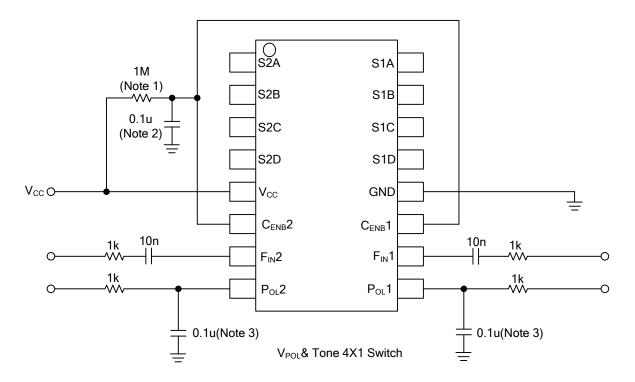
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=5V, T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL TEST CONDITIONS		MIN	TYP	MAX	UNIT
Supply Voltage ( $T_{OPR}$ = -40°C ~ 80°C)	Vcc		5		9	V
Supply Current	Icc	Standby current, no load		10		mA
Output Voltage High	SWHIGH	I <sub>SW</sub> =-5mA	V <sub>cc</sub> -1.0	V <sub>CC</sub> -0.9	Vcc	V
Output Voltage Low	SWLOW	I <sub>SW</sub> =5mA	0	0.3	0.5	
TONE/POLARITY SWITCH CHARACT	ERISTICS					
Tone Switch Threshold	V <sub>TST</sub>	Coupling via 10nF(sin wave)	100	170	350	$mV_{PP}$
Low Frequency Rejection	f <sub>RL</sub>	Coupling via 10nF(sin wave)	1.0	7.5		KHz
Polarity Detector Input Current	IPOL	V <sub>POL</sub> 1, 2 input=25V, via R <sub>POL</sub> =1KΩ	100	130	200	μA
Polarity Detector Threshold Voltage	V <sub>TPOL</sub>	V <sub>POL</sub> 1, 2 input applied via R <sub>POL</sub> =1KΩ	14.0	14.5	15.0	V
Switching Speed	Ts	V <sub>POL</sub> 1, 2 input applied via R <sub>POL</sub> =1KΩ			100	ms
Enable Threshold Voltage	$V_{\text{ENB}}$	I <sub>CENB</sub> =-50μA	0.7	1.2	1.5	V



## TYPICAL APPLICATION CIRCUIT



Note:1. Current limitation resistance

2. Required BYPASS capacitor for noise interference elimination

3. Optional BYPASS capacitor for 22KHz tone interference elimination



≥1.2V

## FUNCTIONAL INFORMATION

The UTC **L8312** is specially designed integrated circuit for control satellite receiver front-end block switch, it contains all the circuits necessary to detect the DC voltage and 22KHz signal on the supply input to the LNB switch.

The tone switch circuit incorporates an operational amplifier used as active filter, a rectifier /a comparator and an output driver. Built-in feedback resistor included in operational amplifier, external component counts are thus minimized. Therefore, low pass filter response can be achieved with a simple external RC.

The gain of operational amplifier must be adjusted with different supply voltage due to the threshold of comparator is supply dependent. To optimize the system performance, the external RC values must be adjusted. The resistor also provides maximum input voltage protection for the P<sub>OL</sub>1, 2 pins. The low pass filter of tone detection circuit allows the UTC **L8312** to reliably detect tone signal. Each channel decodes tone polarization signals to activate one of four outputs. Each of those outputs has the capability of sinking 5mA and sourcing 5mA, providing the capability of driving directly signal routing pin diodes.

In addition, the channel outputs would activate when pins voltage of  $C_{ENB}1$ , 2 set high, which means,  $C_{ENB}1$ , 2 pins input signal have priority over tone polarization signals to control channel outputs.

Table1		_		_		
C <sub>ENB</sub> 1	Tone	V <sub>POL</sub>	S1A	S1B	S1C	S1D
≤0.7V			Low	Low	Low	Low
≥1.2V		<9.5V	Low	Low	Low	Low
≥1.2V	Off	≤14V	High	Low	Low	Low
≥1.2V	Off	≥15.5V	Low	High	Low	Low
≥1.2V	On	≤14V	Low	Low	High	Low
≥1.2V	On	≥15.5V	Low	Low	Low	High

Table 1 shows the logical output truth table.

On

≥15.5V

≥1.2V	On	≤14V	Low	Low	High	Low
≥1.2V	On	≥15.5V	Low	Low	Low	High
C <sub>ENB</sub> 2	Tone	V <sub>POL</sub>	S2A	S2B	S2C	S2D
≤0.7V			Low	Low	Low	Low
≥1.2V		<9.5V	Low	Low	Low	Low
≥1.2V	Off	≤14V	High	Low	Low	Low
≥1.2V	Off	≥15.5V	Low	High	Low	Low
≥1.2V	On	≤14V	Low	Low	High	Low

Low

Low

Low

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