

UNISONIC TECHNOLOGIES CO., LTD

MC4580

LINEAR INTEGRATED CIRCUIT

DUAL OPERATIONAL AMPLIFIER

DESCRIPTION

The UTC MC4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application.

Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

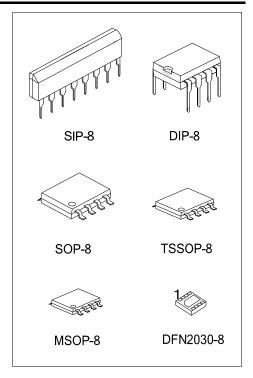
FEATURES

*Operating voltage	(±2V ~ ±18V)
*Low input noise voltage	(0.8µVrms typ.)
*Wide gain bandwidth product	(15MHz typ.)
*Low distortion	(0.0005% typ.)
*Slew rate	(5V/µs typ.)
*Bipolar technology	

ORDERING INFORMATION

Ordering	Number	Dookogo	Decking		
Lead Free	Halogen Free	Package	Packing		
MC4580L-D08-T	MC4580G-D08-T	DIP-8	Tube		
MC4580L-G08-T	MC4580G-G08-T	SIP-8	Tube		
MC4580L-S08-R	MC4580G-S08-R	SOP-8	Tape Reel		
MC4580L-P08-R	MC4580G-P08-R	TSSOP-8	Tape Reel		
MC4580L-SM1-R	MC4580G-SM1-R	MSOP-8	Tape Reel		
MC4580L-K08-2030-R	MC4580G-K08-2030-R	DFN2030-8	Tape Reel		





MARKING

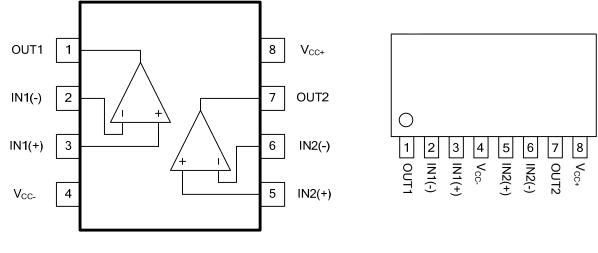
PACKAGE	MARKING
DIP-8	8 7 6 5 Date Code UTC □□□□ L: Lead Free MC4580 C: Halogen Free □□ C: Halogen Free 1 2 3
SIP-8	$\begin{array}{c c} & & & \\ &$
SOP-8	8 7 6 5 UTC □□□□ L: Lead Free MC4580 □ → G: Halogen Free Lot Code 1 2 3
MSOP-8	8 7 6 5 UTC □□□□ MC4580 • □□ • L: Lead Free • G: Halogen F • Lot Code 1 2 3 4
TSSOP-8	Date Code UTC Doc 7 MC4580 C L: Lead Free G G: Halogen Free 4 Lot Code
DFN2030-8	MC 4580 ● □□□□ ● Date Code



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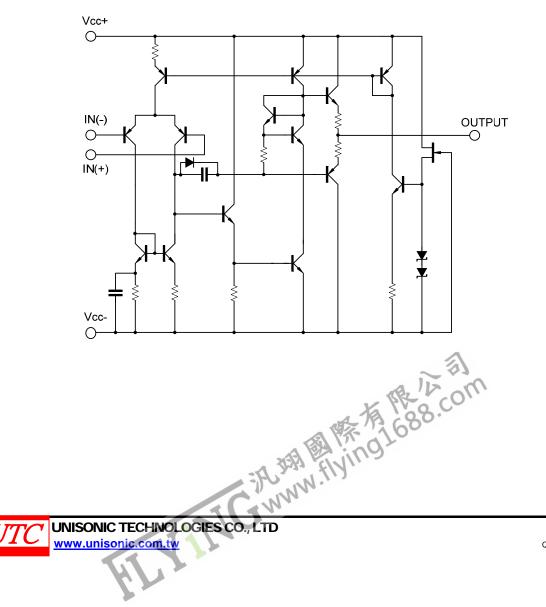
■ PIN CONFIGURATION



DIP-8/SOP-8/TSSOP-8/MSOP-8/DFN2030-8

SIP-8

TEST CIRCUIT



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

PARAMETER		SYMBOL	RATINGS	UNIT	
Supply Voltage		V^+/V^-	±18	V	
Input Voltage		V _{IN}	±15	V	
Differential Input Voltage		V _{I(DIFF)}	±30	V	
Output Current		I _{OUT}	±50	mA	
Power Dissipation	DIP-8 SIP-8	PD	750		
	SOP-8		440		
	TSSOP-8		360	mW	
	MSOP-8		300		
	DFN2030-8		1300		
Junction Temperature		TJ	+125	°C	
Operating Temperature		T _{OPR}	-40 ~ +85	°C	
Storage Temperature		T _{STG}	-40 ~ +125	°C	

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (V+ /V-=±15V, T_A=25°C)

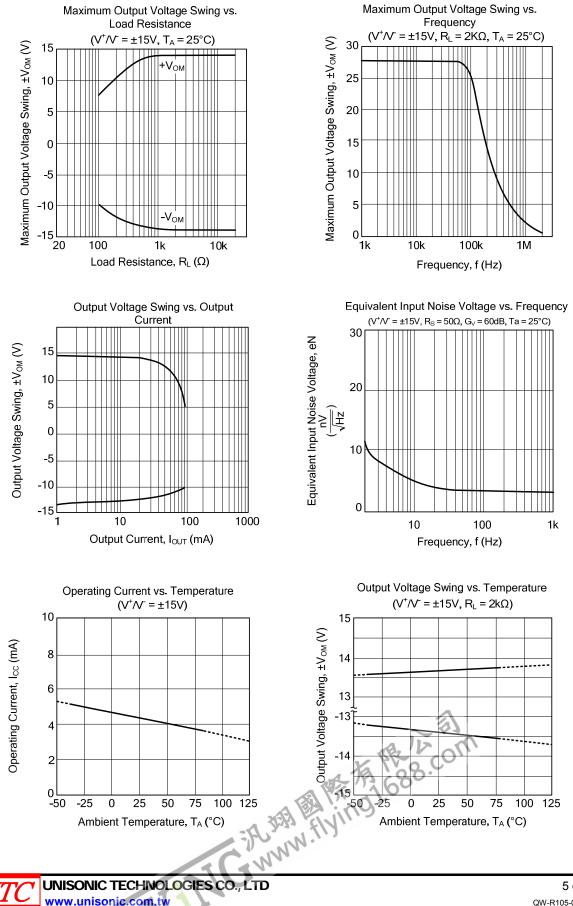
PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{I(OFF)}	$R_{S} \leq 10 k \Omega$		0.5	3	mV
Input Offset Current	I _{I(OFF)}			5	200	nA
Input Bias Current	I _{I(BIAS)}			100	500	nA
Large Signal Voltage Gain	Gv	V_{OUT} =±10V, $R_L \ge 2k\Omega$	90	110		dB
Output Voltage Swing	V _{OM}	$R_L \ge 2k\Omega$	±12	±13.5		V
Input Common Mode Voltage	V _{I(CM)}		±12	±13.5		V
Common Mode Rejection Ratio	CMRR	$R_{S} \leq 10 k \Omega$	80	110		dB
Supply Voltage Rejection Ratio	SVR	Rs≦10kΩ	80	110		dB
Operating Current	Icc			6	9	mA
Slew Rate	SR	$R_L \ge 2k\Omega$		5		V/µs
Gain bandwidth Product	GB	f=10KHz		15		MHz
Total Harmonic Distortion	THD	Gv=20dB,V _{OUT} =5V,R _L =2kΩ, f=1KHz		0.0005		%
Input Noise Voltage	eN	RIAA Rs=2.2 kΩ, 30kHzLPF		0.8		μVrms

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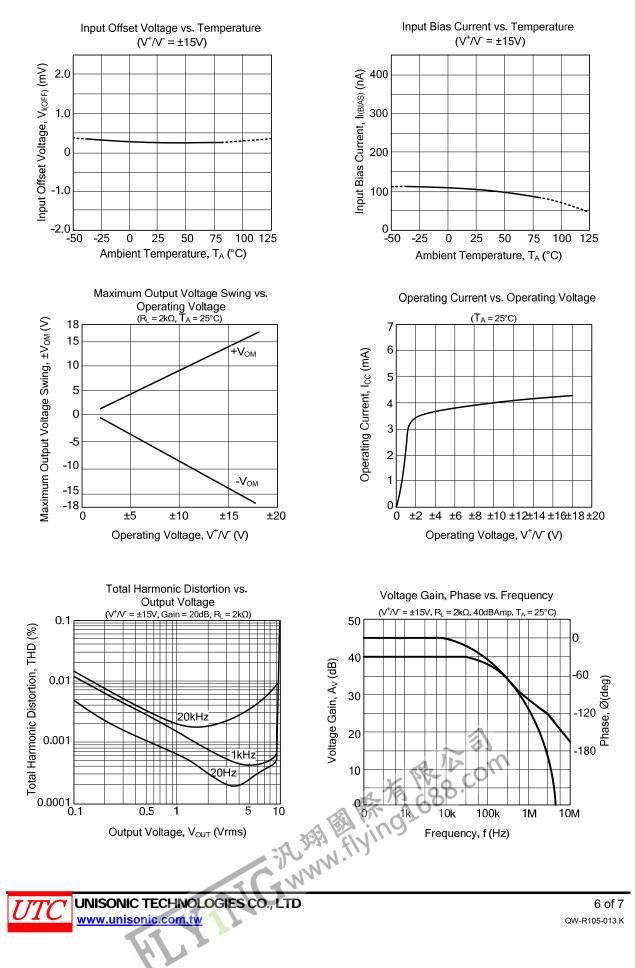
TYPICAL CHARACTERISTICS



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■ TYPICAL CHARACTERISTICS(Cont.)



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