

UNISONIC TECHNOLOGIES CO., LTD

MMDT5551 DUAL TRANSISTOR

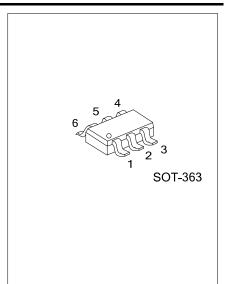
HIGH VOLTAGE SWITCHING **TRANSISTOR**

DESCRIPTION

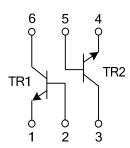
The UTC MMDT5551 is a high voltage fast-switching dual NPN transistor. It is characterized with high breakdown voltage, high current gain and high switching speed.

FEATURES

- * High Collector-Emitter Voltage: V_{CEO}=160V
- * High current gain

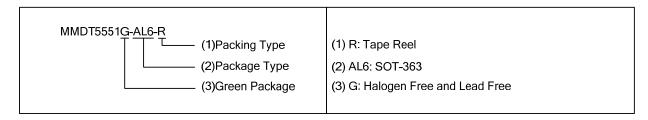


EQUIVALENT CIRCUIT

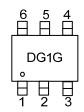


ORDERING INFORMATION

| Ordering Number | Package | Pin Assignment | | | | | Dealing | |
|-----------------|---------|----------------|----|----|----|----|---------|-----------|
| | | 1 | 2 | 3 | 4 | 5 | 6 | Packing |
| MMDT5551G-AL6-R | SOT-363 | E1 | B1 | C2 | E2 | B2 | C1 | Tape Reel |



MARKING



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■ ABSOLUATE MAXIUM RATINGS (T_A= 25°C, unless otherwise specified)

| PARAMETER | SYMBOL | RATINGS | UNIT |
|----------------------------|------------------|------------|------|
| Collector -Base Voltage | V_{CBO} | 180 | V |
| Collector -Emitter Voltage | $V_{\sf CEO}$ | 160 | V |
| Emitter -Base Voltage | V_{EBO} | 6 | V |
| DC Collector Current | Ic | 600 | mA |
| Power Dissipation | P_D | 200 | mW |
| Junction Temperature | TJ | +150 | °C |
| Storage Temperature | T _{STG} | -40 ~ +150 | °C |

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | | TYP | MAX | UNIT | |
|--------------------------------------|----------------------|--|-----|-----|------|------|--|
| Collector-Base Breakdown Voltage | V_{CBO} | I _C =100μA, I _E =0 | 180 | | | V | |
| Collector-Emitter Breakdown Voltage | V_{CEO} | I _C =1mA, I _B =0 | 160 | | | V | |
| Emitter-Base Breakdown Voltage | V_{EBO} | I _E =10μA, I _C =0 | 6 | | | V | |
| Collector Cut-off Current | I _{CBO} | V _{CB} =120V, I _E =0 | | | 50 | nA | |
| Emitter Cut-off Current | I _{EBO} | V_{BE} =4V, I_C =0 | | | 50 | nA | |
| DC Current Gain(note) | h _{FE} | V_{CE} =5V, I_C =1mA | 80 | | | | |
| | | V_{CE} =5V, I_C =10mA | 80 | 160 | 400 | | |
| | | V_{CE} =5V, I_C =50mA | 80 | | | | |
| Collector-Emitter Saturation Voltage | V _{CE(SAT)} | I _C =10mA, I _B =1mA | | | 0.15 | V | |
| | | I _C =50mA, I _B =5mA | | | 0.2 | | |
| Base-Emitter Saturation Voltage | $V_{BE(SAT)}$ | I _C =10mA, I _B =1mA | | | 1 | | |
| | | I _C =50mA, I _B =5mA | | | 1 | V | |
| Current Gain Bandwidth Product | f _T | V _{CE} =10V, I _C =10mA, f=100MHz | 100 | | 300 | MHz | |
| Output Capacitance | C _{ob} | V _{CB} =10V, I _E =0, f=1MHz | | | 6.0 | pF | |
| Noise Figure | NI- | I _C =0.25mA, V _{CE} =5V | | | | 40 | |
| | | R _S =1kΩ, f=10Hz ~ 15.7kHz | | | 8 | dB | |

Note: Pulse test: $P_W < 300\mu s$, Duty Cycle < 2%



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