



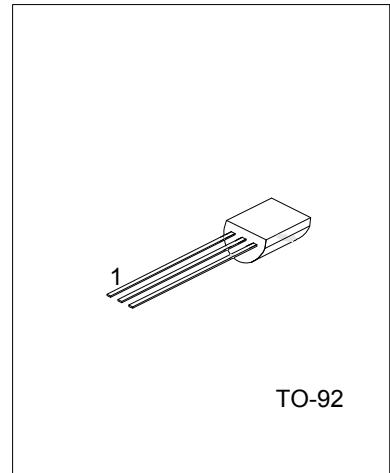
## MPSA92M

## PNP EPITAXIAL SILICON TRANSISTOR

### HIGH VOLTAGE TRANSISTOR

#### FEATURES

- \* Collector-Emitter voltage:  $V_{CE0} = -300V$
- \* Collector Dissipation:  $P_{C(MAX)} = 625mW$
- \* Low collector-Emitter saturation voltage



#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
MPSA92ML-T92-B	MPSA92MG-T92-B	TO-92	E	B	C	Tape Box
MPSA92ML-T92-K	MPSA92MG-T92-K	TO-92	E	B	C	Bulk
MPSA92ML-T92-R	MPSA92MG-T92-R	TO-92	E	B	C	Tape Reel

Note: Pin Assignment: E:EMITTER B:BASE C:COLLECTOR

<p>MPSA92ML-T92-B</p> <p>(1)Packing Type (2)Package Type (3)Lead Free</p>	<p>(1) B: Tape Box, K: Bulk, R: Tape Reel (2) T92: TO-92 (3) Halogen Free, L: Lead Free</p>
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■ ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V <sub>CB0</sub>	-300	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-300	V
Emitter-Base Voltage	V <sub>EBO</sub>	-6	V
Collector Dissipation (T <sub>A</sub> =25°C)	P <sub>C</sub>	625	mW
Collector Current	I <sub>C</sub>	-800	mA
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV <sub>CB0</sub>	I <sub>C</sub> =-100μA, I <sub>E</sub> =0	-300			V
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =-1mA, I <sub>B</sub> =0	-300			V
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> =-100μA, V <sub>BE</sub> =0	-300			V
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-100μA, I <sub>C</sub> =0	-5			V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-300V, I <sub>E</sub> =0			-100	nA
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CB</sub> =-300V, V <sub>BE</sub> =0			-1	μA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> =-4V, I <sub>C</sub> =0			100	nA
DC Current Gain (Note 1)	h <sub>FE</sub>	V <sub>CE</sub> =-10V, I <sub>C</sub> =-1mA	60			
		V <sub>CE</sub> =-10V, I <sub>C</sub> =-10mA	80		300	
		V <sub>CE</sub> =-10V, I <sub>C</sub> =-100mA	80			
		V <sub>CE</sub> =-10V, I <sub>C</sub> =-200mA	40			
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-30mA, I <sub>B</sub> =-1mA			-0.20	V
		I <sub>C</sub> =-100mA, I <sub>B</sub> =-10mA			-0.7	
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> =-10mA, I <sub>B</sub> =-1mA			-0.75	V
Output Capacitance	C <sub>OB</sub>	V <sub>CB</sub> =-20V, I <sub>E</sub> =0, f=1MHz			8	pF

Note: 1. Pulse test: PW<300μs, Duty Cycle<2%

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