## UNISONIC TECHNOLOGIES CO., LTD

### P1595

### LINEAR INTEGRATED CIRCUIT

# 200KHZ, 1.5A PWM BUCK DC/DC CONVERTER

### ■ DESCRIPTION

The UTC **P1595** is a monolithic integrated circuit intended for the use of a step-down DC/DC converter and it can drive a 1.5A load without additional transistors.

The internal compensation makes feedback control having good line and load regulation without external design. The external shutdown function can be controlled by logic level and then enter the standby mode.

The UTC **P1595** features a guaranteed +3% tolerance on output voltage under specified input voltage and output load conditions. Thermal shutdown protection is designed to prevent over temperature operating from damage while current limit protection is against over current operating of the output switch. The switching frequency will be reduced if current limit function occurs and feedback voltage ( $V_{FB}$ ) drops below 0.5V. The UTC **P1595** works at a 200kHz switching frequency so it allows smaller sized filter components.

# SOT-89-5

### **■ FEATURES**

- \* Output Voltage In Adjustable Output Version.
- \* Adjustable Version Output Voltage Range: 0.75V ~ 22V With+3% Accuracy.
- \* Short Circuit Protect
- \* Fixed Switching Frequency: 200KHZ.
- \* On/Off Shutdown Control Input.
- \* Voltage Mode Non-Synchronous Pulse Width Modulation (PWM) Control.
- \* Thermal-Shutdown And Current-Limit Protection.
- \* Operating Voltage Can Be Up To 24V.
- \* 1.5A Output Load Current.
- \* Low Power Standby Mode.
- \* Built-In Switching Transistor

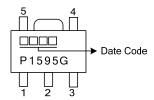
### ■ ORDERING INFORMATION

Ordering Number	Package	Packing
P1595G-AB5-R	SOT-89-5	Tape Reel

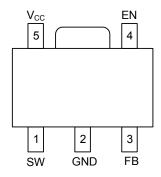


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### ■ MARKING



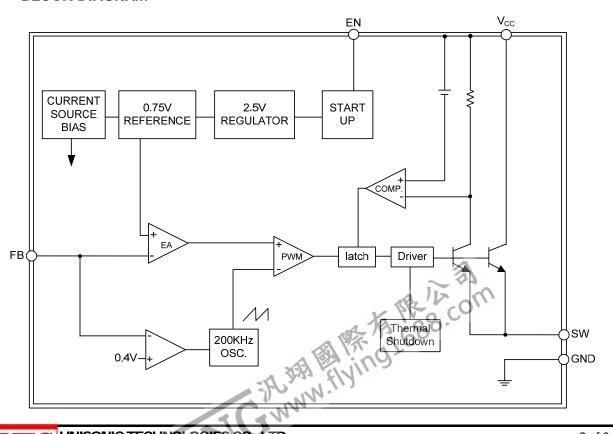
### **■ PIN CONFIGURATION**



### **■ PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION
1	SW	Switching Output
2	GND	Ground
3	FB	Feedback Voltage
4	EN	On/Off Pin
5	Vcc	Operating Voltage Input

### ■ BLOCK DIAGRAM



### ■ ABSOLUTE MAXIMUM RATING (unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		$V_{CC}$	+26	V
ON/OFF Pin Input Voltage		$V_{EN}$	-0.3 ~ V <sub>CC</sub>	V
Feedback Pin Voltage		$V_{FB}$	-0.3 ~ 12	V
Operating Supply Voltage		$V_{OP}$	4.5~24	V
Output Voltage to Ground		$V_{OUT}$	-0.8	V
Power Dissipation (Note 1)	T <sub>A</sub> =25°C	$P_{D}$	690	mW
Operating Temperature		$T_OPR$	-20 ~ +125	°C
Storage Temperature	·	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Note 1)	$\theta_{JA}$	180	°C/W
Junction to Case (Note 1)	$\theta_{JC}$	38	°C/W

Note: 1. It's Measured with the PCB copper area (need connect to V<sub>SS</sub> pins) of approximately 1.5 in<sup>2</sup>(Multi-layer)

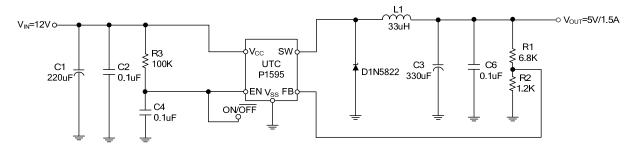
### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, V<sub>CC</sub>=12V, I<sub>LOAD</sub>=0.2A, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Feedback Voltage		$V_{FB}$	I <sub>OUT</sub> =0.2A	0.728	0.750	0.773	V
Saturation Voltage		$V_{SAT}$	I <sub>OUT</sub> =1.5A, No outside circuit V <sub>FB</sub> =0V force driver on		1.2	1.5	V
Quiescent Current		ΙQ	V <sub>FB</sub> =1.2V force driver off		4	8	mA
Feedback Bias Cur	rent	$I_{FB}$	I <sub>OUT</sub> =0.1A		-10	-50	nA
Shutdown Supply C	Current	$I_{SD}$	V <sub>EN</sub> =0V		2	10	μΑ
Current Limit		I <sub>(LIMIT)</sub>	Pear current, No outside circuit V <sub>FB</sub> =0V force driver on				Α
SW Pin Leakage Current SW pin=0V SW pin=-0.8V		No outside circuit  V <sub>FB</sub> =1.0V force driver off				-50	μΑ
			V <sub>CC</sub> =24V force driver off		-5		mA
Oscillator Frequence	Oscillator Frequency			140	200	260	KHz
Oscillator Frequency Circuit Protect of Short		F <sub>SCP</sub>	(Adjustable) When V <sub>FB</sub> <0.5V		80		KHz
Max. Duty Cycle (ON)		DC V <sub>FB</sub> =1.2V force driver off			0		%
Min. Duty Cycle (OFF)		V <sub>FB</sub> =0V for	V <sub>FB</sub> =0V force driver on		100		%
EN Pin Logic Input Threshold		$V_{IH}$	High (regulator ON)		1.2	2.0	V
Voltage		$V_{IL}$	Low (regulator OFF)	0.5	1.2		V
EN pin Logic Input Current		I <sub>H</sub>	V <sub>EN</sub> =2.5V (ON)		20		μΑ
EN pin Input Current		Ι <sub>L</sub>	V <sub>EN</sub> =0.3V (OFF)		-5		μA
Thermal Shutdown Temp		TSD			135		°C



### TYPICAL APPLICATION CIRCUIT

### **Adjustable Output Voltage Version**



$$V_{OUT}$$
= $V_{FB}$ × $\left(1+\frac{R1}{R2}\right)$ ,  $V_{FB}$ =0.75V. R2=0.75K~4K

Resistor select for output voltage setting

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V <sub>OUT</sub>	R2	R1			
5V	1.2K	6.8K			
3.3V	2K	6.8K			
2.5V	2K	4.7K			
1.8V	2K	3K			
1.5V	2K	2K			
1.3V	2K	1.5K			
1.2V	2K	1.2K			

L1 recommend value (V<sub>IN</sub>=12V, I<sub>OUT</sub>=1.5A,)

V <sub>OUT</sub>	1.8 V	2.5V	3.3V	5V
L1 Value	22µH	22µH	33µH	33µH

### **FUNCTION DESCRIPTIONS**

### **Pin Functions**

 $V_{CC}$ 

It provides the IC switching regulator with positive input supply. This pin is used to supply the switching currents needed by the regulator and a suitable input bypass capacitor must be connected at this pin.

### GND

Circuit ground.

### SW

Internal switch. The voltage at this pin switches between (+V<sub>CC</sub>-V<sub>SAT</sub>) and approximately-0.5V, with a duty cycle of approximately V<sub>OUT</sub> / V<sub>CC</sub>. To minimize coupling to sensitive circuitry, you should minimize the PC board copper area connected to this pin.

### **Feedback**

Senses the regulated output voltage to complete the feedback loop.

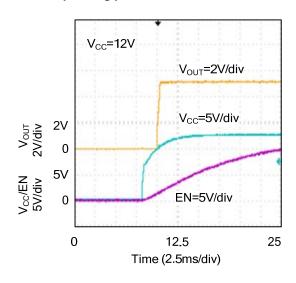
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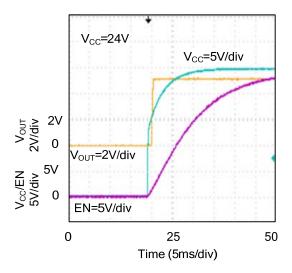
Using logic level signals to allow the switching regulator circuit to be shutdown, which dropping the total input e 2.0V (up t supply current to approximately 10uA. Shut the regulator down through pulling this pin below a threshold voltage of approximately 0.5V, turn the regulator on through pulling this pin above 2.0V (up to Vcc MAX).



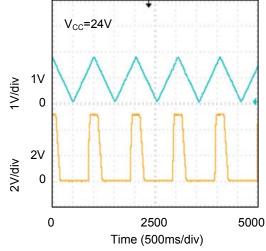
### TYPICAL CHARACTERISTICS

### 1. Startup during power on

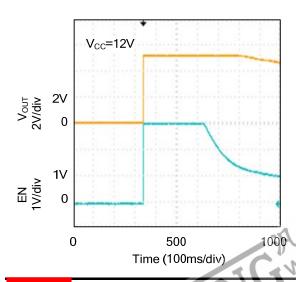


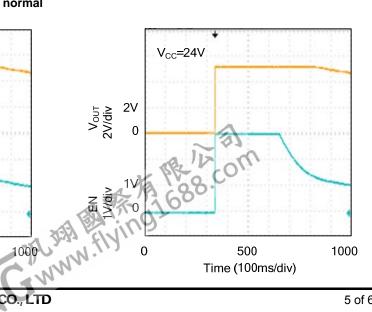


### 2. Low-frequency triangle wave control enable pin



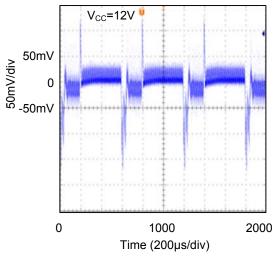
### 3. Control enable pin with power supply to normal

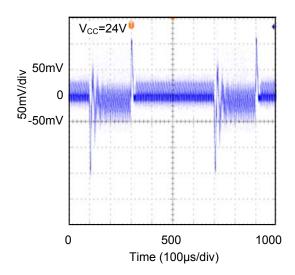




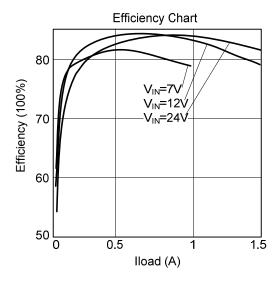
### ■ TYPICAL CHARACTERISTICS(Cont.)

### 4. Transient response with step load





### 5. Efficiency



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