

# UNISONIC TECHNOLOGIES CO., LTD

P4596

**Preliminary** 

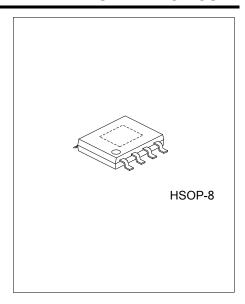
# LINEAR INTEGRATED CIRCUIT

# **PWM CONTROL 3A** STEP-DOWN CONVERTER

#### **DESCRIPTION**

The UTC P4596 consists of 3A step-down switching regulator with PWM control which includes a reference voltage source. oscillation circuit, error amplifier, internal PMOS and etc.

The UTC P4596 can provide low-ripple power, high efficiency, and excellent transient characteristics and an enable function, an over current protect function and a short circuit protect function are built inside, And the PWM control circuit can vary the duty ratio linearly from 100 down to 0%. This converter also includes an error amplifier circuit as well as a soft-start circuit that prevents overshoot at startup. These ICs can work as step-down switching regulators with the addition of an internal P-channel Power MOS, a coil and a diode connected externally. They provide such outstanding features: low current consumption. It is also suitable for the operation via an AC adapter because this converter can accommodate an input voltage up to 40V.



#### **FEATURES**

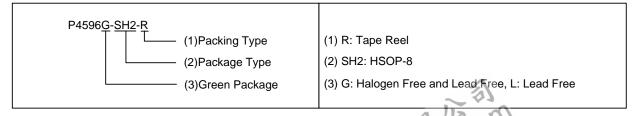
\* Input voltage: 8V~40V

\* Duty ratio: 0%~100% PWM control

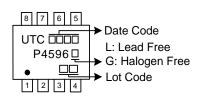
- \* Enable with Soft-Start function
- \* Oscillation frequency can be set by outside resistance
- \* Current Limit, SCP and OTP

### **ORDERING INFORMATION**

Orderin	g Number	Deelsess	Packing	
Lead Free	Halogen Free	Package		
P4596L-SH2-R	P4596G-SH2-R	HSOP-8	Tape Reel	

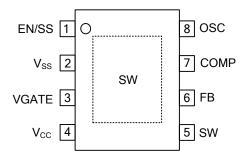


#### **MARKING**



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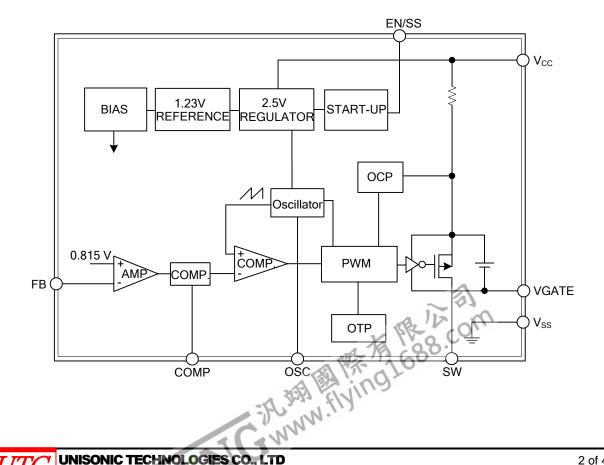
# **PIN CONFIGURATION**



# **PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION
1	EN/SS	Enable and Soft-start pin
2	$V_{SS}$	Ground
3	VGATE	Driver GATE clamping pin.
4	Vcc	IC power supply pin
5	SW	Switch pin.
6	FB	Feedback voltage
7	COMP	Compensation pin
8	osc	Frequency Set Pin.

# **BLOCK DIAGRAM**



#### ■ **ABSOLUTE MAXIMUM RATING** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
VCC Pin Voltage	V <sub>cc</sub>	$V_{SS}$ -0.3 ~ $V_{SS}$ +40	V
Feedback Pin Voltage	$V_{FB}$	V <sub>SS</sub> -0.3 ~ 6	V
EN/SS Pin Voltage	V <sub>EN/SS</sub>	V <sub>SS</sub> -0.3 ~ 6	V
OSC Pin Voltage	V <sub>osc</sub>	V <sub>SS</sub> -0.3 ~ 3	V
COMP Pin Voltage	$V_{COMP}$	V <sub>SS</sub> -0.3 ~ 6	V
VGATE Pin Voltage	$V_{GATE}$	$V_{SS}$ -0.3 ~ $V_{CC}$	V
Switch Pin Voltage	V <sub>SW</sub>	$V_{SS}$ - 0.3 ~ $V_{CC}$ + 0.3	V
Power Dissipation	P <sub>D</sub>	0.7	W
Operating Supply Voltage	$V_{OP}$	8 ~ 40	V
Junction Temperature	TJ	-40 ~ +125	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ <b>+</b> 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	143	°C/W
Junction to Case	$\theta_{JC}$	45	°C/W

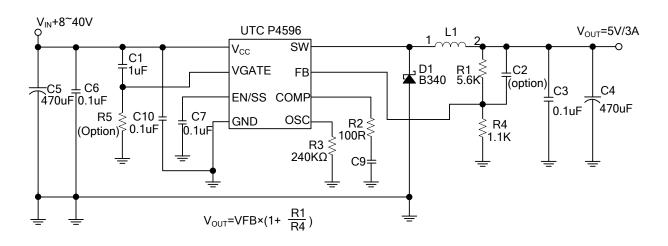
Note: Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board.

# ■ ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=12V, T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Feedback Voltage	$V_{FB}$	V <sub>CC</sub> =10V~30V, I <sub>OUT</sub> =0~2A T <sub>J</sub> =-20°C~125°C	0.790	0.815	0.840	V
Quiescent Current	I <sub>CCQ</sub>	V <sub>FB</sub> =1V		3	6	mA
Feedback Bias Current	$I_{FB}$			0.1		μΑ
Shutdown Supply Current	$I_{SD}$	V <sub>EN/SS</sub> =0V	10	56	300	μΑ
Current Limit	$I_{CL}$		3.5			Α
Adjustable Frequency Range	Fosc	R3=240ΚΩ		200		KHz
Short Frequency	F <sub>osc1</sub>	V <sub>CC</sub> =10V~30V		50		KHz
EN/SS Pin Shutdown Logic Input Threshold Voltage	V <sub>ENL</sub>				0.8	V
EN/SS Pull High Current	I <sub>EN/SS</sub>	V <sub>EN/SS</sub> =0V		8		μΑ
Internal MOSFET R <sub>DSON</sub>	R <sub>DSON</sub>	V <sub>CC</sub> =12V, V <sub>FB</sub> =0V		80	180	mΩ



#### **■ TYPICAL APPLICATION CIRCUIT**



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