PA4871 CMOS IC

# 1.1W AUDIO POWER AMPLIFIER WITH SHUTDOWN MODE

# **DESCRIPTION**

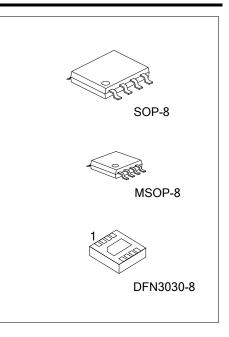
As a mono bridged power amplifier which is operating on a single 5V supply, the UTC PA4871 is capable of delivering 1.1W of output power per channel into 8Ω loads with less than 0.5% THD+N.

The UTC PA4871 is optimally suited for low-power portable applications because of the it do not require output coupling capacitors, bootstrap capacitors or snubber networks.

By using external gain-setting resistors, the closed loop response of the unity-gain stable PA4871 can be configured.

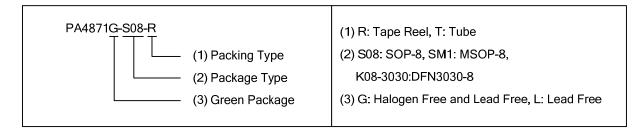
#### **FEATURES**

- \* Output power at 0.5% THD+N Supply voltage:5V Delivering 1.1W into a 8Ω load
- \* With shutdown mode
- \* Stable unity-gain.



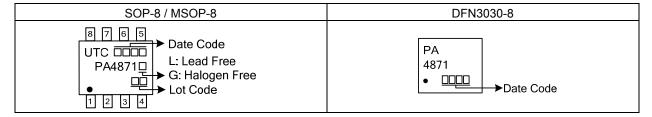
## ORDERING INFORMATION

Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
PA4871L-S08-R	PA4871G-S08-R	SOP-8	Tape Reel	
PA4871L-SM1-R	PA4871G-SM1-R	MSOP-8	Tape Reel	
PA4871L-K08-3030-R	PA4871G-K08-3030-R	DFN3030-8	Tape Reel	

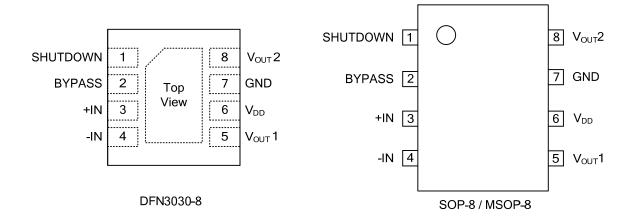


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## ■ MARKING



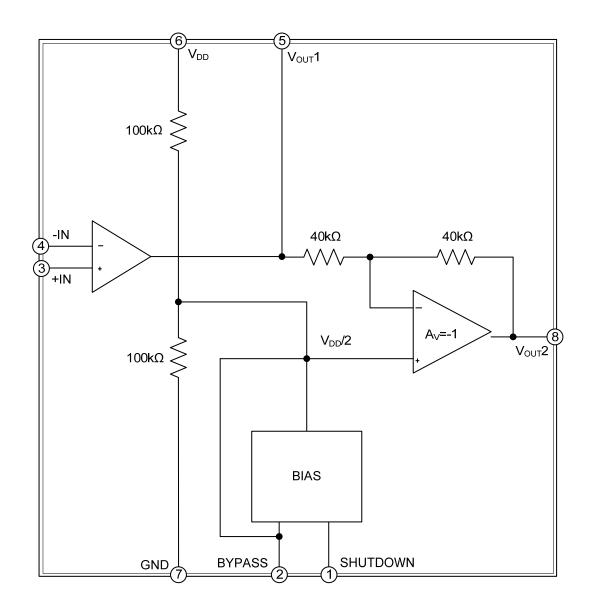
# **■ PIN CONFIGURATION**



## PIN DESCRIPTION

PIN NO	PIN NAME	DESCRIPTION
1	SHUTDOWN	Shutdown control input pin.
2	BYPASS	Connected to a bypass capacitor.
3	+IN	+ pin of input signal.
4	-IN	- pin of input signal.
5	V <sub>OUT1</sub>	Output pin1
6	$V_{DD}$	Supply voltage
7	GND	GND
8	$V_{OUT2}$	Output pin2

## **BLOCK DIAGRAM**



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## ■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{DD}$	6	V
Input Voltage	$V_{IN}$	-0.3 ~ V <sub>DD</sub> +0.3	V
Power Dissipation	$P_{D}$	Internally Limited	W
Junction Temperature	$T_J$	150	°C
Operating Temperature	$T_{OPR}$	-40 ~ +85	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
	SOP-8		140	°C/W
Junction to Ambient	MSOP-8	$\theta_{JA}$	210	°C/W
	DFN3030-8		59	°C/W
	SOP-8		35	°C/W
Junction to Case	MSOP-8	$\theta_{JC}$	56	°C/W
	DFN3030-8		4.3 (Note)	°C/W

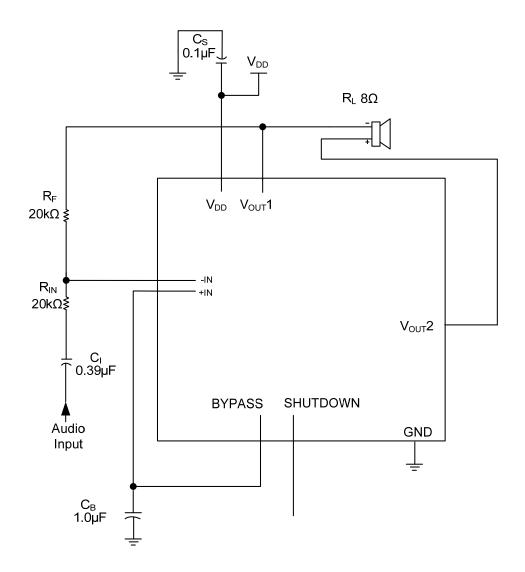
Note: Surface mounted on 1 in <sup>2</sup> copper pad of FR4 board

# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, V<sub>DD</sub>=5V, R<sub>L</sub>=8Ω, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
DC ELECTRICAL CHARACTERISTICS							
Supply Voltage		$V_{DD}$		2.0	5	5.5	V
DC Differential Output Voltage		V <sub>OUT(DIFF)</sub>	V <sub>IN</sub> =0V		5	50	mV
Supply Current	Mute Mode	- I <sub>DD</sub>	V <sub>IN</sub> =0V,I <sub>OUT</sub> =0A		6.5	10.0	mA
	Shutdown Mode		$V_{PIN1}=V_{DD}$		0.6	2	μΑ
Output Power		P <sub>OUT</sub>	THD=0.5%, f <sub>IN</sub> =1kHz	1.0	1.10		W
			THD=10%, f <sub>IN</sub> =1kHz		1.5		W
Total Harmonic Distortion+Noise		THD+N	P <sub>OUT</sub> =1W <sub>RMS</sub> , 20Hz <f<sub>IN&lt;20kHz, G=2V/V</f<sub>		0.25		%
Power Supply Ripple Rejection		PSRR	V <sub>DD</sub> =4.9V to 5.1V		65		dB

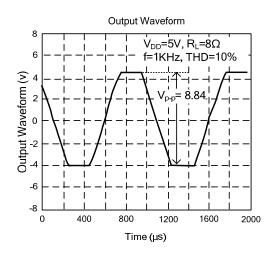


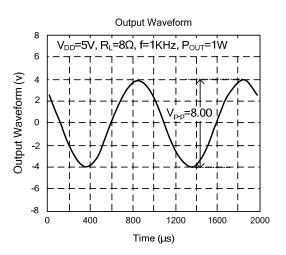
# **TYPICAL APPLICATION CIRCUIT**

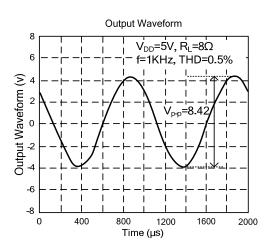


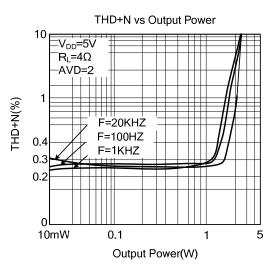
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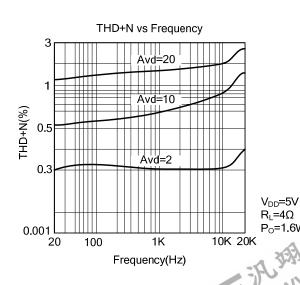
## TYPICAL CHARACTERISTICS

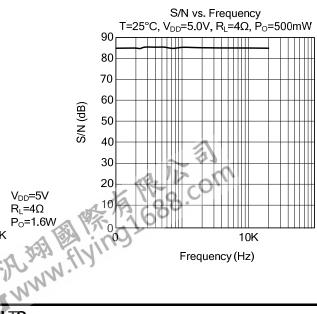




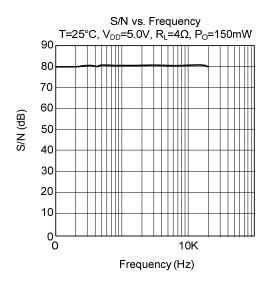








# ■ TYPICAL CHARACTERISTICS (Cont.)



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