

UNISONIC TECHNOLOGIES CO., LTD

UBCR304 Preliminary TRIACS

4A TRIAC

■ DESCRIPTION

The UTC **UBCR304** is a 4A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

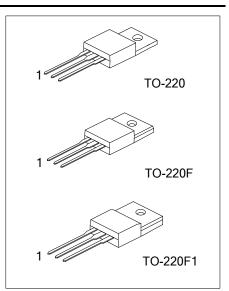
The UTC **UBCR304** is suitable for use in electric pot, electric rice cooker and controller for other heater.

■ FEATURES

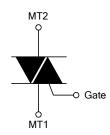
* I_{T(RMS)}: 4A

* V_{DRM}: 800V (T_J=125°C)

* I_{GT I-II-III}: 30mA



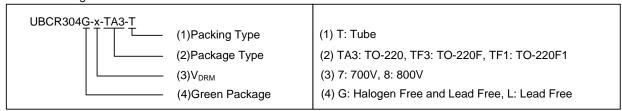
■ SYMBOL



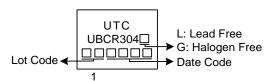
ORDERING INFORMATION

Ordering Number		Doolsons	Pin Assignment			Da alda a	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UBCR304L-x-TA3-T	UBCR304G-x-TA3-T	TO-220	MT1	MT2	G	Tube	
UBCR304L-x-TF1-T	UBCR304G-x-TF1-T	TO-220F1	MT1	MT2	G	Tube	
UBCR304L-x-TF3-T	UBCR304G-x-TF3-T	TO-220F	MT1	MT2	G	Tube	

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Repetitive Peak Off-State Voltage (Note 1)	V_{DRM} / V_{RRM}	800	V
On-State RMS Current (Commercial Frequency, Sine Full Wave 360° Conduction)	I _{T(RMS)}	4	Α
Surge On-State Current (60Hz Sinewave 1 Full Cycle, Peak Value, Non-Repetitive)	I _{TSM}	30	А
I ² t for Fusing (Value Corresponding to 1 Cycle of Half Wave 60Hz, Surge On-State Current)	l ² t	3.7	A ² s
Peak Gate Current	I _{GM}	2	Α
Peak Gate Power Dissipation	P _{GM}	3	W
Average Gate Power Dissipation	$P_{G(AV)}$	0.3	W
Peak Gate Voltage	V_{GM}	10	V
Operating Junction Temperature	T_J	-40 ~ +125	°C
Storage Junction Temperature	T _{STG}	-40 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
handing to Occa	TO-220	3.9		°C/W
Junction to Case	TO-220F/TO-220F1	AlC	5.2	°C/W

■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
		T 0500 V 0V	I			35	mA
Gate Trigger Current (Note 2)	I_{GT}	T_J =25°C, V_D =6V, R_L =6 Ω , R_G =330 Ω	II			35	mΑ
			III			35	mA
Gate Trigger Voltage (Note 2)		T_J =25°C, V_D =6V, R_L =6 Ω , R_G =330 Ω	Į			1.5	V
	V_{GT}		II			1.5	V
			III			1.5	V
Gate Non-Trigger Voltage	$V_{\sf GD}$	T _J =100°C, V _D =1/2 V _{DRM}		0.1			V
Holding Current (Note 2)	lμ	I _T =100mA			5		mA
Latching Current			Į		8		mA
	IL	I _G =36mA	II	II 17		mA	
			II		30		mA
Critical Rate of Rise of Off-State commutation Voltage (Note 3)	(dv/dt)c	T _J =125°C		2.5			V/µs

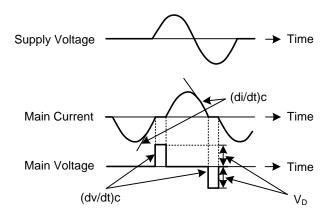
■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
On-State Voltage	V_{TM}	T _J =25°C, I _{TM} =5.5A, Instantaneous Measurement			1.5	V
Repetitive Peak Off-State Current	I_{DRM}	T _J =150°C, V _{DRM} Applied			2.0	mA

Notes: 1. Gate open.

- 2. Measurement using the gate trigger characteristics measurement circuit.
- 3. Test conditions of the critical-rate of rise of off-state commutation voltage is shown in the table below.

■ COMMUTATING VOLTAGE AND CURRENT WAVEFORMS (INDUCTIVE LOAD)

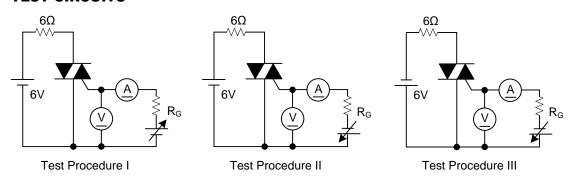


Note: Test Conditions: 1. Junction temperature: T_J=125°C

2. Rate of decay of on-state commutating current: (di/dt)c=-1.5A/ms

3. Peak off-state voltage: V_D=400V

■ TEST CIRCUITS



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.