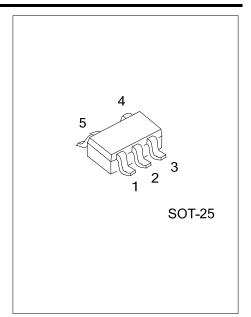
UD05122 **Preliminary CMOS IC**

1.4MHz, 1.2A, V_{FR} =0.6V SYNCHRONOUS STEP-DOWN CONVERTER

DESCRIPTION

UTC UD05122 is a 1.4MHz constant frequency current mode PWM step-down converter. It is ideal for portable equipment requiring very high current up to 1.2A from single-cell Lithium-ion batteries while still achieving over 90% efficiency during peak load conditions. The 2.5V to 5.5V input voltage range makes UTC UD05122 ideally suited for single Li-lon, two to four AA battery-powered applications. 100% duty cycle provides low dropout operation, extending battery life in portable systems. Pulse skipping mode operation provides very low output ripple voltage for noise sensitive applications. Switching frequency is internally set at 1.4MHz, allowing the use of small surface mount inductors and capacitors. The internal synchronous switch increases efficiency and decreases need of an external Schottky diode. Low output voltages are easily supported with the 0.6V feedback reference voltage.



FEATURES

- * Input Voltage Range :2.5V~5.5V
- * Output Voltage: from 0.6V to VIN
- * High Efficiency: Up to 92%
- * Constant Frequency Operation: 1.4MHz
- * Output Current: 1.2A
- * Quiescent Current: 200uA (input < 4.2V)
- * No Schottky Diode Required
- * 100% Duty Cycle in Dropout
- * 0.6V Reference Allows Low Output Voltages
- * Current Mode Operation for Excellent Line and Load Transient Response
- * Current limit, Enable function
- * Short Circuit Protect (SCP)
- * ≤1µA Shutdown Current

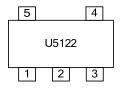


ORDERING INFORMATION

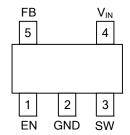
Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
UD05122L-AF5-R	UD05122G-AF5-R	SOT-25	Tape Reel	

UD05122G-AF5-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AF5: SOT-25 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



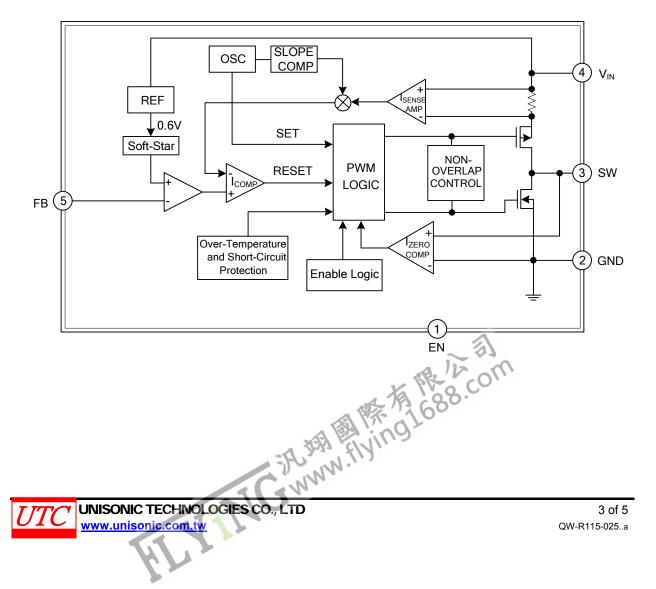
PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
		Enable pin
1	1 EN	H: normal operation
		L: Shutdown
2	GND	Ground Pin
3 SW	CM	Switch output pin. Connect external inductor here. Minimize trace area at this pin to
	reduce EMI.	
4	V_{IN}	Power Supply Input Pin
5	FB	Output Feedback pin

BLOCK DIAGRAM



■ **ABSOLUTE MAXIMUM RATING** (T_A= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
V _{IN} Pin Voltage	V_{IN}	6	V
Feedback Pin Voltage	V_{FB}	V_{CC}	V
EN Pin Voltage	V_{EN}	V _{CC}	V
Switch Pin Voltage	V_{SW}	V _{CC}	V
Peak SW Sink and Source Current	I _{PSW}	1.5	Α
Junction Temperature	T_J	+125	°C
Storage Temperature Range	T _{STG}	-40 ~ +150	°C
Operation Temperature Range	T _{OPR}	-40 ~ +85	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	2 6 0	°C/W	

Note: θ_{JA} is measured with the PCB copper area of approximately 1 in²(Multi-layer). That need connect to GND pin of the UTC **UD05122.**

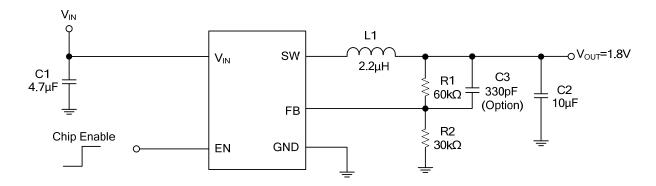
■ ELECTRICAL CHARACTERISTICS (V_{IN}=V_{EN}=3.6V, T_A=25°C, unless otherwise specified) (Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Range	V_{IN}		2.5		5.5	V
Feedback Voltage	V_{FB}	V _{FB} =0.6V	0.588	0.600	0.612	V
Feedback Bias Current	I _{FB}	V _{FB} =0.65V	-30		+30	nA
Quiescent Current	Icc	V _{FB} =1V		200	400	μΑ
Shutdown Supply Current	I _{SD}	V _{EN} =0V		0.1	1	μΑ
Switching Current Limit	I _{LIMIT}	V _{IN} =5V		1.8		Α
Oscillation Frequency	Fosc	SW pin	1.2	1.4	1.6	MHz
R _{DS(ON)} of P-CH MOSFET	R _{DSON}	I _{OUT} =1.0A		0.26	0.33	Ω
R _{DS(ON)} of N-CH MOSFET	R _{DSON}	I _{OUT} =1.0A		0.26	0.33	Ω
Efficiency	EFFI	V_{IN} =5V, V_{OUT} =3.3V, I_{OUT} =0.5A	92			%
EN Din Logic Input Throubold Voltage	V_{ENL}				0.6	V
EN Pin Logic Input Threshold Voltage	V_{ENH}		1.2			V
EN Pin Input Current	I _{EN}			±0.1	±1	μΑ
Soft-Start Interval	T _{SS}			1.2		ms

Note: 100% production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.



■ TYPICAL APPLICATION CIRCUIT



 $V_{OUT} = 0.6 \times (1 + R1/R2)$

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