UF1010-S Preliminary Power MOSFET

# 84A, 60V N-CHANNEL POWER MOSFET

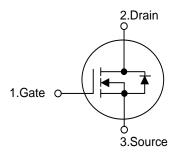
#### DESCRIPTION

The UTC UF1010-S is a N-channel mode power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

## ■ FEATURES

- \*  $R_{DS(ON)} \le 12 \text{ m}\Omega$  @  $V_{GS} = 10V$ ,  $I_{D} = 50A$
- \* Fast Switching Capability
- \* Improved dv/dt Capability, High Ruggedness

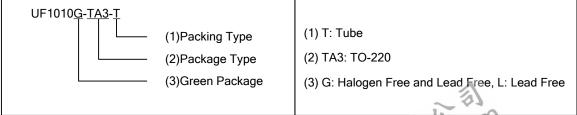
#### **■ SYMBOL**



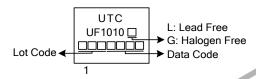
# ORDERING INFORMATION

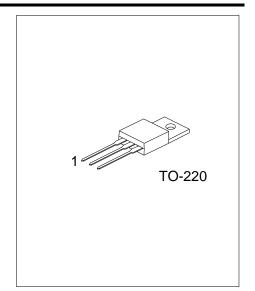
Ordering Number		Daylor	Pin Assignment			Da alda a	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UF1010L-TA3-T	UF1010G-TA3-T	TO-220	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



#### **■ MARKING**





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# ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Drain Current	Continuous	$I_D$	84	Α
	Pulsed (Note 2)	I <sub>DM</sub>	330	Α
Avalanche Energy	valanche Energy Single Pulsed (Note 3)		490	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	11	V/ns
Power Dissipation		$P_{D}$	200	W
Junction Temperature		TJ	+150	°C
Storage Temperature		$T_{STG}$	-55 ~ <b>+</b> 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 0.1 mH,  $I_{AS}$  = 99 A,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25°C
- 4.  $I_{SD} \le 30A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25$ °C

## **■ THERMAL DATA**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62	°C/W
Junction to Case	$\theta_{JC}$	0.75	°C/W

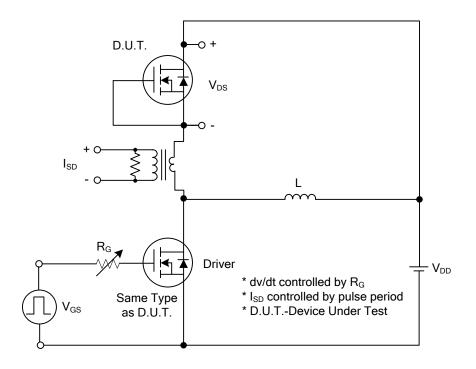
## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	LINIT
OFF CHARACTERISTICS	JINDOL	1 LOT CONDITIONS	1711114	1 1 1 1	IVICA	CIVII	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60			V	
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	- 00		1	μA
Forward Forwar		1000	$V_{GS}=20V, V_{DS}=0V$			100	μ/ τ
Gate-Source Leakage Current	Reverse	$I_{GSS}$	$V_{GS}$ =-20V, $V_{DS}$ =0V			-100	nA
ON CHARACTERISTICS		1.00 = 1.1, 1.50 = 1		1			
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0		3.0	V	
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =50A			12	mΩ
DYNAMIC CHARACTERISTICS				•			
Input Capacitance		C <sub>ISS</sub>			3385		pF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		580		pF
Reverse Transfer Capacitance		C <sub>RSS</sub>			80		pF
SWITCHING CHARACTERISTICS				•			
Total Gate Charge (Note 1)		$Q_G$	V 50V V 40V L 40A		224		nC
Gate to Source Charge		Q <sub>GS</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A,		14.7		nC
Gate to Drain Charge		$Q_{GD}$	I <sub>G</sub> =100μA (Note 1, 2)		21.6		nC
Turn-ON Delay Time (Note 1)		t <sub>D(ON)</sub>			60		ns
Rise Time	, , ,		$V_{DD}=30V, V_{GS}=10V, I_{D}=0.5A,$		105		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω (Note 1, 2)		1180		ns
all-Time		t <sub>F</sub>			345		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous (	Current	Is	a 113 c	C		84	Α
Maximum Body-Diode Pulsed Curre	ent	I <sub>SM</sub>	I BE CO'			330	Α
Drain-Source Diode Forward Voltage (Note 1)		$V_{SD}$	I <sub>S</sub> =30A, V <sub>GS</sub> =0V			1.3	V
Body Diode Reverse Recovery Time (Note 1)		t <sub>rr</sub>	$l_S=30A$ , $V_{GS}=0V$ ,		55		ns
Body Diode Reverse Recovery Charge		Q <sub>rr</sub>	dl <sub>F</sub> /dt=100A/µs		102	,	μC

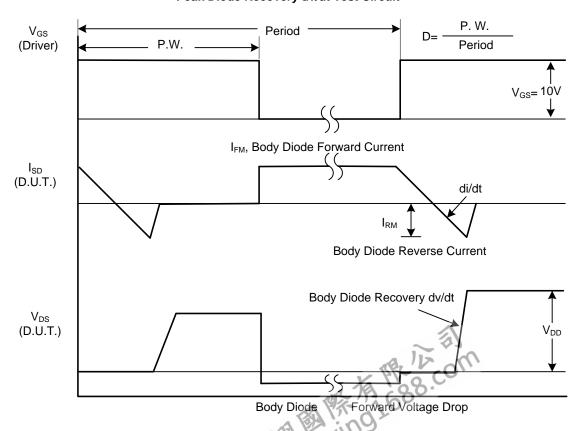
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

## ■ TEST CIRCUITS AND WAVEFORMS

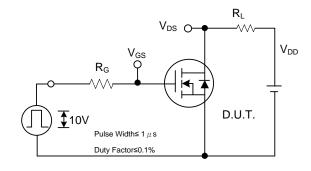


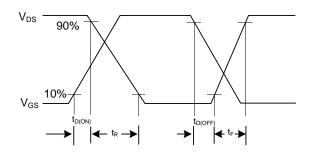
## Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

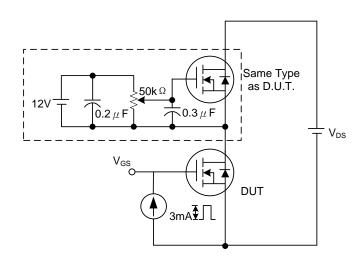
## TEST CIRCUITS AND WAVEFORMS (Cont.)

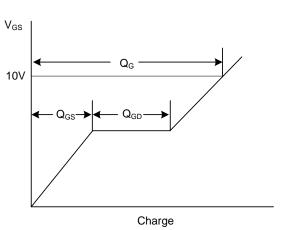




**Switching Test Circuit** 

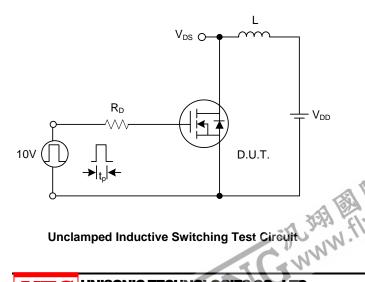
**Switching Waveforms** 

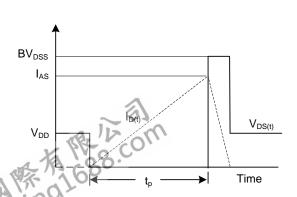




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





Unclamped Inductive Switching Waveforms

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