



# UF90N10

*Power MOSFET*

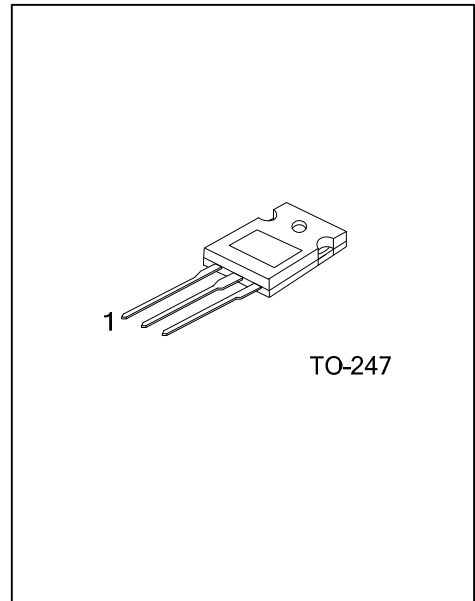
## 90A, 100V N-CHANNEL POWER MOSFET

■ DESCRIPTION

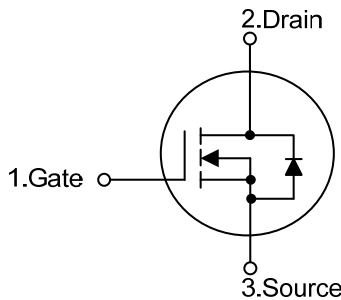
The UTC **UF90N10** provide excellent  $R_{DS(ON)}$ , low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

■ FEATURES

- \*  $R_{DS(ON)} \leq 10m\Omega @ V_{GS}=10V, I_D=45A$
- $R_{DS(ON)} \leq 16m\Omega @ V_{GS}=4.5V, I_D=45A$
- \* Low Reverse Transfer Capacitance
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness



■ SYMBOL



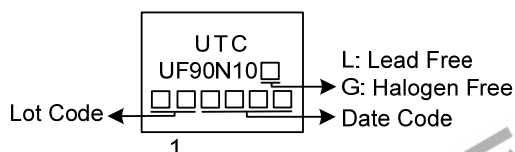
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF90N10L-T47-T	UF90N10G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UF90N10G-T47-T</p>	<p>(1) T: Tube</p> <p>(2) T47: TO-247</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	100	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous	$I_D$	90	A
	Pulsed (Note 2)	$I_{DM}$	180	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	3610	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6.2	V/ns
Power Dissipation		$P_D$	310	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L=10\text{mH}$ ,  $I_{AS}=26.9\text{A}$ ,  $V_{DD}=90\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD}\leq 30\text{A}$ ,  $di/dt\leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	62	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	0.403	$^\circ\text{C}/\text{W}$

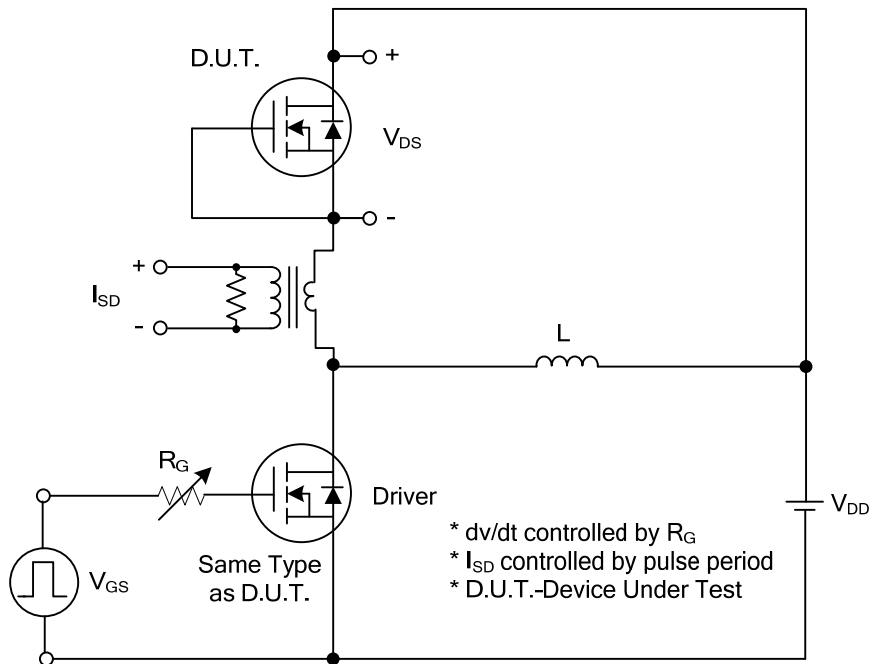
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =45A			10	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =45A			16	mΩ
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		7280		pF
Output Capacitance	C <sub>OSS</sub>			1150		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			140		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	Q <sub>G</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A I <sub>G</sub> =1mA (Note 1, 2)		165		nC
Gate-Source Charge	Q <sub>GS</sub>			25		nC
Gate-Drain Charge	Q <sub>GD</sub>			40		nC
Turn-On Delay Time (Note 1)	t <sub>D(ON)</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =90A, R <sub>G</sub> =25Ω (Note 1, 2)		72		ns
Turn-On Rise Time	t <sub>R</sub>			55		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			584		ns
Turn-Off Fall Time	t <sub>F</sub>			100		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>				90	A
Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>				180	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =90A, V <sub>GS</sub> =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =30A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs		278		nS
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>				1.3	

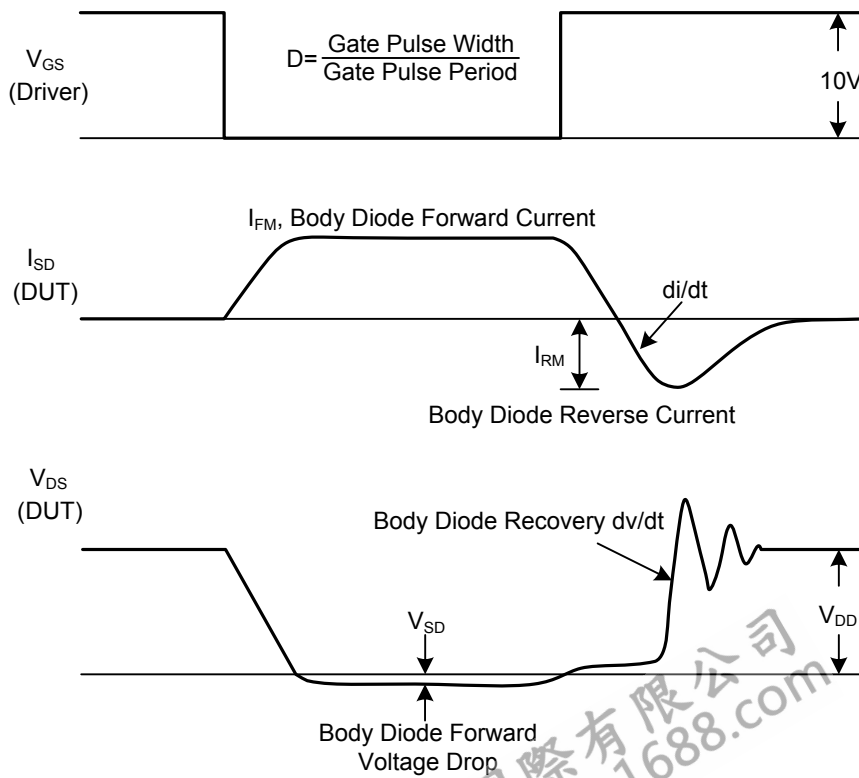
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

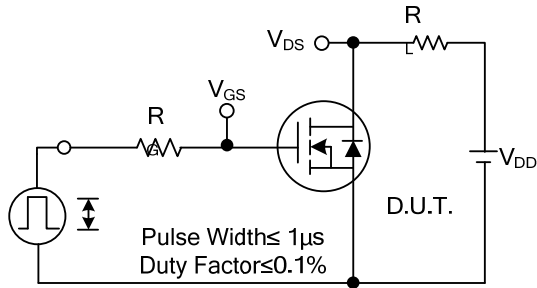


Peak Diode Recovery dv/dt Test Circuit

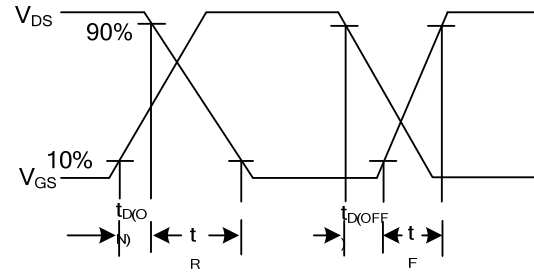


Peak Diode Recovery dv/dt Waveforms

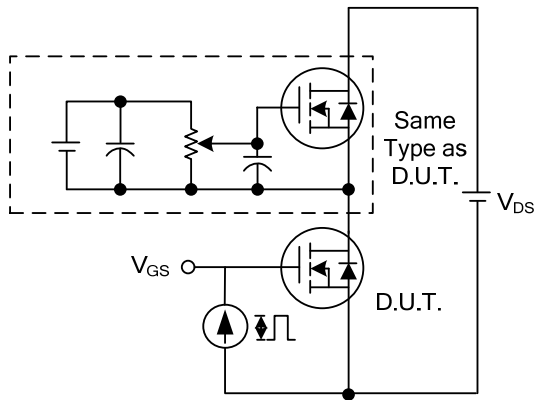
### TEST CIRCUITS AND WAVEFORMS



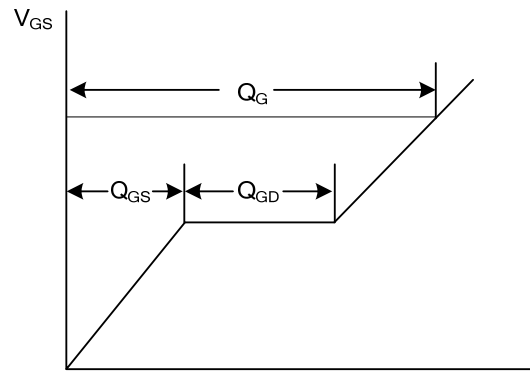
Switching Test Circuit



Switching Waveforms

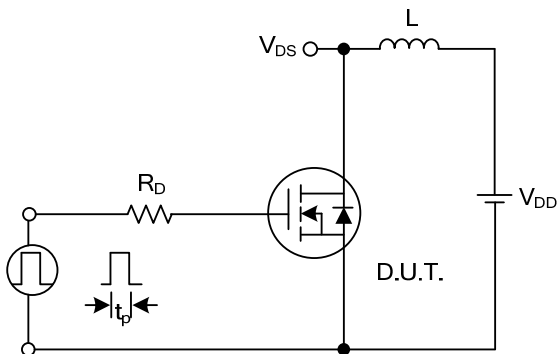


Gate Charge Test Circuit

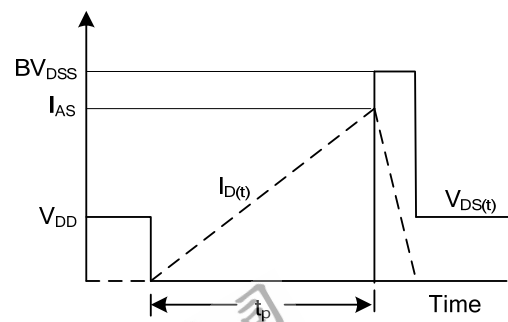


Charge

Gate Charge Waveform

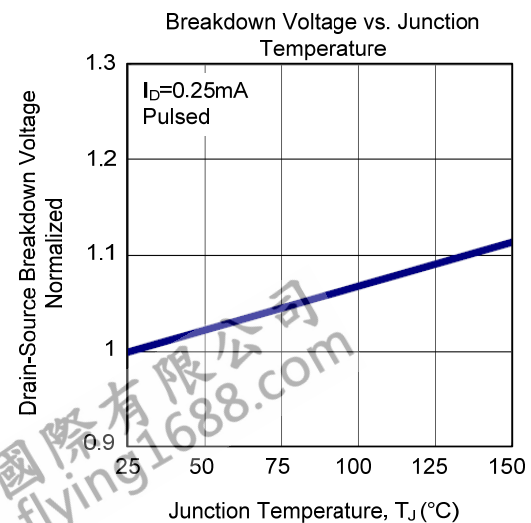
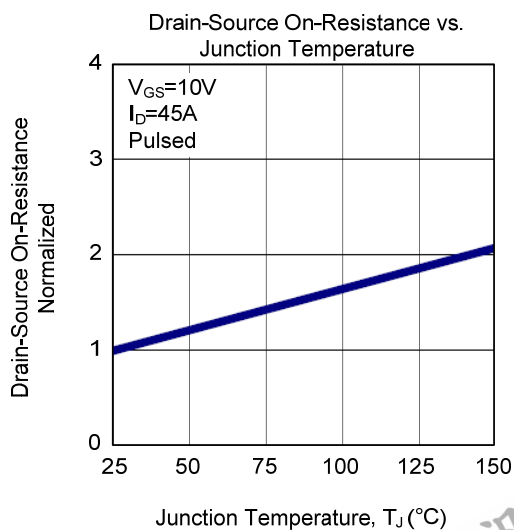
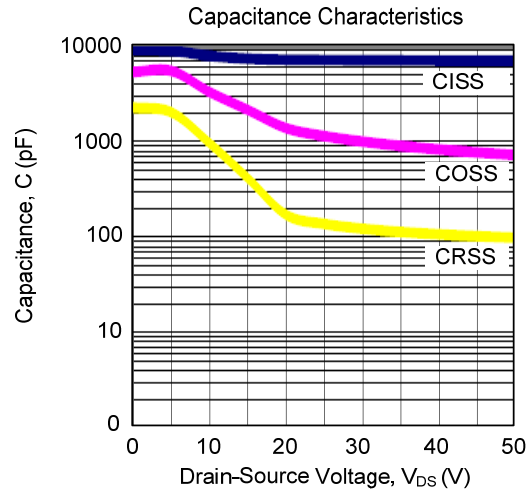
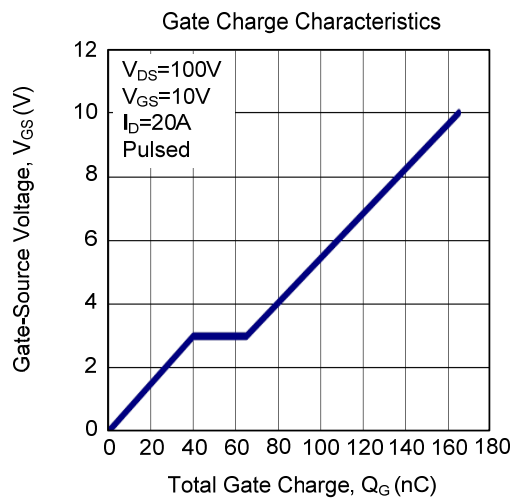
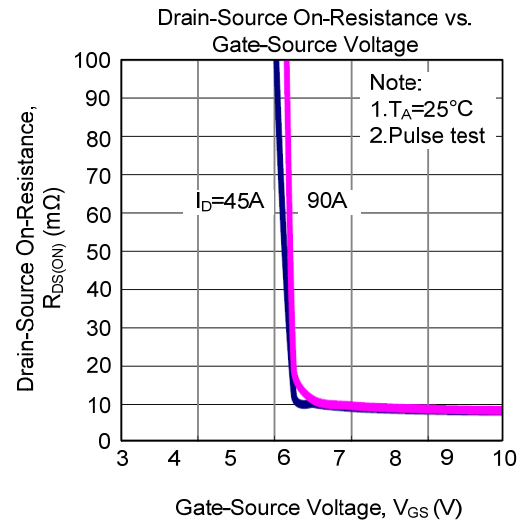
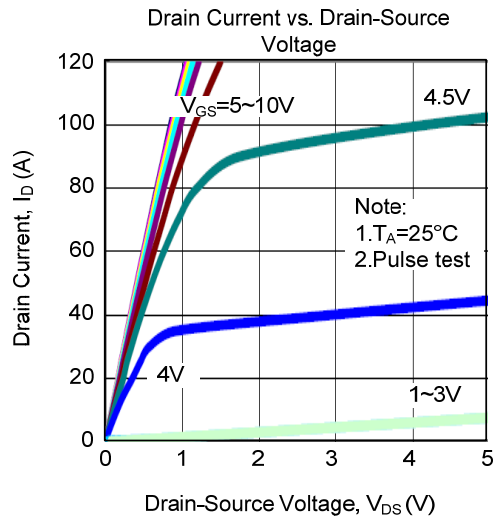


Unclamped Inductive Switching Test Circuit

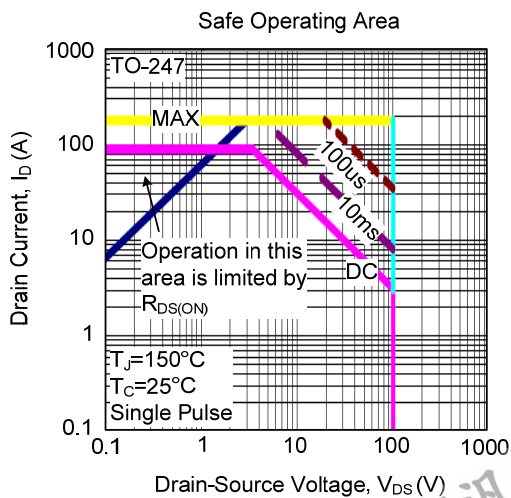
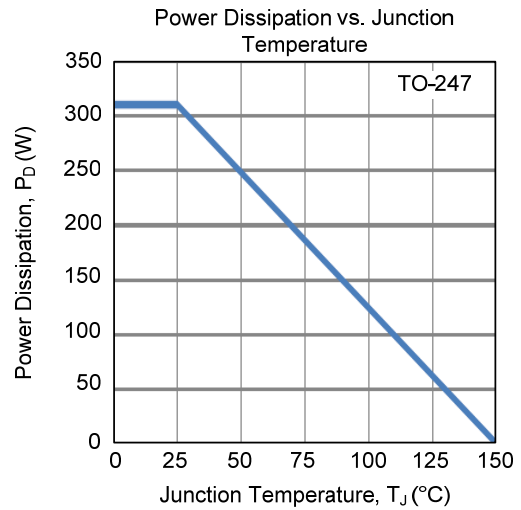
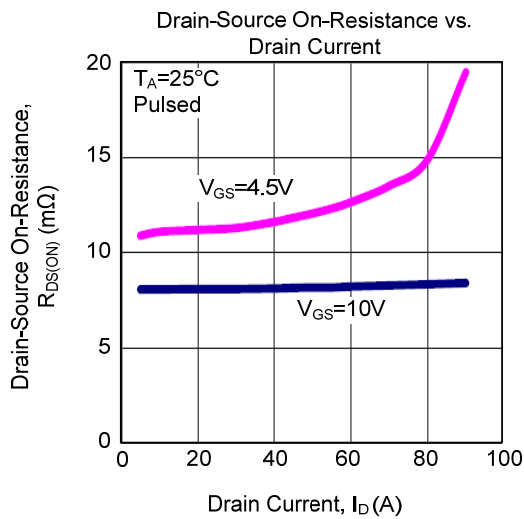
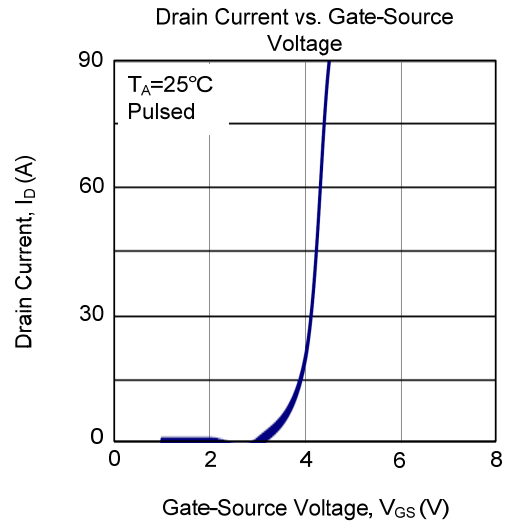
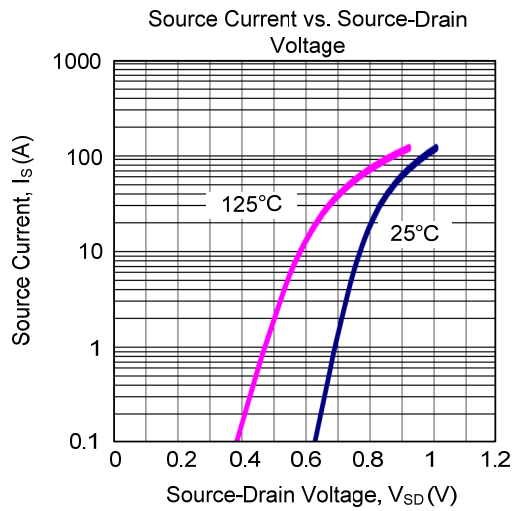


Unclamped Inductive Switching Waveforms

### TYPICAL CHARACTERISTICS



### ■ TYPICAL CHARACTERISTICS (Cont.)



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