# UNISONIC TECHNOLOGIES CO., LTD

UG5N120

**Preliminary** 

Insulated Gate Bipolar Transistor

# 21A, 1200V NPT N-CHANNEL **IGBT WITH ANTI-PARALLEL** HYPERFAST DIODES

#### **DESCRIPTION**

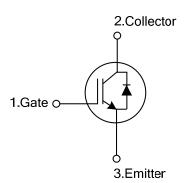
The UTC UG5N120 is a NPT N-Channel IGBT, it uses UTC's advanced technology to provide the customers with a minimum on-state resistance, etc.

The UTC UG5N120 is suitable for AC and DC motor controls, power supplies, and drivers for solenoids, relays and contactors, etc.

#### **FEATURES**

- \* Low conduction loss
- \* Short circuit rating

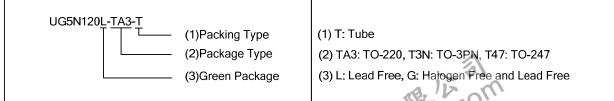
#### **SYMBOL**



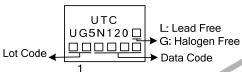
# **ORDERING INFORMATION**

Ordering Number		Daakaga	Pin	Assignn	Dooking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UG5N120L-TA3-T	UG5N120G-TA3-T	TO-220	G	С	Е	Tube	
UG5N120L-T3N-T	UG5N120G-T3N-T	TO-3PN	G	С	E	Tube	
UG5N120L-T47-T	UG5N120G-T47-T	TO-247	G	С	Е	Tube	

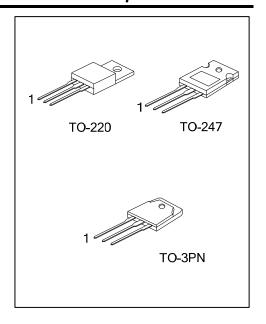
C: Collector E: Emitter Note: Pin Assignment: G: Gate



## **MARKING**



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# ABSOLUTE MAXIMUM RATING (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector to Emitter Voltage		BV <sub>CES</sub>	1200	V
Gate-Emitter Voltage		$V_{\sf GES}$	±20	V
Gate to Emitter Voltage Pulsed		$V_{GEM}$	±30	V
Collector Current Continuous	T <sub>C</sub> =25°C		21	Α
Collector Current Continuous	T <sub>C</sub> =110°C	I <sub>C</sub>	10	Α
Collector Current Pulsed (Note 1)		I <sub>CM</sub>	40	Α
Power Dissipation Total at T <sub>C</sub> = 25°C	TO-220		78	W
	TO-3PN		201	W
	TO-247	Б	167	W
Power Dissipation Derating T <sub>C</sub> > 25°C	TO-220	$P_D$	0.62	W/°C
	TO-3PN		1.6	W/°C
	TO-247		1.33	W/°C
Short Circuit Withstand Time (Note 2) at V <sub>GE</sub> =15V		t <sub>SC</sub>	8	μs
Short Circuit Withstand Time (Note 2) at V <sub>GE</sub> =12V		t <sub>sc</sub>	15	μs
Operating Junction Temperature Range		$T_J$	-55 ~ +150	°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Pulse width limited by maximum junction temperature.
- 3.  $I_{CE}$ =10A, L=400 $\mu$ H,  $T_{J}$ =25 $^{\circ}$ C.
- 4.  $V_{CE(PK)}$ =840V,  $T_J$ =125°C,  $R_G$ =25 $\Omega$ .

## THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Case	TO-220	θ <sub>JC</sub>	1.6	°C/W
	TO-3PN		0.62	°C/W
	TO-247		0.75	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Collector to Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> =250μA, V <sub>GE</sub> =0V		1200			٧
Collector to Emitter Leakage Current	I <sub>CES</sub>	V <sub>CE</sub> =1200V	T <sub>C</sub> =25°C			250	μΑ
			T <sub>C</sub> =125°C		100		μΑ
			T <sub>C</sub> =150°C			1.5	mΑ
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =5A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C		2.45	2.7	V
			T <sub>C</sub> =150°C		3.7	4.2	V
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I <sub>C</sub> =45µA, V <sub>CE</sub> =V <sub>GE</sub>		6.0	6.8		V
Gate to Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> =±20V				±250	nA
Switching SOA	SSOA	$T_J = 150^{\circ}\text{C}, R_G = 25\Omega, V_{GE} = 15V,$ 30					Α
Switching SOA		L=5mH, V <sub>CE(PK)</sub> =1200V		30			^
Gate to Emitter Plateau Voltage	$V_{GEP}$	I <sub>C</sub> =5A, V <sub>CE</sub> =600V			10.5		V
On-State Gate Charge	Q <sub>G(ON)</sub>	I <sub>C</sub> =5A, V <sub>CE</sub> =600V	V <sub>GE</sub> =15V	2	53	65	nC
On-State Gate Charge			V <sub>GE</sub> =20V	07	60	72	nC
Current Turn-On Delay Time	t <sub>d(ON)I</sub>	ICPT and Diada at	TASEC	JU,	220		ns
Current Rise Time	t <sub>rl</sub>	-IGBT and Diode at $T_J$ =25°C - $I_{CE}$ =1.0A, $V_{CE}$ =30V, $V_{GE}$ =15V, - $R_G$ =25Ω		Ò	360		ns
Current Turn-Off Delay Time	t <sub>d(OFF)I</sub>				320		ns
Current Fall Time	t <sub>fl</sub>				120		ns
	THE STATE OF THE S	R <sub>G</sub> =25Ω					
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