

UTC UNISONIC TECHNOLOGIES CO., LTD

UH4921

Preliminary

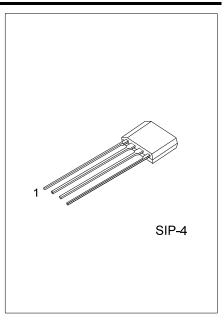
LINEAR INTEGRATED CIRCUIT

DYNAMIC DIFFERENTIAL HALL EFFECT SENSOR IC WITH DIGITAL SIGNAL OUTPUT

DESCRIPTION

The differential Hall Effect sensor UTC UH4921 provides a superior stability and a high sensitivity over temperature and symmetrical thresholds in order to achieve a stable duty cycle. The integrated circuit provides a digital signal output with frequency proportional to the speed of rotation. Unlike other rotational sensors differential Hall ICs are not influenced by radial vibration within the effective airgap of the sensor and require no external signal processing.

UTC UH4921 is particularly suitable for rotational speed detection and timing applications of ferromagnetic toothed wheels such as anti-lock braking systems, transmissions, crankshafts, etc.



FEATURES

- * Digital output signal
- * Low cut-off frequency
- * High sensitivity
- * Symmetrical thresholds
- * Reduced power consumption
- * AC coupled
- * Output protection against electrical disturbances
- * Large temperature range

- * Large airgap
- * Protection against overvoltage
- * Protection against reversed polarity
- * Two-wire and three-wire configuration possible
- * Advanced performance
- * South and north pole pre-induction possible
- * High piezo resistivity

ORDERING INFORMATION

Ordering Number	Package	Packing
UH4921G-G04-K	SIP-4	Bulk

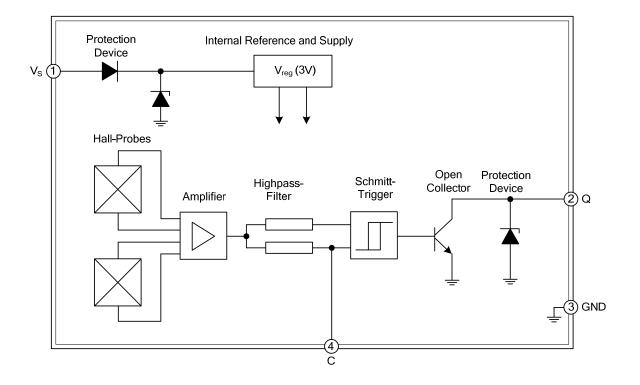
(1)Packing Type (1) K: Bulk (2) G04: SIP-4 (3) Green Package (3) G: Halogen Free and Lead Free	12 1 688. com		MARKING UTC UH4921G
(2)Package Type (2) G04: SIP-4	(3) G: Halogen Free and Lead Free	(3)Green Package	
UH4921 <u>Ģ-G04</u> -Ķ	(2) G04: SIP-4	(2)Package Type	UH4921 <u>G-G04-K</u>

5

PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	Vs	Supply voltage
2	Q	Output
3	GND	Ground
4	С	Capacitor

BLOCK DIAGRAM





ABSOLUTE MAXIMUM RATING

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage (Note 1)		Vs	30	V
Output Voltage		V _Q	-0.7 ~ 30	V
Output Current		lq	50	mA
Output Reverse Current		-l _Q	50	mA
Capacitor Voltage		Vc	-0.3 ~ 3	V
Current Through Input-Protection Device t<2ms, v=0.1		I _{SZ}	200	mA
Current Through Output-Protection Device	l _{QZ}	200	mA	
Junction Temperature		TJ	150	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL RESISTANCES CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	190	K/W

OPERATING RANGE

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	Vs			24	V
Pre-Induction	B ₀	-500		500	mT
Differential Induction	ΔΒ	-80		80	mT

AC/DC ELECTRICAL CHARACTERISTICS

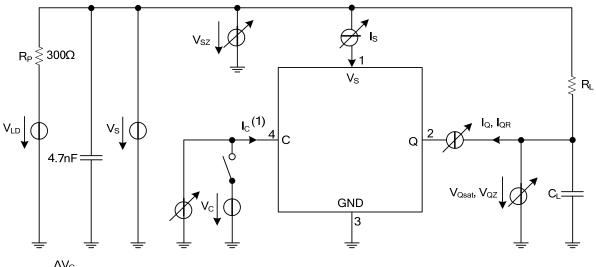
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
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Supply Current (Note 1)	Is	V _Q =high, I _Q =0mA	3.8	11	15	mA
Supply Sullent (Note 1)	15	V _Q =low, I _Q =40mA	4.3	12	18	mA
Output Saturation Voltage	V _{Qsat}	I _Q =40mA		0.25	0.6	V
Output Leakage Current	I _{QL}	V _Q <=24V			50	μA
Centre of Switching Points:		-20mT< ∆B<20mT				_
$(\Delta B_{OP} + \Delta B_{RP})/2$	ΔB_m	(Note 2, 3) ; f=200Hz	-2	0	2	mT
Operate Point	ΔB_{OP}	f=200Hz, ∆B=20mT			0	mT
Release Point	ΔB_{RP}	f=200Hz, ∆B=20mT	0			mT
Hysteresis	ΔB_{Hy}	f=200Hz, ∆B=20mT		1.5		mT
Output Rise Time	tr	I _Q =40mA, C _L =10pF			0.5	μs
Output Fall Time	t _f	I _Q =40mA, C _L =10pF			0.5	μs
	t _{dop}				25	μs
Delay Time	t _{drp}	f=10kHz, ∆B=5mT			10	μs
	t _{dop} -t _{drp}			0	15	μs
Filter Input Resistance	R _c	25°C±2°C	35	55	62	kΩ
Filter Sensitivity to ∆B	Sc			-5		mV/mT
Filter Bias Voltage	Vc	∆B=0	1.6	2	2.4	V

Notes: 1. Reverse current<10mA

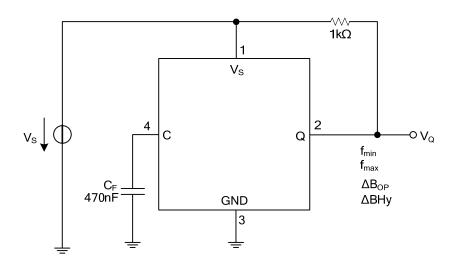
- 2. The Current consumption characteristic will be different and the specified values can slightly change.
- 3. Leakage currents at pin 4 should be avoided. The bias shift of Bm caused by a leakage current IL can be THE FEEL FILLING LAB IN THE INTERNAL STREET calculated by: $\Delta B_m = (I_L \times R_C(T))/S_C(T)$.
- 4. For higher ΔB the values may exceed the limits like following $|\Delta B_m| \le |0.05 \times \Delta B|$

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TEST CIRCUIT

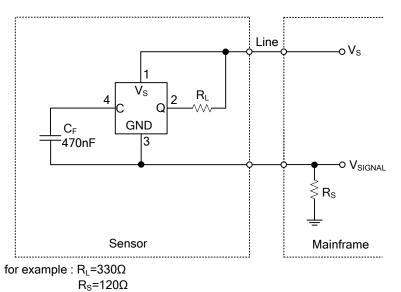




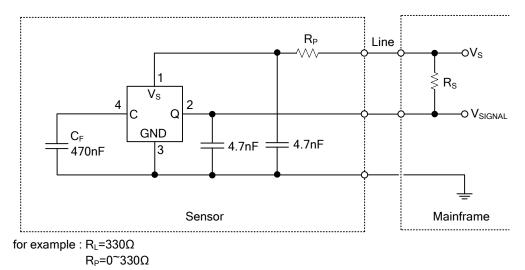


TYPICAL APPLICATION CIRCUIT

Two-wire-application



Three-wire-application



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