UPG12N60E

Preliminary

Insulated Gate Bipolar Transistor

600V, SMPS N-CHANNEL IGBT

DESCRIPTION

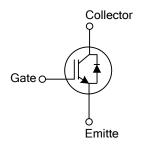
The UTC **UPG12N60E** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

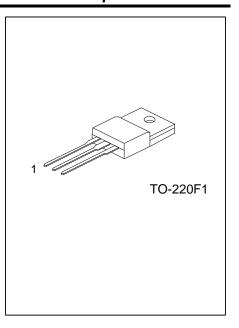
The UTC **UPG12N60E** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * $V_{CE(SAT)} \le 2.0 V @ I_C=12A, V_{GE}=15 V$
- * High switching speed
- * High input impedance
- * Low conduction loss

■ SYMBOL





ORDERING INFORMATION

Ordering Number		Darden	Pin Assignment			Da alda a	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UPG12N60EL-TF1-T	UPG12N60EG-TF1-T	TO-220F1	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UPG12N60EG-TF1-T (1)Packing Type (1) T: Tube

(2)Package Type (2) TF1: TO-220F1 (3)Green Package (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



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ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V _{CES}	600	V
Gate to Emitter Voltage Continuous		$V_{\sf GES}$	±20	V
Continuous Collector Current	T _C =25°C	_	24	Α
	T _C =100°C	Ic	12	Α
Collector Current Pulsed (Note 2)		I _{CM}	48	Α
Cantinuous Famuund Current	T _C =25°C		12	Α
Continuous Forward Current	T _C =100°C	I _F	6	Α
Forward Current Pulsed		I _{FM}	96	Α
Peak Diode Recovery dv/dt (Note 3)		dv/dt	6.9	V/ns
Power Dissipation		P_{D}	29	W
Junction Temperature		T_J	-55 ~ + 150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. $I_F \le 12A$, $di/dt \le 200A/\mu s$, $V_{CC} \le BV_{CES}$, Starting $T_J = 25$ °C

THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	4.3	°C/W

ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C =250μA, V _{GE} =0V		600			V
Collector-Emitter Leakage Current	I _{CES}	V _{CE} =600V, V _{GE} =0V				10	μΑ
Gate to Emitter Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nA
ON CHARACTERISTICS							
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	1 124 \/ 15\/	T _J =25°C		1.7	2.0	V
		I _C =12A, V _{GE} =15V	T _J =150°C		1.9		V
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I _C =250μA, V _{CE} =V _{GE}		4.0		6.5	V
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{IES}	V _{CE} =30V, V _{GE} =0V, f=1MHz			463		pF
Output Capacitance	C _{OES}				60		pF
Reverse Transfer Capacitance	C _{RES}				9.8		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q_G	I _C =12A, V _{CE} =50V, V _{GE} =15V			17		nC
Gate-Emitter Charge	Q_GE				5		nC
Gate-Collector Charge	Q_GC				5.7		nC
Current Turn-On Delay Time	t _{D(ON)}	I_{C} =12A, V_{CE} =50V, V_{GE} =15V, R_{G} =10 Ω			52		ns
Current Rise Time	t _R				57		ns
Current Turn-Off Delay Time	t _{D(OFF)}				57		ns
Current Fall Time	t _F	~ 3			75		ns
DRAIN-SOURCE DIODE CHARACTER	RISTICS	4	a, 12	U			
Forward Voltage Drop	V_{FM}	I _F =5A	LAS CO			2.1	V
Reverse Recovery Time	t _{rr}	I _F =5A, dl/dt=100A/μS, V _{CC} =400V			67		ns
Reverse Recovery Charge	Q_{rr}				150		nC
Note: Pulse Test: Pulse width ≤ 50µs.		300 111119					
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■ TEST CIRCUIT AND WAVEFORMS

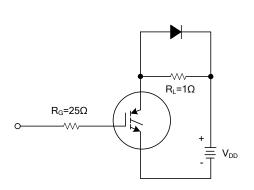


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

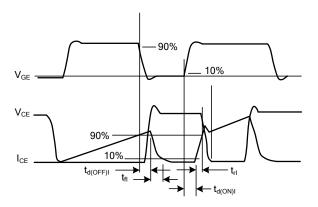


Fig 2. SWITCHING TEST WAVEFORMS

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