



UPG15N120

Insulated Gate Bipolar Transistor

1200V NPT PLANAR IGBT

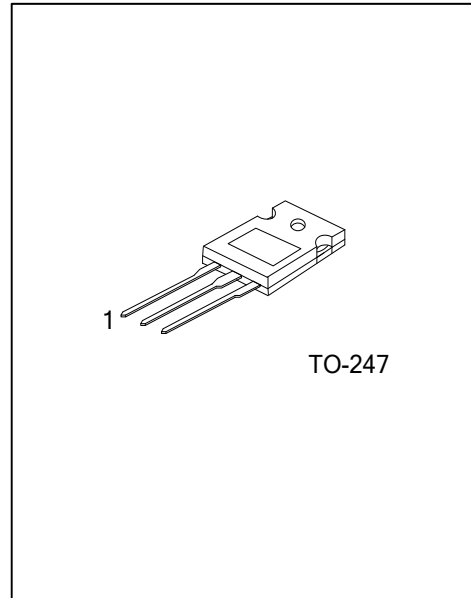
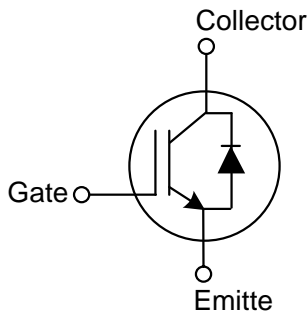
DESCRIPTION

The UTC **UPG15N120** is a 1200V NPT Planar Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to offers superior conduction and switching performance, high avalanche ruggedness and easy parallel operation.

FEATURES

- * High speed switching
- * High input impedance
- * Low saturation voltage: $V_{CE(SAT)} = 2.4V @ I_C = 15A$

SYMBOL



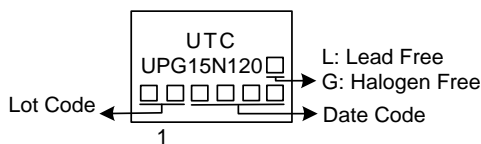
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UPG15N120L-T47-T	UPG15N120G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UPG15N120G-T47-T	(1) Packing Type	(1) T: Tube
	(2) Package Type	(2) T47: TO-247
	(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V_{CES}	1200	V	
Gate-Emitter Voltage	V_{GES}	± 20	V	
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	30	A
		$T_C=100^\circ\text{C}$	15	A
Collector Current Pulsed (Note 1)	I_{CM}	60	A	
Power Dissipation	P_D	300	W	
Operating Junction Temperature	T_J	-55 ~ +150	$^\circ\text{C}$	
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

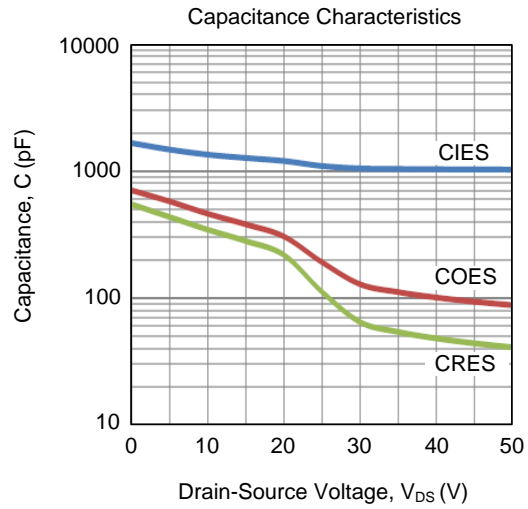
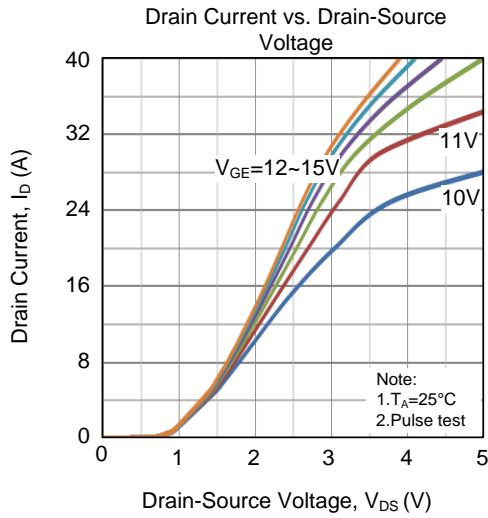
■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	0.42	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off Characteristics						
Collector-Emitter Breakdown Voltage	$B_{V_{CES}}$	$I_C=250\mu\text{A}, V_{GE}=0\text{V}$	1200			V
Collector Cut-Off Current	I_{CES}	$V_{CE}=V_{CES}, V_{GE}=0\text{V}$			250	μA
G-E Leakage Current	I_{GES}	$V_{GE}=V_{GES}, V_{CE}=0\text{V}$			± 250	nA
On Characteristics						
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=90\mu\text{A}, V_{CE}=V_{GE}$	4.0		6.0	V
Collector to Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C=15\text{A}, V_{GE}=15\text{V}$		2.0	2.4	V
Dynamic Characteristics						
Input Capacitance	C_{IES}	$V_{CE}=25\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$		1090		pF
Output Capacitance	C_{OES}			190		pF
Reverse Transfer Capacitance	C_{RES}			110		pF
Switching Characteristics						
Total Gate Charge	Q_G	$V_{CE}=100\text{V}, V_{GE}=15\text{V}, I_C=15\text{A}$		95		nC
Gate-Emitter Charge	Q_{GE}	$V_{CE}=100\text{V}, V_{GE}=15\text{V}, I_C=15\text{A}$		20		nC
Gate-Collector Charge	Q_{GC}			44		nC
Turn-On Delay Time	$t_{D(ON)}$			50		ns
Rise Time	t_R	$V_{CC}=50\text{V}, V_{GE}=15\text{V}, I_C=15\text{A}, R_G=10\Omega,$		155		ns
Turn-Off Delay Time	$t_{D(OFF)}$			210		ns
Fall Time	t_F			85		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Forward Voltage Drop	V_{FM}	$I_F=15\text{A}$		2.2		V
Reverse Recovery Time	t_{rr}	$I_F=15\text{A}, di/dt=200\text{A}/\mu\text{S}$		116		ns
Reverse Recovery Charge	Q_{rr}			370		nC

TYPICAL CHARACTERISTICS



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