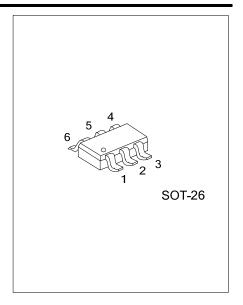
UPSL304 **Preliminary CMOS IC** 

# **HIGH POWER FACTOR & ACCURACY CONSTANT** CURRENT LED DRIVER

#### DESCRIPTION

The UTC UPSL304 is a low startup current, Transition Mode, fixed on-time PFC control and PWM controller. These functions enable the LED driver to easily meet the accuracy average LED current and high power factor requirements.

The UTC UPSL304 improves the performance and reduces the cost of the LED driver.

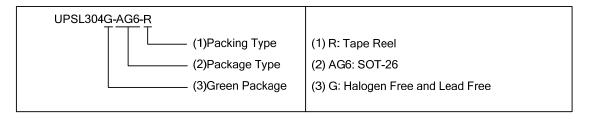


#### **FEATURES**

- \* Transition Mode Fixed On-Time PFC Control
- \* Accuracy Constant Current
- \* Low BOM Cost
- \* Inductor Size Reduction
- \* Frequency Range Adjusted by The CT Pin
- \* LED Protection: SCP, OLP, OVP
- \* SOT-26 Package
- \* Compatible Inductance Ballast
- \* Incompatible Electronic Ballast

#### ORDERING INFORMATION

Ordering Number	Package	Packing		
UPSL304G-AG6-R	SOT-26	Tape Reel		



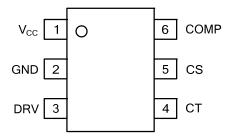
## **MARKING**



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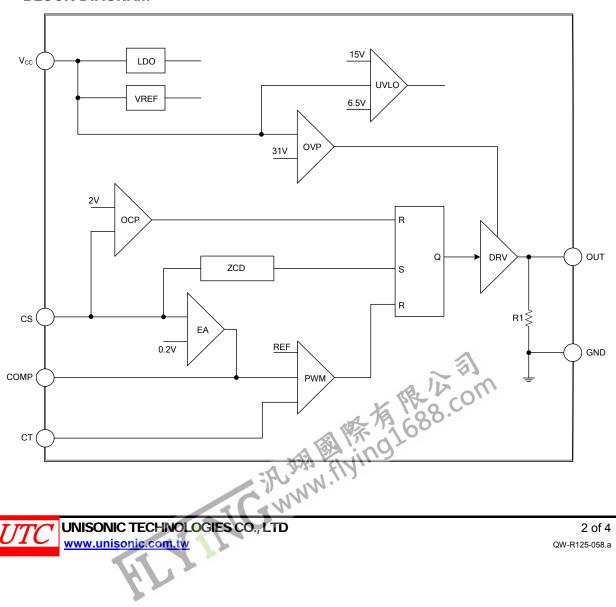
# **PIN CONFIGURATION**



# **PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION		
1	$V_{CC}$	Power Supply Pin		
2	GND	Ground Pin		
3	DRV	The DRV pin is connected to the gate driver to drive the external power switch.		
4	СТ	The CT pin is connected to the current source to charge the external capacitor and compare the COMP voltage to terminate the power switch.		
5	CS	Current Sense Pin		
6	COMP	Feedback Compensation Network		

# **BLOCK DIAGRAM**



## **■ ABSOLUTE MAXIMUM RATING**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{CC}$	30	V
CT, COMP, CS		-0.3 ~ +7.0	V
DRV		15	V
Power Dissipation (T <sub>A</sub> =85°C)	$P_D$	250	mW
Junction Temperature	$T_J$	+150	°C
Operating Ambient Temperature		-20 ~ +85	°C
Storage Temperature Range	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT	
	Junction to Ambient	θιΔ	500	°C/W	

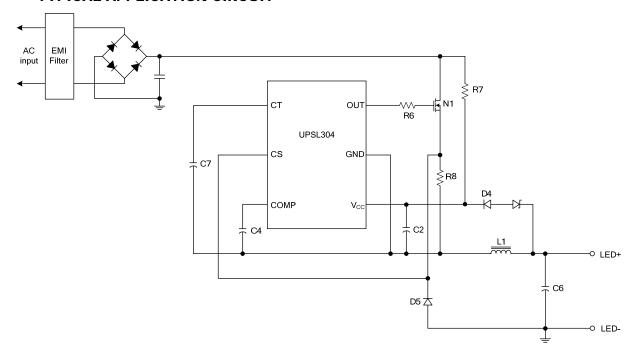
## **■ ELECTRICAL CHARACTERISTICS**

(V<sub>CC</sub>=15.0V & T<sub>A</sub>=+25°C, unless otherwise specified.)

(100 10:01 a 1A 120 0; amous o		Jooniou.)				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Startup Current	I <sub>ST</sub>	V <sub>CC</sub> =UVLO on-1V		10	50	uA
Operating Current	Icc	with 1nF load on OUT pin, V <sub>COMP</sub> =2.5V		2.5	3.2	mA
Operating Current	Iqc	with 1nF load on OUT pin, Protection Tripped (OVP, SCP)		1.6	2.5	mA
UVLO (off)	$V_{MIN}$		5.5	6.5	7.5	<b>V</b>
UVLO (on)	$V_{ST}$		14	15	16	V
OVP Level on VCC Pin	$V_{OVP}$		29.5	31.5	33.5	V
OVP De-Bounce Time				40		uS
Feedback Reference Voltage	$V_{FB}$		0.195	0.200	0.205	V
Tran-Conductance				300		uS
Output Sink Current	I <sub>SINK</sub>			300		uA
Output Source Current	I <sub>SOURCE</sub>			60		uA
Input Over Voltage Protection	$V_{OCP}$		1.8	2.0	2.2	V
Open Loop Voltage, CS Pin Open	V <sub>CS</sub>			5		V
Leading-Edge Blanking Time	T <sub>LEB</sub>			450		nS
Delay to Output	T <sub>DELAY</sub>			130	220	nS
Rising Time	$T_R$	Load Capacitance=1000pF		280	500	nS
Falling Time	$T_F$	Load Capacitance=1000pF		80	150	nS
VGATE-Clamp	$V_{CLAMP}$	V <sub>CC</sub> =25V		12.5	15	V
CT Pin Current			130	150	170	uA



## TYPICAL APPLICATION CIRCUIT



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