

UR6225

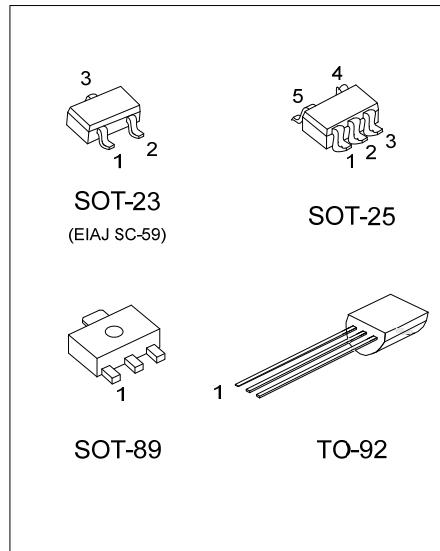
CMOS IC

POSITIVE VOLTAGE REGULATOR

■ DESCRIPTION

The UTC **UR6225** is a positive voltage output, three-pin regulator that provides a high current even when the input/output voltage differential is small. Low power consumption and high accuracy is achieved through CMOS and laser trimming technologies.

The UTC **UR6225** consists of a high-precision voltage reference, an error amplification circuit, and a current limited output driver. Transient responses to load variations have improved in comparison to the existing series.



■ FEATURES

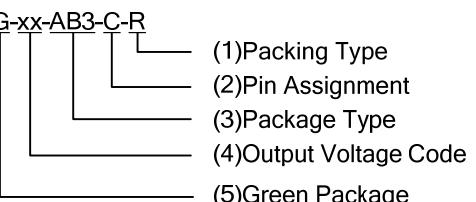
- * Maximum Output Current: 300mA (Within Max. Power Dissipation, $V_{OUT} = 5.0V$)
- * Output Voltage Range: 1.5V ~ 6.0V in 0.1V Increments (1.5V ~ 1.9V for Custom Products)
- * Highly Accurate: Output Voltage $\pm 2\%$ ($\pm 1\%$ for Semi-Custom Products)
- * Low Power Consumption: Typ. 2.0 μ A @ $V_{OUT}=5.0V$
- * Output Voltage Temperature Characteristics: Typ. $\pm 100\text{ppm}/^{\circ}\text{C}$
- * Input Stability : Typ. 0.2%/V
- * Small Input-Output Differential: $I_{OUT} = 100\text{mA}$ @ $V_{OUT} = 5.0\text{V}$ with a 0.12V Differential.

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment					Packing
Lead Free	Halogen Free		1	2	3	4	5	
UR6225L-xx-AB3-C-R	UR6225G-xx-AB3-C-R	SOT-89	G	I	O	-	-	Tape Reel
UR6225L-xx-AE3-3-R	UR6225G-xx-AE3-3-R	SOT-23	G	O	I	-	-	Tape Reel
UR6225L-xx-AF5-C-R	UR6225G-xx-AF5-C-R	SOT-25	I	G	N	N	O	Tape Reel
UR6225L-xx-AF5-F-R	UR6225G-xx-AF5-F-R	SOT-25	G	I	O	N	N	Tape Reel
UR6225L-xx-T92-C-B	UR6225G-xx-T92-C-B	TO-92	G	I	O	-	-	Tape Box
UR6225L-xx-T92-C-K	UR6225G-xx-T92-C-K	TO-92	G	I	O	-	-	Bulk
UR6225L-xx-T92-B-B	UR6225G-xx-T92-B-B	TO-92	O	G	I	-	-	Tape Box
UR6225L-xx-T92-B-K	UR6225G-xx-T92-B-K	TO-92	O	G	I	-	-	Bulk

Note: Pin Assignment: I: V_{IN} O: V_{OUT} G: V_{SS} N: No Connection

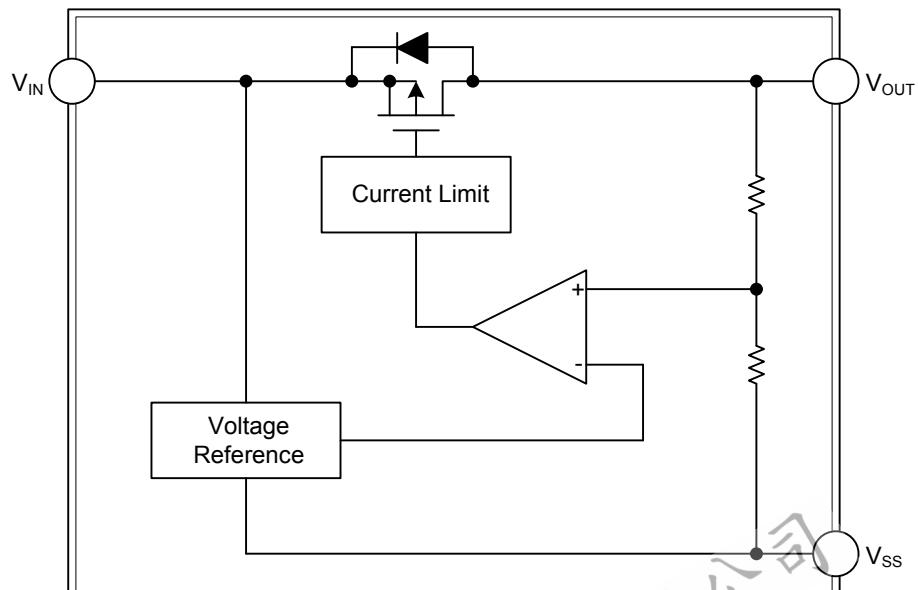
xx: Output Voltage, refer to Marking Information.

 (1)Packing Type (2)Pin Assignment (3)Package Type (4)Output Voltage Code (5)Green Package	(1) R:Tape Reel, K:Bulk, B:Tape Box (2) refer to Pin Assignment (3) AB3:SOT-89, AE3:SOT-23, AF5:SOT-25, T92:TO-92 (4) xx:refer to Marking Information (5) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
SOT-89	15:1.5V 18:1.8V 20:2.0V	Date Code ← UR6225 → 1 2 3 Voltage Code ← L: Lead Free G: Halogen Free
	21:2.1V 25:2.5V 26:2.6V 27:2.7V 28:2.8V 2J:2.85V	Voltage Code ← F2XX → 1 2 3 L: Lead Free G: Halogen Free
SOT-23	30:3.0V 31:3.1V 33:3.3V 35:3.5V 36:3.6V 38:3.8V	Voltage Code ← F2XX → 1 2 L: Lead Free G: Halogen Free
	40:4.0V 45:4.5V 50:5.0V 60:6.0V	Pin Code ← UTC UR6225 → 1 2 3 L: Lead Free G: Halogen Free
TO-92		Voltage Code ← Date Code → 1 2 3 Pin Code ← UTC UR6225 → L: Lead Free G: Halogen Free

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	10	V
Output Current	I_{OUT}	300	mA
Output Voltage	V_{OUT}	$V_{SS}-0.3 \sim V_{IN}+0.3$	V
Power Dissipation	SC-23/SOT-25	250	mW
	SOT-89	500	mW
	TO-92	300	mW
Junction Temperature	T_J	+125	°C
Operating Temperature	T_{OPR}	-40 ~ +85	°C
Storage Temperature	T_{STG}	-40 ~ +125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

UR6225-6.0V (Note1)

PARAMETER	CIRCUIT	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	1	$V_{OUT}(E)(\text{Note2})$	$I_{OUT}=40\text{mA}, V_{IN}=7.0\text{V}$	5.880	6.000	6.120	V
Maximum Output Current	1	$I_{OUT(\text{MAX})}$	$V_{IN}=7.0\text{V}, V_{OUT}(E)\geq 5.4\text{V}$	250			mA
Minimum Load Current		$I_{OUT(\text{MIN})}$	$V_{IN}=V_{OUT}+1\text{V}$			50	μA
Load Stability	1	ΔV_{OUT}	$V_{IN}=7.0\text{V}, 1\text{mA}\leq I_{OUT}\leq 100\text{mA}$	40	80		mV
Input-Output Voltage	1	V_{DIF1}	$I_{OUT}=100\text{mA}$		120		mV
Differential (Note3)	1	V_{DIF2}	$I_{OUT}=200\text{mA}$		380		mV
Supply Current	2	I_{SS}	$V_{IN}=7.0\text{V}$		2.0	4.5	μA
Input Stability	1	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$I_{OUT}=40\text{mA}$ $7.0\text{V}\leq V_{IN}\leq 10\text{V}$		0.2	0.3	%/V
Input Voltage		V_{IN}	$I_{OUT}=5\text{mA}$			10	V
Output Voltage Temperature Characteristics	1	$\frac{\Delta V_{OUT}}{\Delta T_{OPR} \times V_{OUT}}$	$I_{OUT}=40\text{mA}$ $-40^\circ\text{C}\leq T_{OPR}\leq 85^\circ\text{C}$		±100		ppm/°C

UR6225-5.0V (Note1)

PARAMETER	CIRCUIT	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	1	$V_{OUT}(E)(\text{Note2})$	$I_{OUT}=40\text{mA}, V_{IN}=6.0\text{V}$	4.900	5.000	5.100	V
Maximum Output Current	1	$I_{OUT(\text{MAX})}$	$V_{IN}=6.0\text{V}, V_{OUT}(E)\geq 4.5\text{V}$	250			mA
Minimum Load Current		$I_{OUT(\text{MIN})}$	$V_{IN}=V_{OUT}+1\text{V}$			50	μA
Load Stability	1	ΔV_{OUT}	$V_{IN}=6.0\text{V}, 1\text{mA}\leq I_{OUT}\leq 100\text{mA}$	40	80		mV
Input-Output Voltage	1	V_{DIF1}	$I_{OUT}=100\text{mA}$		120		mV
Differential (Note3)	1	V_{DIF2}	$I_{OUT}=200\text{mA}$		380		mV
Supply Current	2	I_{SS}	$V_{IN}=6.0\text{V}$		2.0	4.5	μA
Input Stability	1	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$I_{OUT}=40\text{mA}$ $6.0\text{V}\leq V_{IN}\leq 10\text{V}$		0.2	0.3	%/V
Input Voltage		V_{IN}	$I_{OUT}=5\text{mA}$			10	V
Output Voltage Temperature Characteristics	1	$\frac{\Delta V_{OUT}}{\Delta T_{OPR} \times V_{OUT}}$	$I_{OUT}=40\text{mA}$ $-40^\circ\text{C}\leq T_{OPR}\leq 85^\circ\text{C}$		±100		ppm/°C

■ ELECTRICAL CHARACTERISTICS (Cont.)

UR6225-3.0V (Note1)

PARAMETER	CIRCUIT	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	1	$V_{OUT}(E)(Note2)$	$I_{OUT}=40mA, V_{IN}=4.0V$	2.940	3.000	3.060	V
Maximum Output Current	1	$I_{OUT(MAX)}$	$V_{IN}=4.0V, V_{OUT}(E) \geq 2.7V$	150			mA
Minimum Load Current		$I_{OUT(MIN)}$	$V_{IN}=V_{OUT}+1V$			50	μA
Load Stability	1	ΔV_{OUT}	$V_{IN}=4.0V, 1mA \leq I_{OUT} \leq 80mA$		45	90	mV
Input-Output Voltage Differential(Note3)	1	V_{DIF1}	$I_{OUT}=80mA$		180		mV
Supply Current	2	I_{SS}	$V_{IN}=4.0V$		2.0	4.5	μA
Input Stability	1	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$I_{OUT}=40mA$ $4.0V \leq V_{IN} \leq 10V$		0.2	0.3	%/V
Input Voltage		V_{IN}	$I_{OUT}=5mA$			10	V
Output Voltage Temperature Characteristics	1	$\frac{\Delta V_{OUT}}{\Delta T_{OPR} \times V_{OUT}}$	$I_{OUT}=40mA$ $-40^{\circ}C \leq T_{OPR} \leq 85^{\circ}C$		± 100		ppm/ $^{\circ}C$

UR6225-3.1V (Note1)

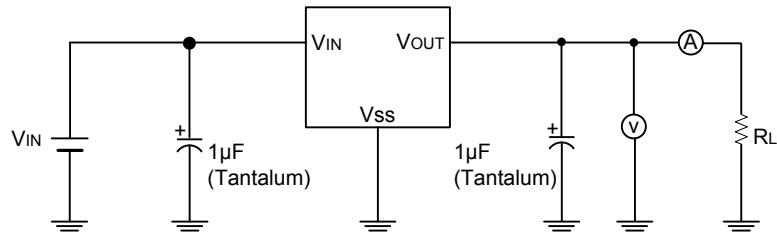
PARAMETER	CIRCUIT	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	1	$V_{OUT}(E)(Note2)$	$I_{OUT}=40mA, V_{IN}=4.0V$	3.038	3.1	3.162	V
Maximum Output Current	1	$I_{OUT(MAX)}$	$V_{IN}=4.0V, V_{OUT}(E) \geq 2.7V$	150			mA
Minimum Load Current		$I_{OUT(MIN)}$	$V_{IN}=V_{OUT}+1V$			50	μA
Load Stability	1	ΔV_{OUT}	$V_{IN}=4.0V, 1mA \leq I_{OUT} \leq 80mA$		45	90	mV
Input-Output Voltage Differential(Note3)	1	V_{DIF1}	$I_{OUT}=80mA$		180		mV
Supply Current	2	I_{SS}	$V_{IN}=4.0V$		2.0	4.5	μA
Input Stability	1	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$I_{OUT}=40mA$ $4.0V \leq V_{IN} \leq 10V$		0.2	0.3	%/V
Input Voltage		V_{IN}	$I_{OUT}=5mA$			10	V
Output Voltage Temperature Characteristics	1	$\frac{\Delta V_{OUT}}{\Delta T_{OPR} \times V_{OUT}}$	$I_{OUT}=40mA$ $-40^{\circ}C \leq T_{OPR} \leq 85^{\circ}C$		± 100		ppm/ $^{\circ}C$

UR6225-2.85V (Note1)

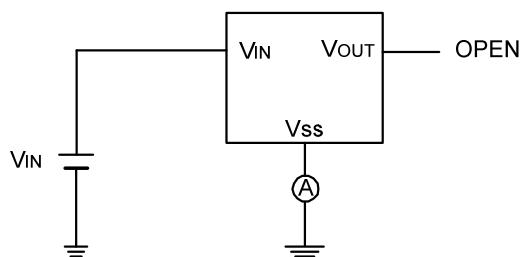
PARAMETER	CIRCUIT	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	1	$V_{OUT}(E)(Note2)$	$I_{OUT}=40mA, V_{IN}=3.85V$	2.793	2.850	2.907	V
Maximum Output Current	1	$I_{OUT(MAX)}$	$V_{IN}=3.85V, V_{OUT}(E) \geq 2.565V$	150			mA
Minimum Load Current		$I_{OUT(MIN)}$	$V_{IN}=V_{OUT}+1V$			50	μA
Load Stability	1	ΔV_{OUT}	$V_{IN}=3.85V, 1mA \leq I_{OUT} \leq 77mA$		45	90	mV
Input-Output Voltage Differential(Note3)	1	V_{DIF1}	$I_{OUT}=77mA$		180		mV
Supply Current	2	I_{SS}	$V_{IN}=3.85V$		2.0	4.5	μA
Input Stability	1	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$I_{OUT}=40mA$ $3.85V \leq V_{IN} \leq 10V$		0.2	0.3	%/V
Input Voltage		V_{IN}	$I_{OUT}=5mA$			10	V
Output Voltage Temperature Characteristics	1	$\frac{\Delta V_{OUT}}{\Delta T_{OPR} \times V_{OUT}}$	$I_{OUT}=40mA$ $-40^{\circ}C \leq T_{OPR} \leq 85^{\circ}C$		± 100		ppm/ $^{\circ}C$

■ TEST CIRCUITS

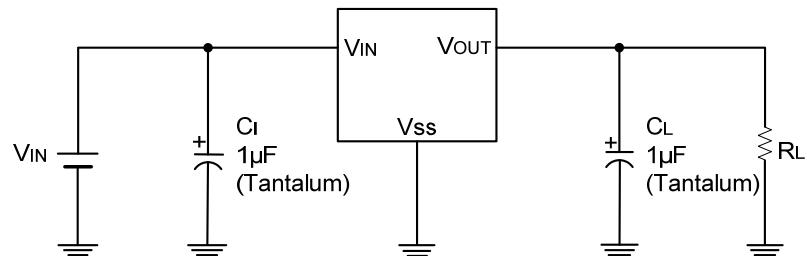
Circuit 1



Circuit 2

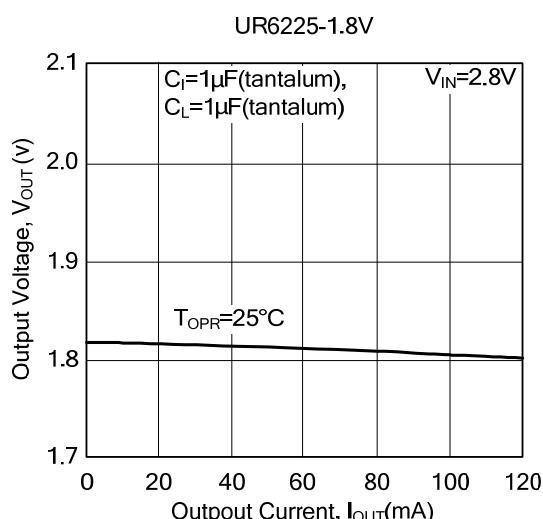
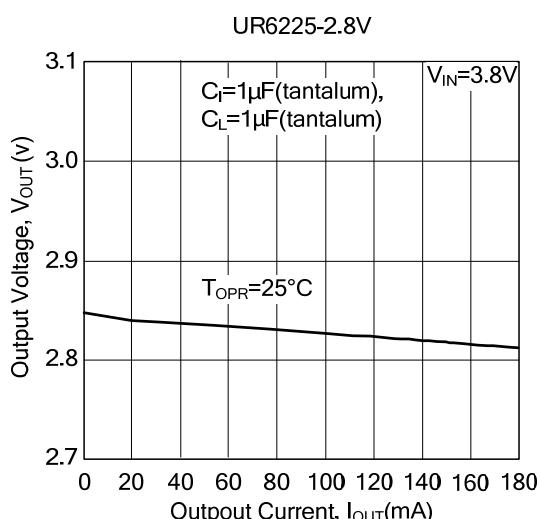
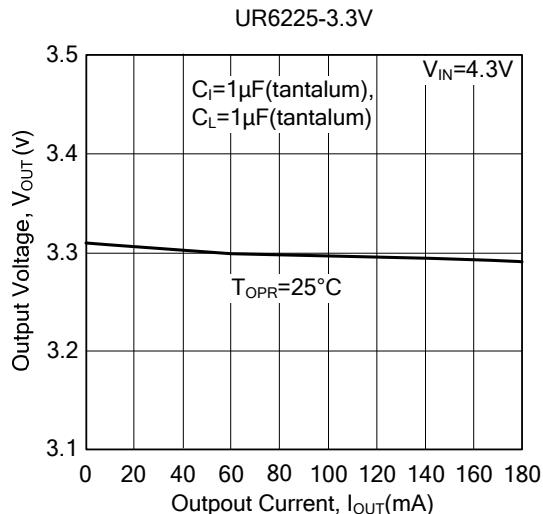
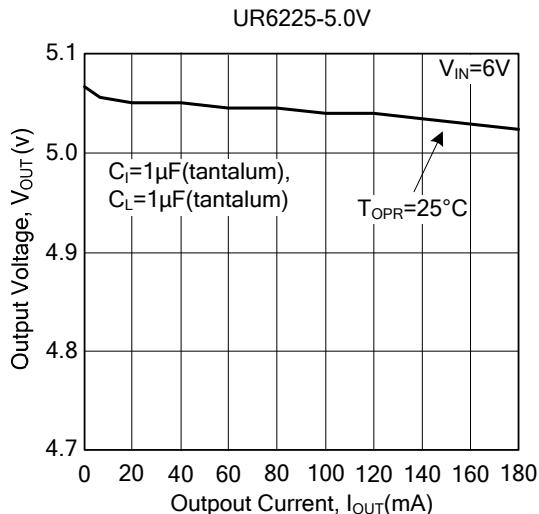


■ TYPICAL APPLICATION CIRCUIT

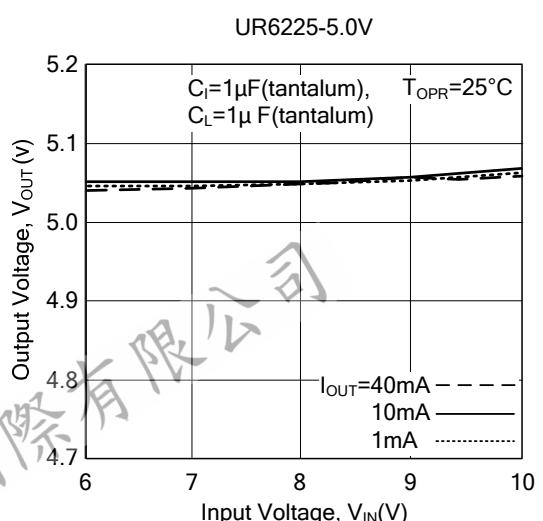
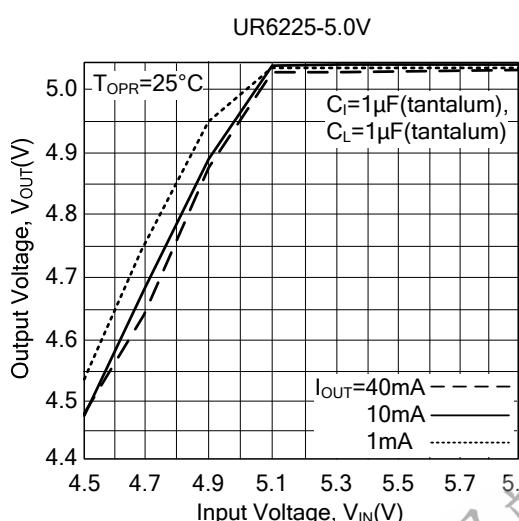


■ TYPICAL CHARACTERISTIC

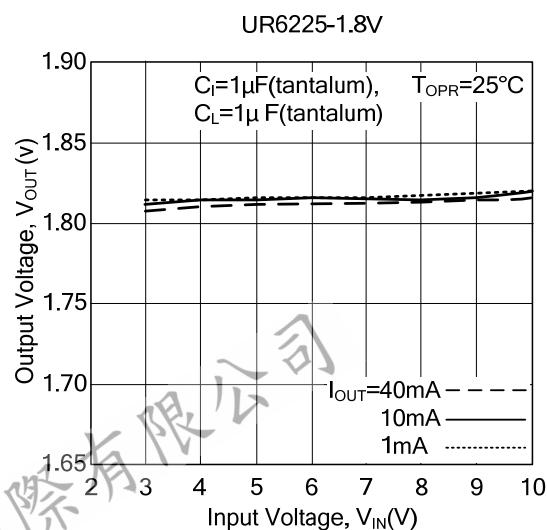
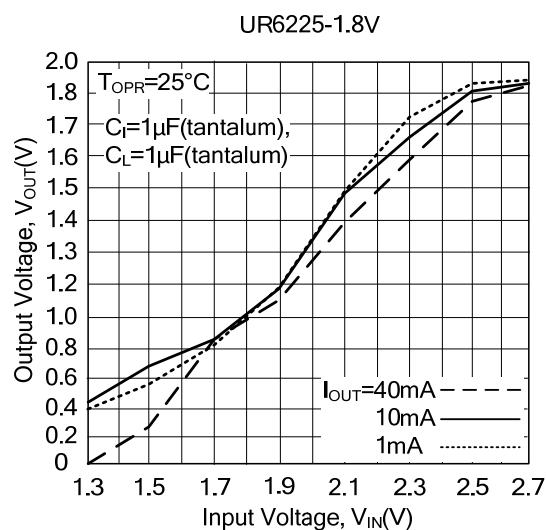
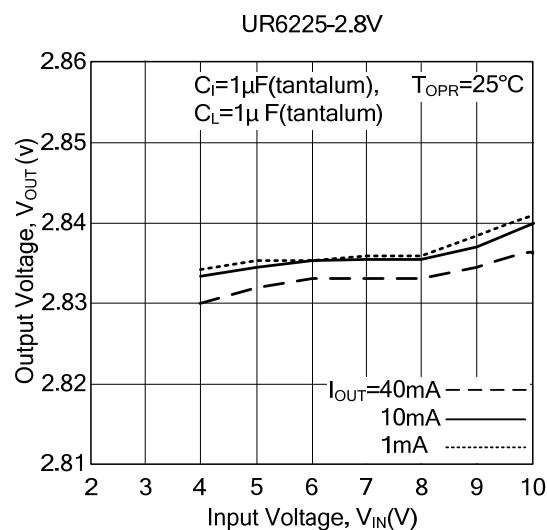
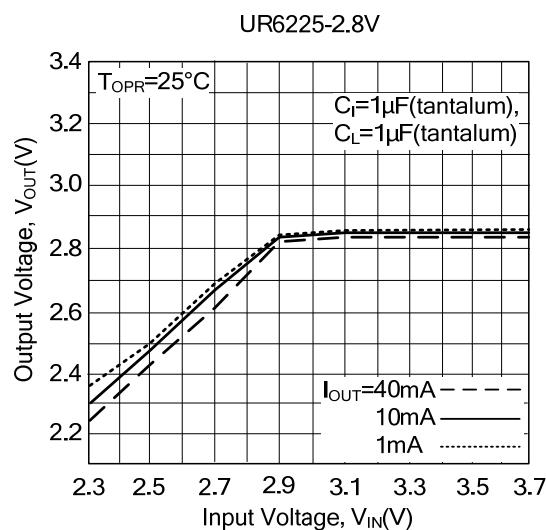
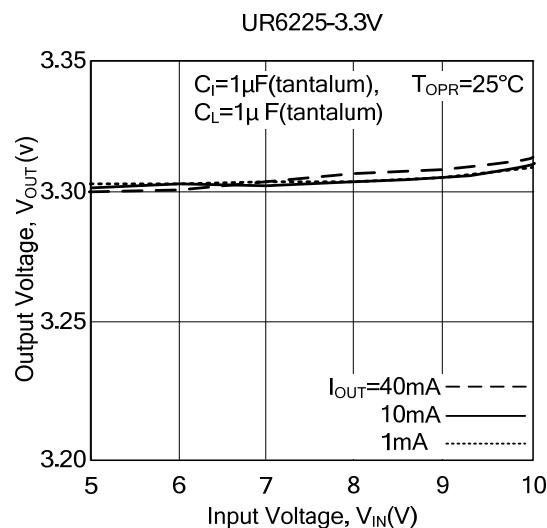
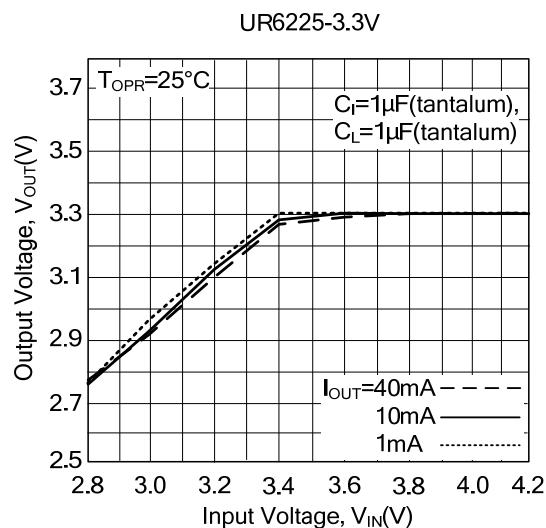
(1) OUTPUT VOLTAGE VS. OUTPUT CURRENT



(2) OUTPUT VOLTAGE VS. INPUT VOLTAGE

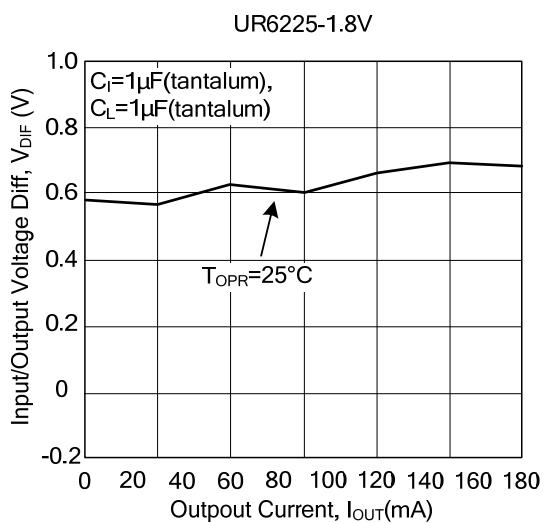
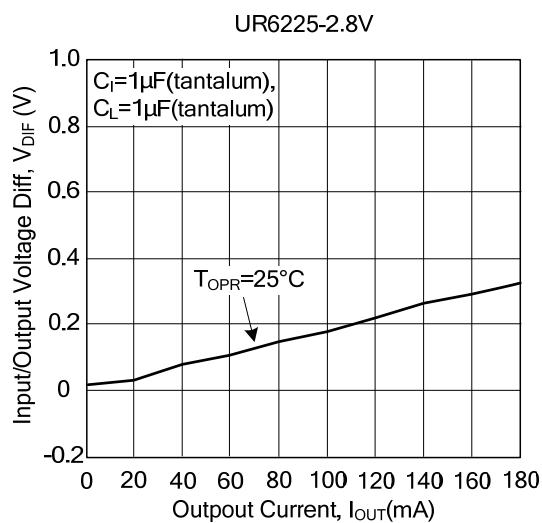
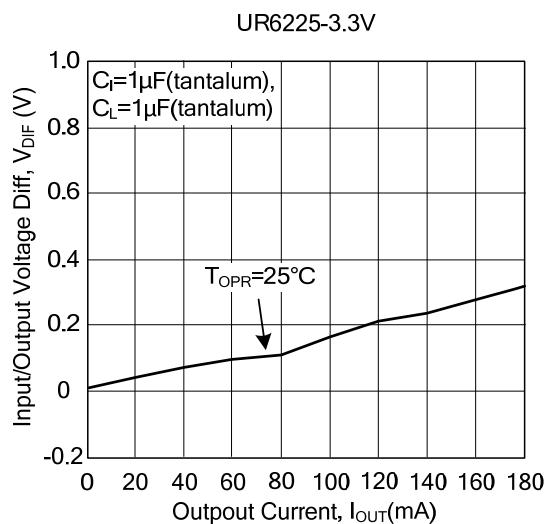
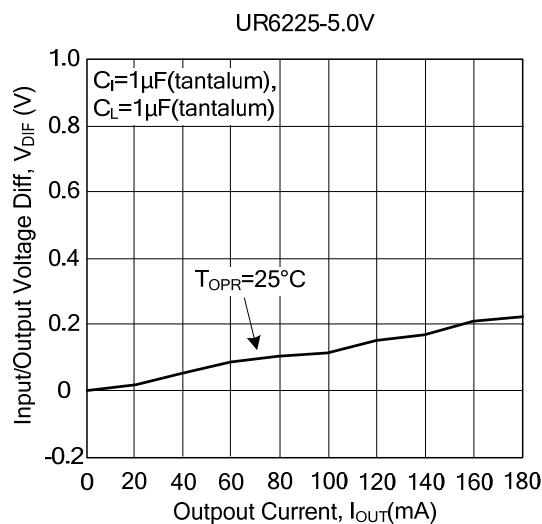


■ TYPICAL CHARACTERISTIC (Cont.)

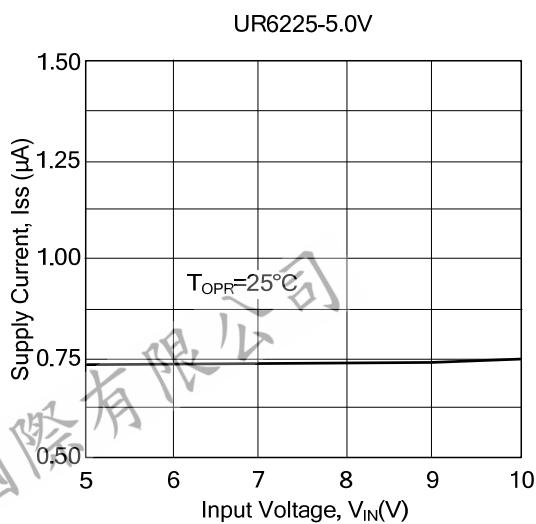
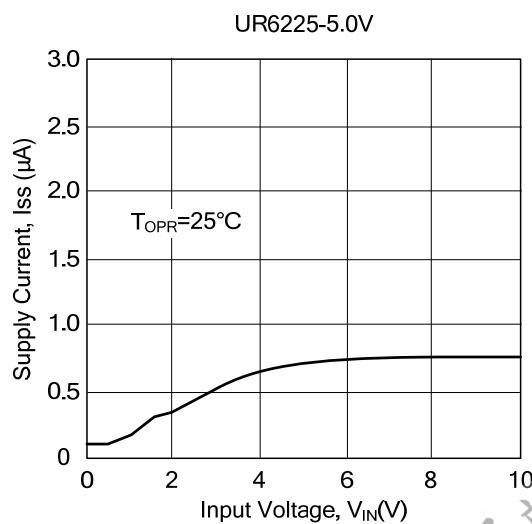


■ TYPICAL CHARACTERISTIC (Cont.)

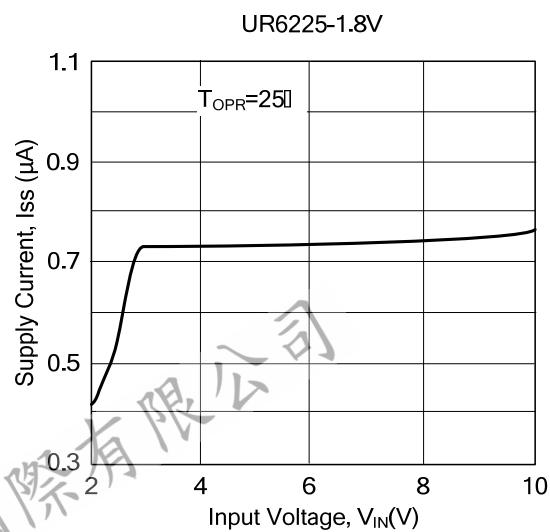
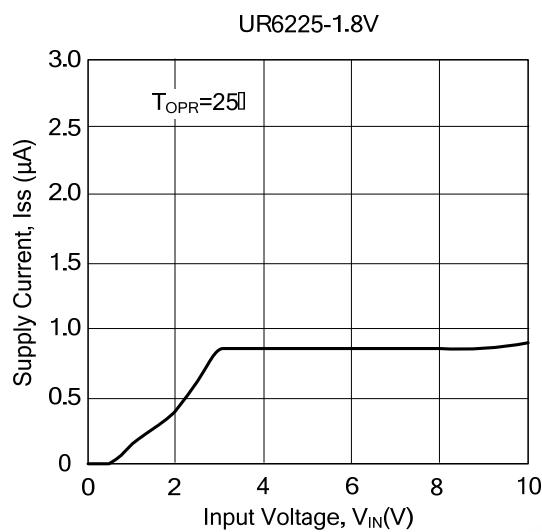
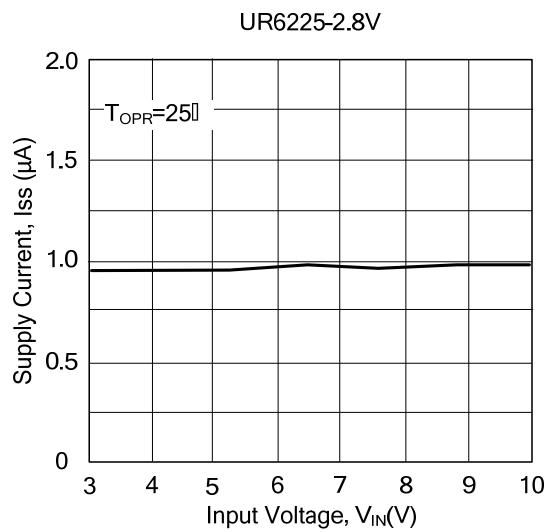
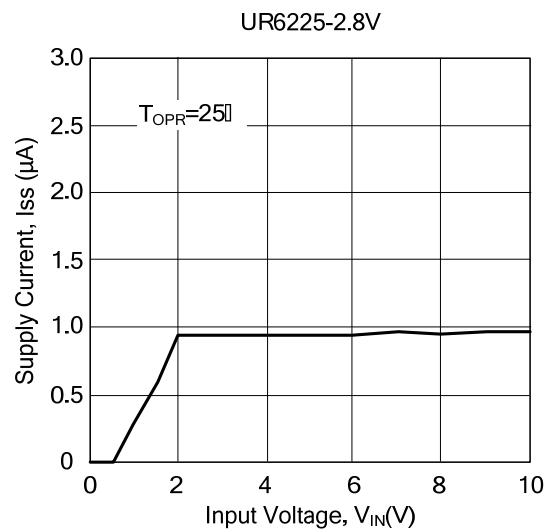
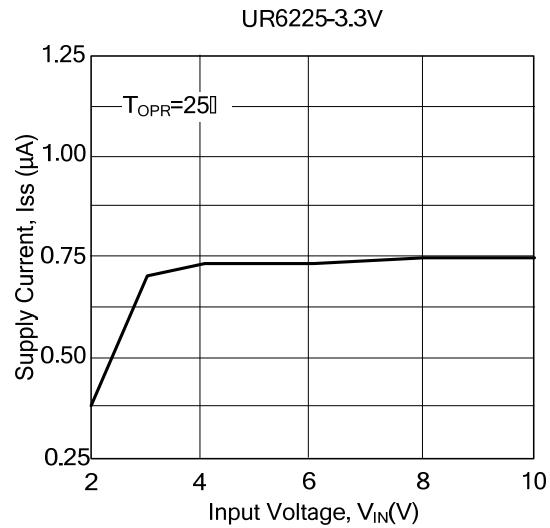
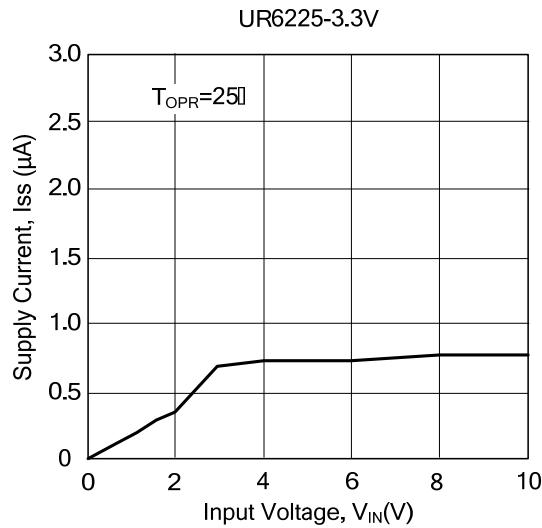
(3) INPUT/OUTPUT VOLTAGE DIFFERENTIAL VS. OUTPUT CURRENT



(4) SUPPLY CURRENT VS. INPUT VOLTAGE

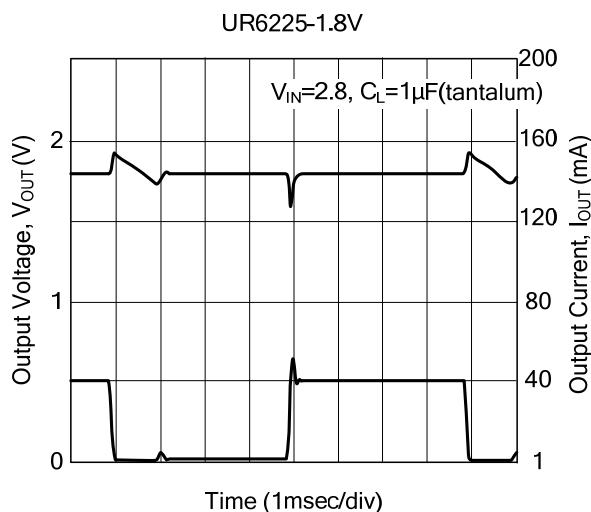
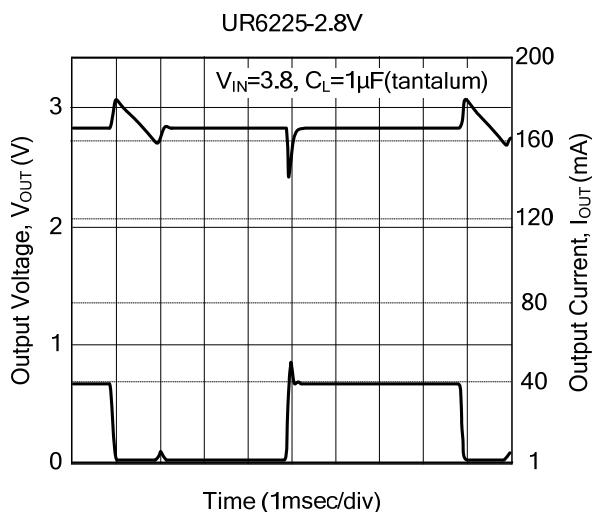
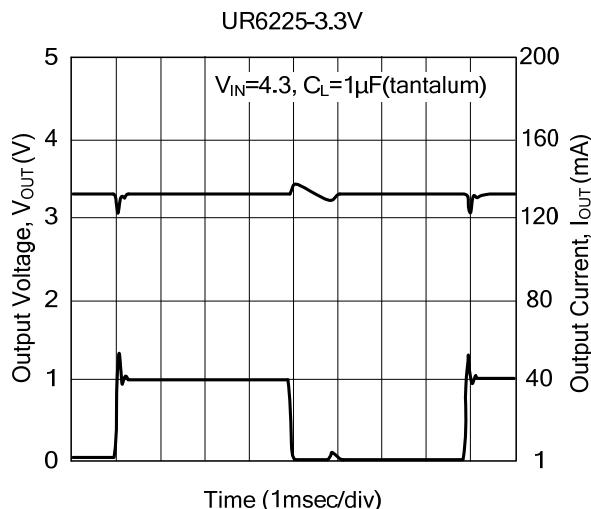
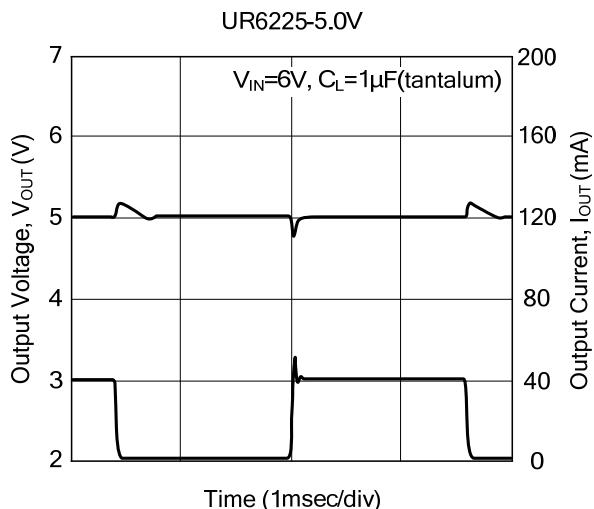


■ TYPICAL CHARACTERISTIC (Cont.)



■ TYPICAL CHARACTERISTIC (Cont.)

(5) LOAD TRANSIENT RESPONSE



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