# **USS4350**

#### NPN SILICON TRANSISTOR

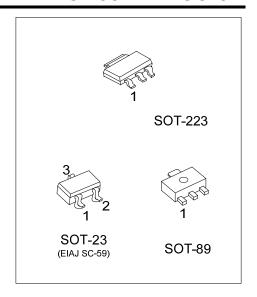
# 50V, 3A NPN LOW $V_{CE(SAT)}$ TRANSISTOR

#### DESCRIPTION

The **UTC USS4350** is a low  $V_{CE\,(SAT)}$  NPN transistor designed for applications, such as: DC/DC converter, supply line switching, battery charger, linear voltage regulation, driver in low supply voltage applications and inductive load driver.

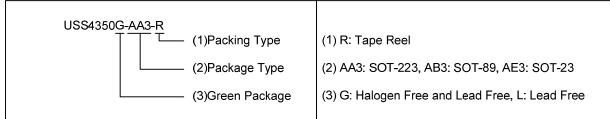
#### **■ FEATURES**

- \* Collector-emitter saturation voltage:50V
- \* High collector current gain (hFE) under high I<sub>C</sub> conditions
- \* High collector current capability
- \* Higher efficiency resulting in less heat generation
- \* Complementary to UTC USS5350

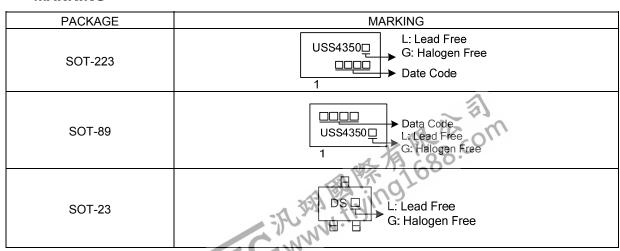


#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
USS4350L-AA3-R	USS4350G-AA3-R	SOT-223	В	С	Е	Tape Reel	
USS4350L-AB3-R	USS4350G-AB3-R	SOT-89	В	С	Е	Tape Reel	
USS4350L-AE3-R	USS4350G-AE3-R	SOT-23	В	E	С	Tape Reel	



#### MARKING



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### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

			1		
PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Base Voltage		$V_{CBO}$	60	V	
Collector-Emitter Voltage		$V_{CEO}$	50	V	
Emitter-Base Voltage		$V_{EBO}$	6	V	
Collector Current	DC	Ic	3	Α	
	Peak	I <sub>CM</sub>	5	Α	
Peak Base Current		I <sub>BM</sub>	1	Α	
Power Dissipation (T <sub>C</sub> =25°C) (Note 2)	SOT-89		1.4		
	SOT-223	$P_{D}$	2	W	
	SOT-23		0.35		
unction Temperature		TJ	150	°C	
Operating Temperature		T <sub>OPR</sub>	-65 ~ +150	°C	
Storage Temperature		T <sub>STG</sub>	-65 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient (Note)	SOT-89		90	°C/W	
	SOT-223	$\theta_{JA}$	62.5		
	SOT-23		357.1		

Note: Device mounted on FR-4 substrate P<sub>C</sub> board, 2oz copper, with 1inch square copper plate.

#### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> =50 V, I <sub>E</sub> =0			100	nA
Emitter Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> =5 V, I <sub>C</sub> =0			100	nA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =500 mA, I <sub>B</sub> =50 mA			90	mV
		I <sub>C</sub> =1 A, I <sub>B</sub> =50 mA			170	mV
		I <sub>C</sub> =2 A, I <sub>B</sub> =200 mA (Note)			290	mV
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	I <sub>C</sub> =2 A, I <sub>B</sub> =200 mA (Note)			1.2	V
Base-Emitter Turn-On Voltage	$V_{BE(ON)}$	V <sub>CE</sub> =2V; I <sub>C</sub> = 1 A (Note)			1.1	V
DC Current Gain	h <sub>FE1</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =500 mA	200			
	h <sub>FE2</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =1 A (Note)	200			
	h <sub>FE3</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =2 A (Note)	100			
Equivalent On-Resistance	R <sub>CE(SAT)</sub>	I <sub>C</sub> =2 A, I <sub>B</sub> =200 mA (Note)		110	<145	mΩ
Transition Frequency	f <sub>T</sub>	I <sub>C</sub> =100 mA, V <sub>CE</sub> =5 V, f=100 MHz	100			MHz
Collector Capacitance	Cc	$V_{CB}$ =10 V; $I_{E}$ = $I_{e}$ = 0; f =1 MHz			30	pF

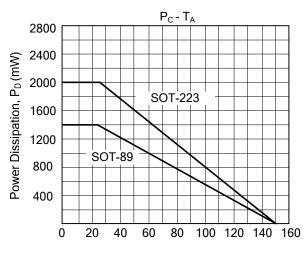
Note: Pulse test: tp ≤300 µs; Duty cycle≤2%.





<sup>2.</sup> Device mounted on a printed-circuit board; single sided copper; tinplated; mounting pad for collector 6 cm<sup>2</sup>

## **■ TYPICAL CHARACTERISTICE**



Ambient Temperature, T<sub>A</sub>(°C)

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