



UT1N10

Power MOSFET

1A, 100V N-CHANNEL ENHANCEMENT MODE POWER MOSFET

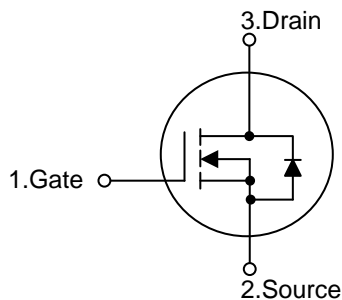
DESCRIPTION

The UTC **UT1N10** is a N-channel power MOSFET providing very low on-resistance. It has high efficiency and perfect cost-effectiveness. It can be generally applied in the commercial and industrial fields.

FEATURES

- * $R_{DS(ON)} \leq 0.5 \Omega$ @ $V_{GS}=10V, I_D=0.5A$
- * $R_{DS(ON)} \leq 0.55 \Omega$ @ $V_{GS}=4.5V, I_D=0.5A$
- * Simple drive requirement

SYMBOL



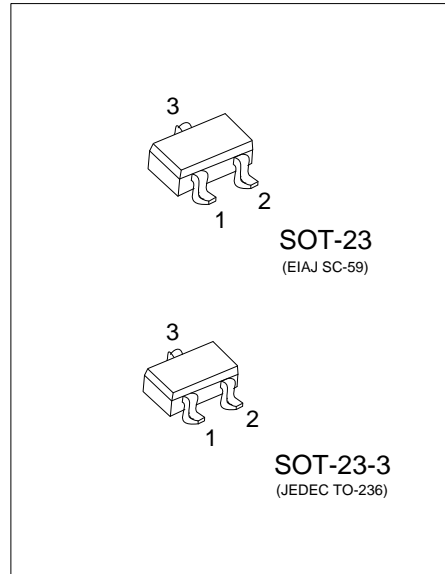
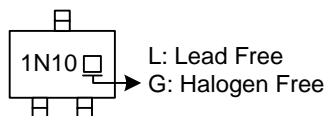
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT1N10L-AE2-R	UT1N10G-AE2-R	SOT-23-3	G	S	D	Tape Reel
UT1N10L-AE3-R	UT1N10G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UT1N10G-AE2-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AE2: SOT-23-3, AE3: SOT-23</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATING (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	100	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous	I _D	1	A
	Pulsed (Note 2)	I _{DM}	2	A
Avalanche Energy (Note 3)	Single Pulsed (Note 3)	E _{AS}	0.1	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.1	V/ns
Power Dissipation		P _D	0.3	W
Junction Temperature		T _J	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.1mH, I_{AS} = 1.2A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C.

4. I_{SD} ≤ 1.0A, di/dt ≤ 100 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J = 25°C.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	416	°C/W
Junction to Case	θ _{JC}	250 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

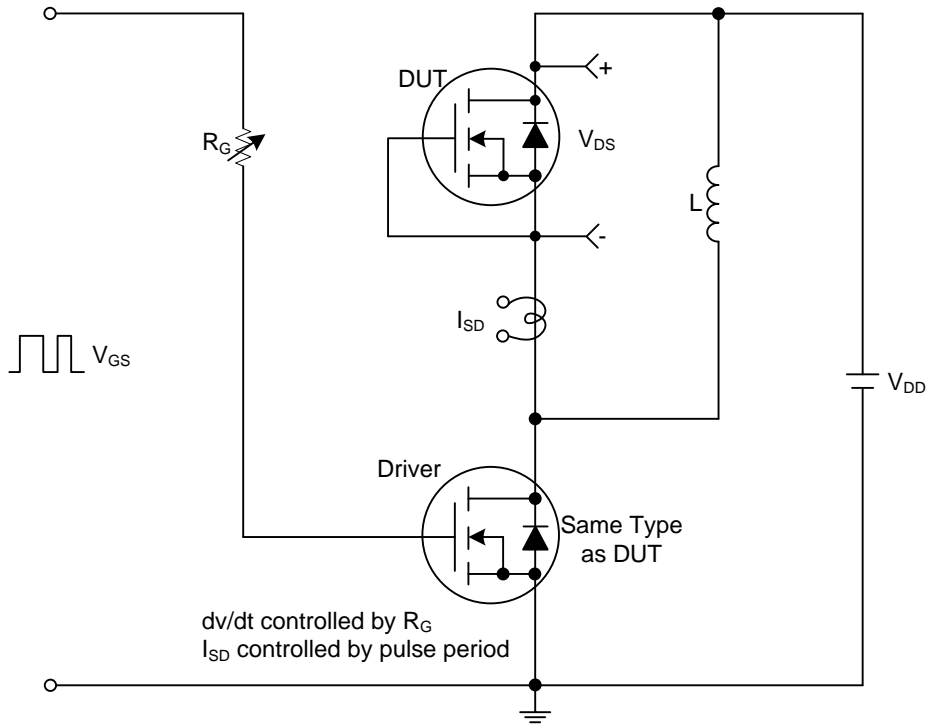
■ **ELECTRICAL CHARACTERISTICS** ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5		1.5	V
Drain to Source On-state Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=0.5A$			0.5	Ω
		$V_{GS}=4.5V, I_D=0.5A$			0.55	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$		120		pF
Output Capacitance	C_{OSS}			17		pF
Reverse Transfer Capacitance	C_{RSS}			12		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=80V, V_{GS}=10V, I_D=1A, I_G=1mA$ (Note 1, 2)		5.8		nC
Gate Source Charge	Q_{GS}			1.4		nC
Gate Drain Charge	Q_{GD}			1		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=50V, V_{GS}=10V, I_D=1A, R_G=25\Omega$ (Note 1, 2)		3		ns
Turn-ON Rise Time	t_R			14.5		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			20		ns
Turn-OFF Fall-Time	t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				1	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				2	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=1A, V_{GS}=0V$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$I_S=1A, V_{GS}=0V, di/dt=100A/\mu s$		42		ns
Reverse Recovery Charge	Q_{rr}				37	

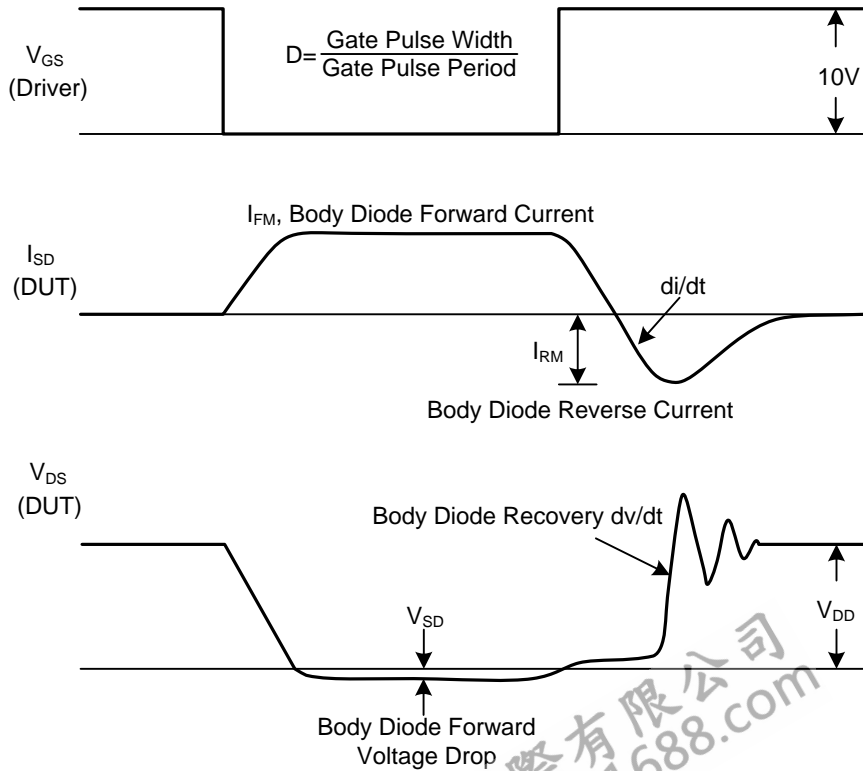
Notes: 1. Pulse Test : Pulse width $\leq 1000\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS



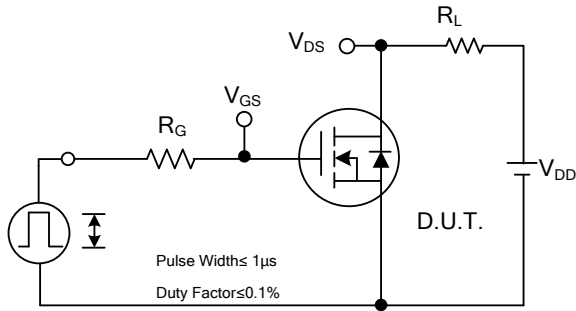
Peak Diode Recovery dv/dt Test Circuit



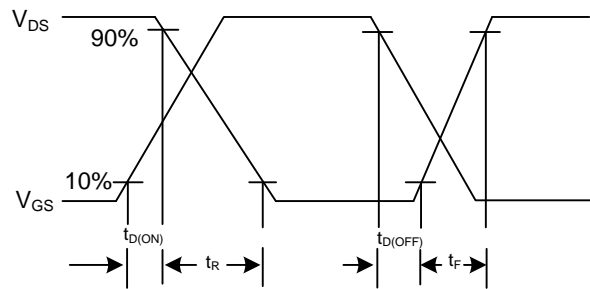
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

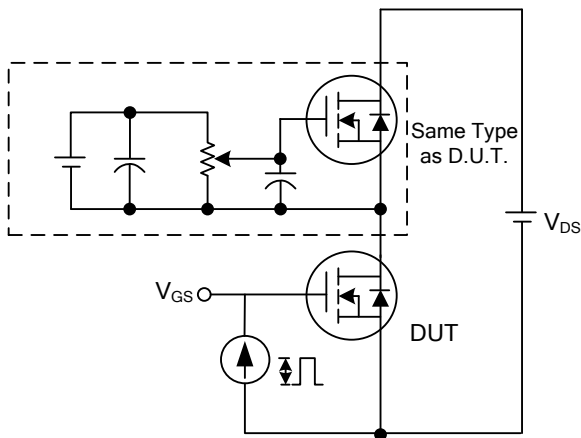
TEST CIRCUITS AND WAVEFORMS



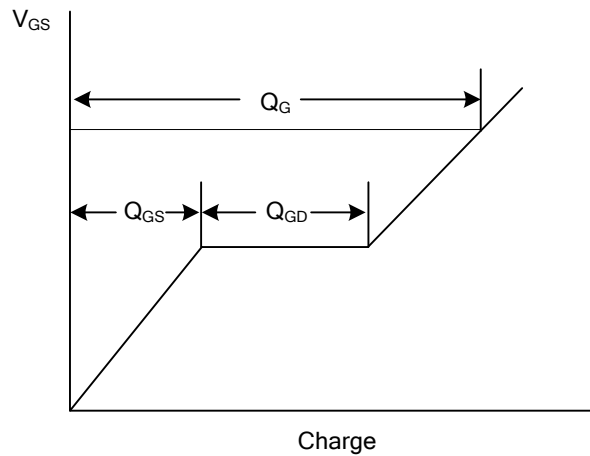
Switching Test Circuit



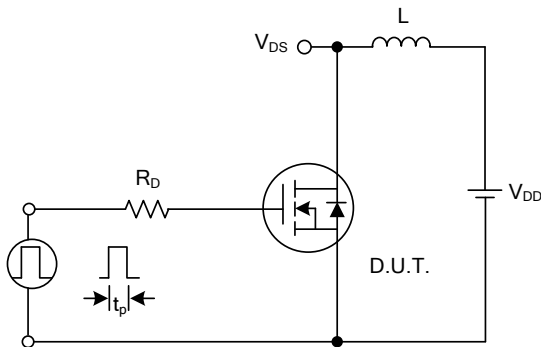
Switching Waveforms



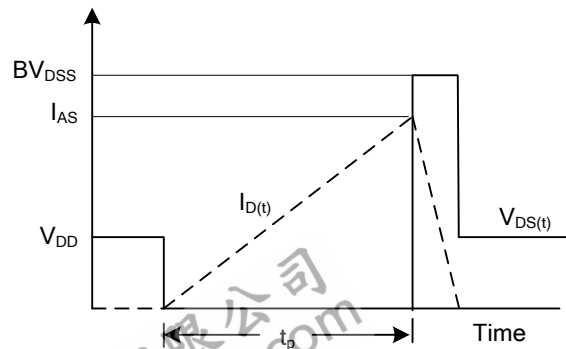
Gate Charge Test Circuit



Gate Charge Waveform

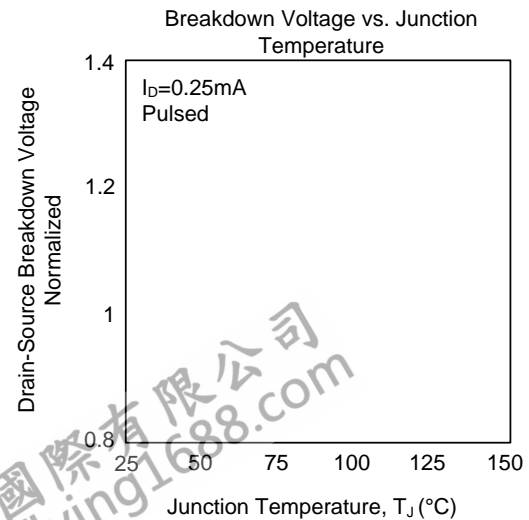
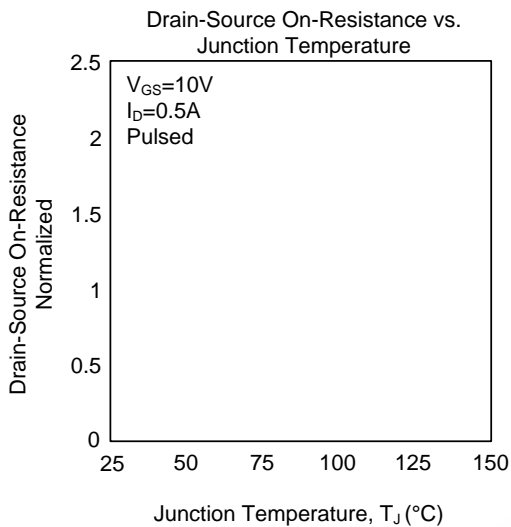
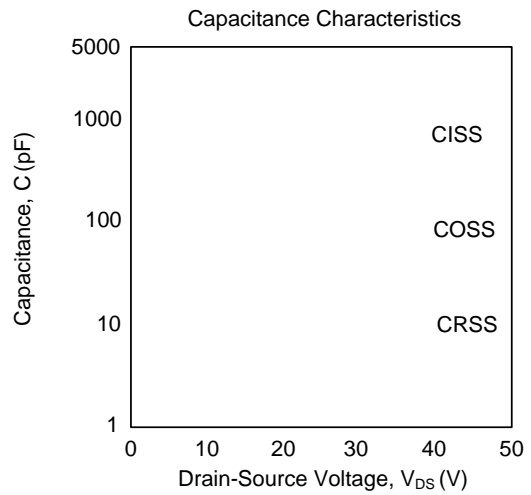
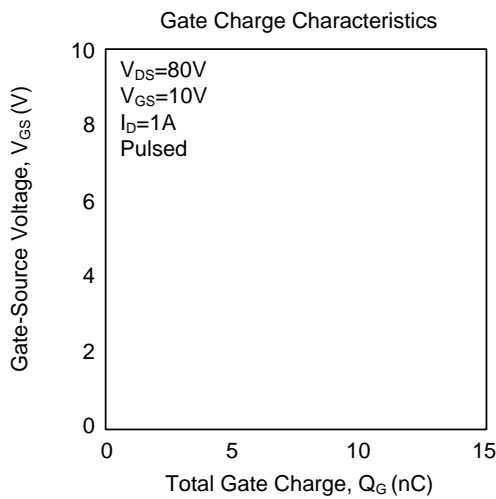
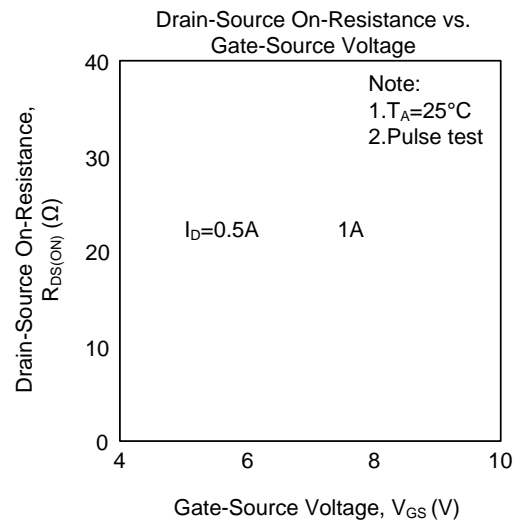
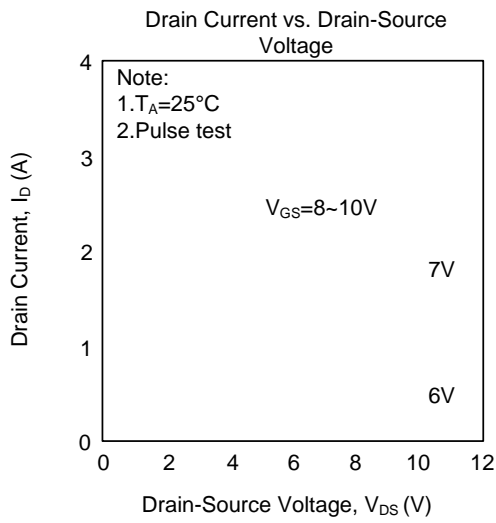


Unclamped Inductive Switching Test Circuit

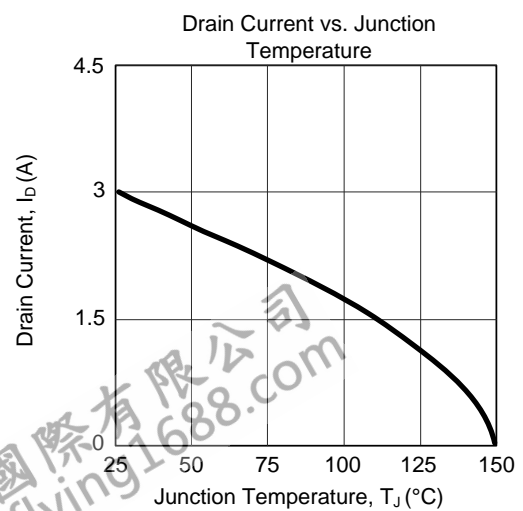
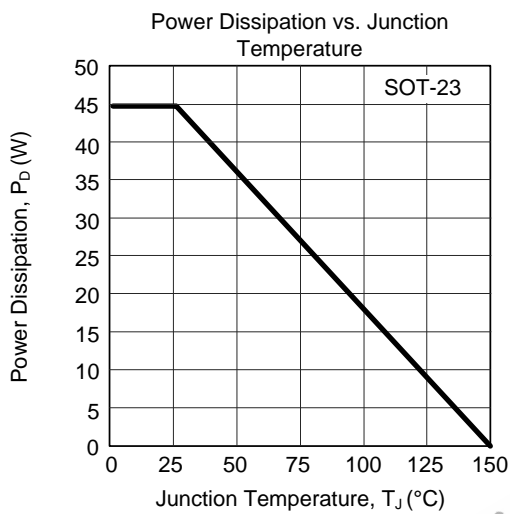
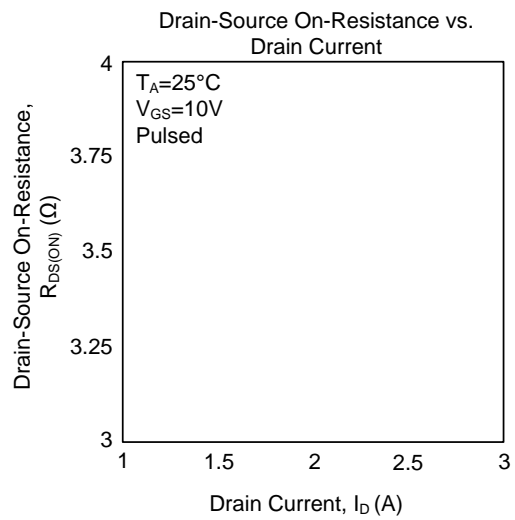
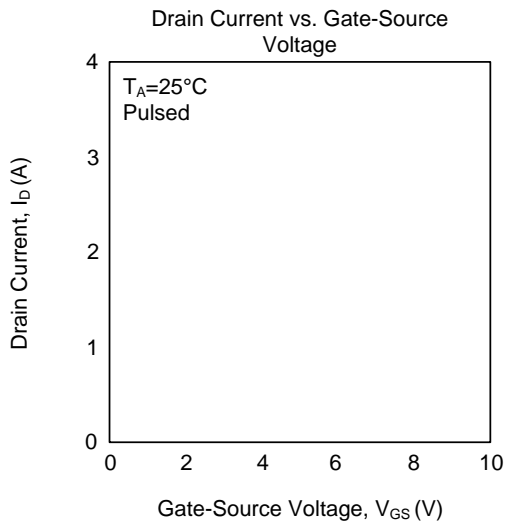
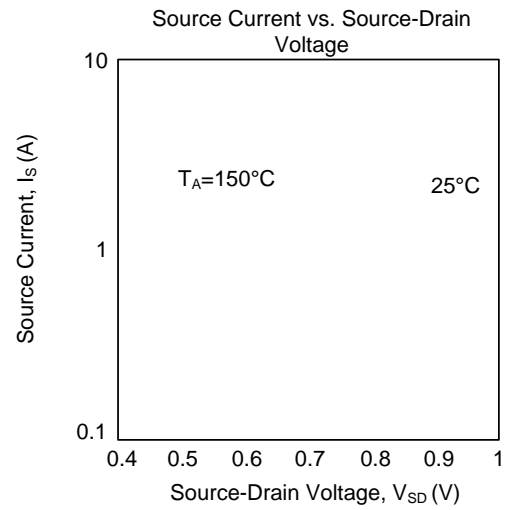
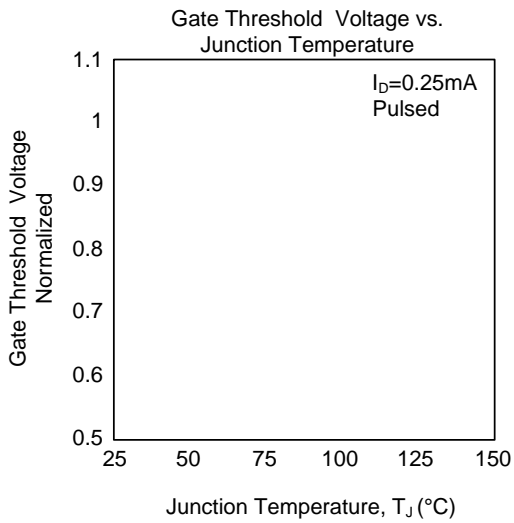


Unclamped Inductive Switching Waveforms

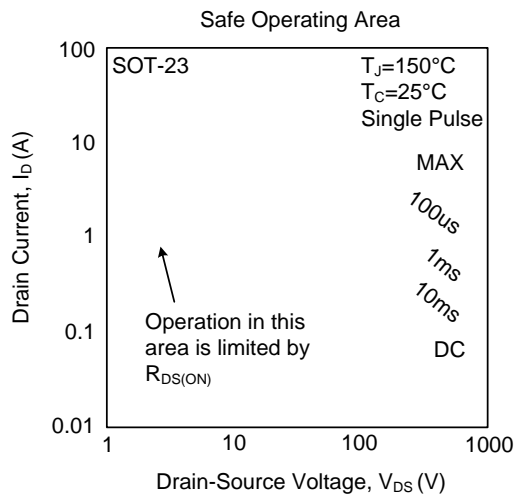
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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