

UNISONIC TECHNOLOGIES CO., LTD

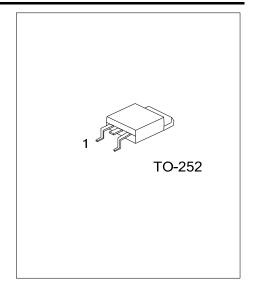
UT3009 Preliminary Power MOSFET

30V, 78A N-CHANNEL FAST SWITCHING POWER MOSFETS

■ DESCRIPTION

The UTC **UT3009** is an N-channel enhancement power MOSFET using UTC's advanced technology to provide the customers with perfect $R_{\rm DS(ON)}$, low gate charge, ultra high cell density and high switching speed.

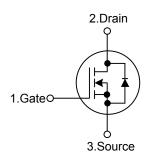
This UTC **UT3009** is suitable for most of the synchronous buck converter applications, etc.



■ FEATURES

- * $R_{DS(ON)}$ =5.5m Ω @ V_{DSS} =30V, I_D =78A
- * High Switching Speed
- * Low Gate Charge(typical 20.8nC)

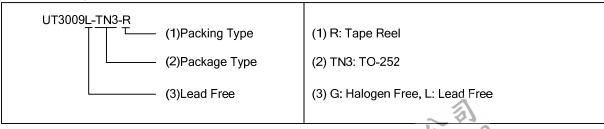
■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Dookone	Pin Assignment			Deekine	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT3009L-TN3-R	UT3009G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	30	٧
Gate-Source Voltage		V _{GSS} ±20		V
Drain Current	Continuous T _C =25°C		78	Α
	V _{GS} @10V (Note 1) T _C =100°C	I _D	55	Α
	Pulsed (Note 2)	I _{DM}	155	Α
Avalanche Current		I _{AR}	48	Α
Single Pulsed Avalanche Energy (Note 3)		E _{AS}	252	mJ
Power Dissipation (T _C =25°C) (Note 4)		P_D	53	W
Junction Temperature		T_J	-55~175	°C
Storage Temperature		T _{STG}	-55~175	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Note 1)	θ_{JA}	62	°C/W
Junction to Case (Note 1)	θ_{JC}	2.8	°C/W

Notes: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

- 2. The data tested by pulsed, pulse width ≤ 300µs, duty cycle ≤ 2%.
- 3. The EAS data shows Max. rating. The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =48A.
- 4. The power dissipation is limited by 175°C junction temperature.



■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified.)

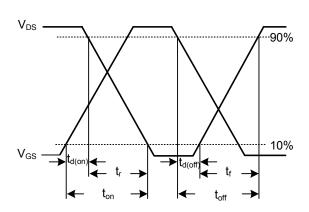
PARAMETER	SYMBOL	TEST CONDITIONS MI		TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	30			V	
Breakdown Voltage Temperature Coeffici	ent ∆BV _{DSS} /∆T _J	Reference to 25°C, I _D =1mA		96.4		mV/°C	
Drain-Source Leakage Current	I _{DSS}	$V_{DS} = 24V, V_{GS} = 0V$ $\frac{T_J = 25^{\circ}C}{T_J = 55^{\circ}C}$			1 5	μΑ	
Gate- Source Leakage Current Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA	
ON CHARACTERISTICS		V _{GS} =-20V, V _{DS} =0V			-100	nA	
Gate Threshold Voltage	V	$V_{GS(TH)}$ $V_{DS}=V_{GS}$, $I_D=250\mu A$		1.5	2.5	V	
V _{GS(th)} Temperature Coefficient				-6.16	2.0	mV/°C	
Static Drain-Source On-State Resistance	△ V GS(TH)	V _{GS} =10V, I _D =30A		4.7	5.5	mΩ	
(Note 2)	R _{DS(ON)}	V_{GS} =4.5V, I_{D} =15A		7.5	9	mΩ	
Forward Transconductance	g FS	V_{DS} =5V, I_{D} =30A		22	-	S	
DYNAMIC PARAMETERS	y F5	V DS OV, 10 OOM					
Input Capacitance	C _{ISS}			2361		pF	
Output Capacitance	C _{OSS}	V _{GS} =0V, V _{DS} =15V,		315		pF	
Reverse Transfer Capacitance	C _{RSS}	f=1.0MHz		237		pF	
SWITCHING PARAMETERS	1.00			ı			
Total Gate Charge (4.5V)	Q_{G}	V 45V V 00V		20.8		nC	
Gate to Source Charge	Q_{GS}	V _{GS} =4.5V, V _{DS} =20V, I _D =12A		5.3		nC	
Gate to Drain Charge	Q_GD	1D-12A		10.5		nC	
Gate Resistance	R_{G}	V_{GS} =0V, V_{DS} =0V, f=1.0MHz		1.7	3.4	Ω	
Turn-ON Delay Time	$t_{D(ON)}$		7.2	9	13.5	ns	
Rise Time	t _R	V_{DD} =12V, V_{GS} =10V, I_{D} =5A,	17.3	21.6	32.4	ns	
Turn-OFF Delay Time	t _{D(OFF)}	$R_G=3.3\Omega$	21.3	26.6	40	ns	
Fall-Time	t _F		8.4	10.5	15.8	ns	
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERIS	TICS					
Maximum Body-Diode Continuous	Is				78	Α	
Current (Note 1,4)	15	V _D =V _G =0V, Force Current			'	,,	
Maximum Body-Diode Pulsed Current (Note 2, 4)	I _{SM}	To the over the content			155	Α	
Drain-Source Diode Forward Voltage (Note 2)	V _{SD}	I _S =1A, V _{GS} =0V, T _J =25°C			1	V	
Single Pulse Avalanche Energy (Note 3)	Eas	V _{DD} =25V, L=0.1mH, I _{AS} =24A	63			mJ	

Notes: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

- 2. The data tested by pulsed, pulse width \leq 300 μ s, duty cycle \leq 2%.
- 3. The Min. value is 100% EAS tested guarantee.
- 4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



■ TEST CIRCUITS AND WAVEFORMS



Switching Time Waveform

$$E_{AS} = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS}} V_{DD}$$

$$BV_{DSS} V_{DD}$$

$$V_{DD}$$

Unclamped Inductive Switching Wave

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