

UT3400

5.8A, 30V N-CHANNEL ENHANCEMENT MODE POWER MOSFET

DESCRIPTION

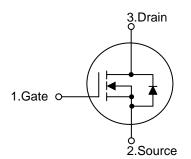
The UTC **UT3400** is an N-ch enhancement MOSFET providing the customers with perfect $R_{DS(ON)}$ and low gate charge. This device can be operated with 2.5V low gate voltage.

The UTC **UT3400** is optimized for applications, such as a load switch or in PWM.

FEATURES

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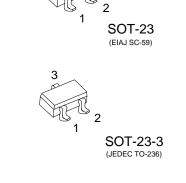
SYMBOL



ORDERING INFORMATION

Ordering Number			Pin Assignment				
Lead Free	Halogen Free	Package 1 2		3	Packing		
UT3400L-AE2-R UT3400G-AE2-R		SOT-23-3	G	S	D	Tape Reel	
UT3400L-AE3-R	UT3400L-AE3-R UT3400G-AE3-R		G	S	D	Tape Reel	
Note: Pin Assignment: G: Gate S: Source D: Drain							
UT3400 <u>G</u> - <u>AE2</u> -R	 (1) R: Tape Reel (2) AE2: SOT-23-3, AE3: SOT-23 (3) G: Halogen Free and Lead Free, L: Lead Free 						
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ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current	Ι _D	5.8	A
Pulsed Drain Current (Note 2)	I _{DM}	30	A
Power Dissipation	PD	1.4	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. Pulse width \leq 300µs, duty cycle \leq 0.5%.

THERMAL DATA

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Junction to Ambient (Note)	θ _{JA}		85	125	°C/W

Note: Surface mounted on 1 in² copper pad of FR4 board with 2oz.

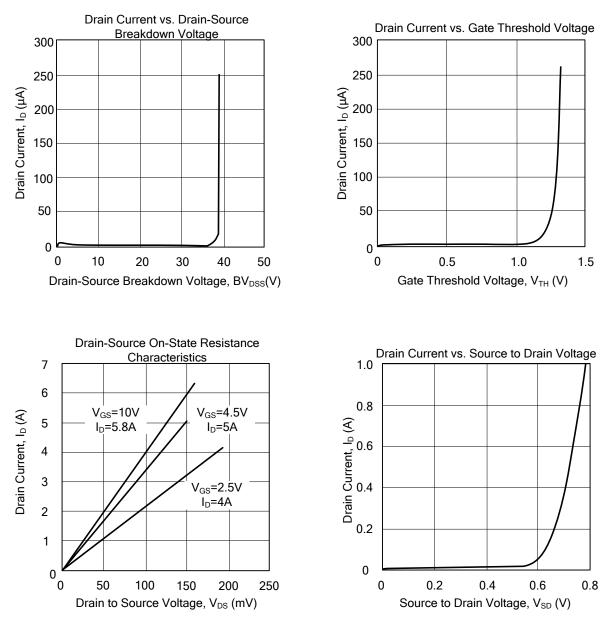
ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		·				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250µA	30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =24V,V _{GS} =0V			1	μA
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$			100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA	0.7	1.1	1.4	V
On-State Drain Current	I _{D(ON)}	V _{DS} =5V, V _{GS} =4.5V	30			Α
		V _{GS} =10V, I _D =5.8A		22.8	28	mΩ
Drain to Source On-state Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =5A		27.3	33	mΩ
	-(-)	V _{GS} =2.5V, I _D =4 A		43.3	52	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	CISS			823		рF
Output Capacitance	Coss	V _{DS} =15V, V _{GS} =0V, f =1MHz		99		pF
Reverse Transfer Capacitance	C _{RSS}			77		pF
Gate Resistance	R _G	$V_{GS}=0V, V_{DS}=0V, f=1MHz$		1.2		Ω
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	V _{GS} =4.5V, V _{DS} =15V, I _D =5.8A		9.7		nC
Gate Source Charge	Q_{GS}			1.6		nC
Gate Drain Charge	Q_{GD}			3.1		nC
Turn-ON Delay Time	t _{D(ON)}			5.5		ns
Turn-ON Rise Time	t _R	$V_{GS} = 10V, V_{DS} = 15V$ $R_L = 2.7\Omega, R_{GEN} = 6\Omega$		5.1		ns
Turn-OFF Delay Time	t _{D(OFF)}			37		ns
Turn-OFF Fall-Time	t _F			4.2		ns
SOURCE- DRAIN DIODE RATINGS AND CH	ARACTERI	STICS				
Diode Continuous Forward Current (Note 1)	Is	A. V.	3		2.5	Α
Drain-Source Diode Forward Voltage	V_{SD}	I _S =1A, V _{GS} =0V	*	0.71	1	V
Reverse Recovery Time	t _{rr}	4 18 , 90.		16		ns
Reverse Recovery Charge	Qrr	−I _F =5A, dl/dt=100A/μs		8.9		nC
Notes: 1. Repetitive Rating : Pulse width limite	d by maximu	im junction temperature.				
2. Pulse width ≤300µs, duty cycle≤0.5	% JC 34	WEW				
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UT3400





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