

UT40N06 Advance Power MOSFET

40A, 60V N-CHANNEL POWEF MOSFET

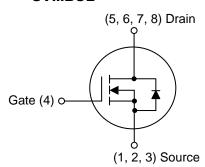
■ DESCRIPTION

The UTC **UT40N06** is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

- * $R_{DS(ON)} \le 17 \text{ m}\Omega$ @ $V_{GS}=10V$, $I_D=20A$ $R_{DS(ON)} \le 30 \text{ m}\Omega$ @ $V_{GS}=4.5V$, $I_D=20A$
- * Low on-Resistance
- * Fast Switching Speed

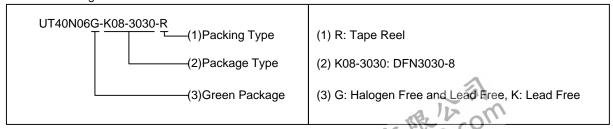




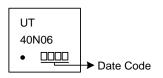
■ ORDERING INFORMATION

Ordering Number		Doolsons	Pin Assignment							Darakinan		
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing	
UT40N06L-K08-3030-R	UT40N06G-K08-3030-R	DFN3030-8	S	S	S	G	D	D	D	D	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



MARKING



1 trees | DFN3030-8

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■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C Unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I _D	40	Α
Pulsed Drain Current (Note 1)	I _{DM}	80	Α
Avalanche Energy	E _{AS}	8	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	2.3	V/ns
Power Dissipation	P_{D}	60	W
Operating Junction Temperature	T_J	-55 ~ + 150	°C
Storage Temperature	T _{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=0.1mH, I_{AS} =12.7A, V_{DD} =20V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 40A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	41.6	°C/W	
Junction to Case	θ _{JC}	2.08	°C/W	

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

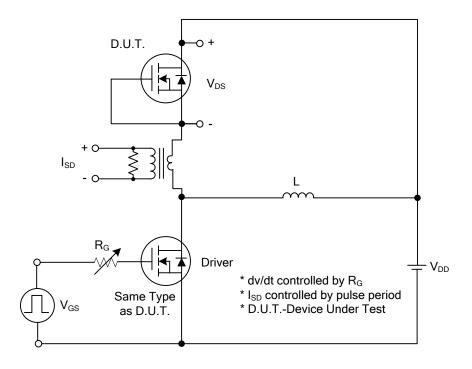
■ **ELECTRICAL CHARACTERISTICS** (T_J =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV _{DSS}	$I_D=250\mu A, V_{GS}=0V$	60			V		
Drain-Source Leakage Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			10	μΑ		
Gate- Source Leakage Current	I _{GSS}	V _{GS} =±20V			±100	nA		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0		3.0	V		
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A			17	mΩ		
(Note 1)		V _{GS} =4.5V, I _D =20A			30	mΩ		
DYNAMIC PARAMETERS								
Input Capacitance	C _{ISS}			1950		pF		
Output Capacitance	Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		180		pF		
Reverse Transfer Capacitance	C_{RSS}			140		pF		
SWITCHING PARAMETERS (Note 2)								
Total Gate Charge	Q_G	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		22		nC		
Gate to Source Charge	Q_GS	V _{DS} =48V, V _{GS} =4.5V, I _D =40A,		6		nC		
Gate to Drain Charge	Q_GD	I _G =1mA (Note1,2)		10		nC		
Turn-ON Delay Time	$t_{D(ON)}$			8		ns		
Rise Time	t_R	V _{DS} =30V, V _{GS} =10V, I _D =40A,		16		ns		
Turn-OFF Delay Time	t _{D(OFF)}	R _G =3Ω (Note1,2)		32		ns		
Fall-Time	t _F			18		ns		
SOURCE- DRAIN DIODE RATINGS AND (CHARACTER	RISTICS						
Maximum Continuous Drain-Source Diode	I.				40	Α		
Forward Current	Is	~ 0.	-0-		40	А		
Maximum Pulsed Drain-Source Diode	Levi	WR O	(1)		80	Α		
Forward Current	I _{SM}	1000			00	^		
Drain-Source Diode Forward Voltage	V_{SD}	I _F =40A, V _{GS} =0V			1.3	V		
Reverse Recovery Time	t _{rr}	/ _{Is} =30A , V _{GS} =0V di/dt=100A/µs		40		ns		
Reverse Recovery Charge	Qrr	is—soλ, ves=ov αι/αι–rooλ/μs	68		nC			

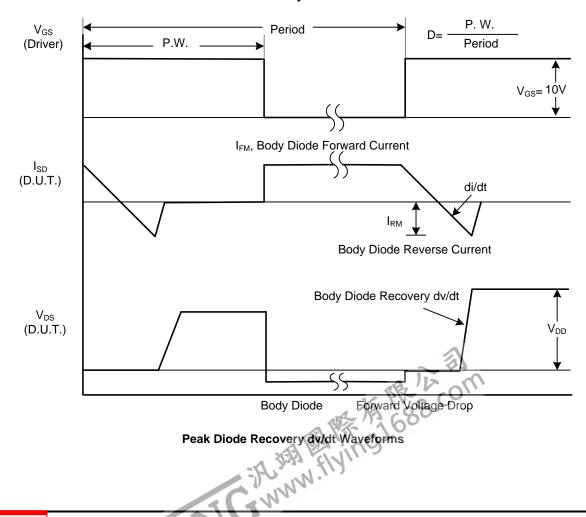
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤2%.

^{2.} Essentially independent of operating temperature.

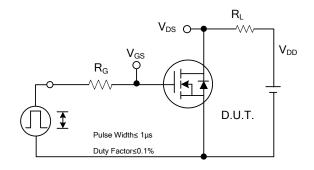
TEST CIRCUITS AND WAVEFORMS

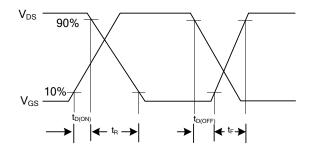


Peak Diode Recovery dv/dt Test Circuit



TEST CIRCUITS AND WAVEFORMS

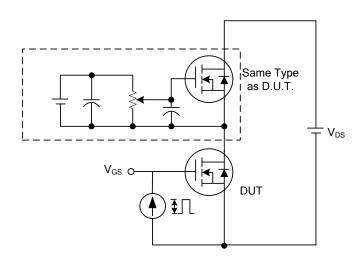


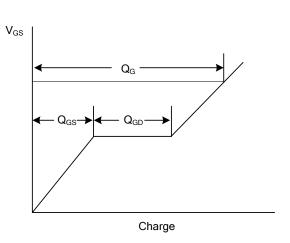


Power MOSFET

Switching Test Circuit

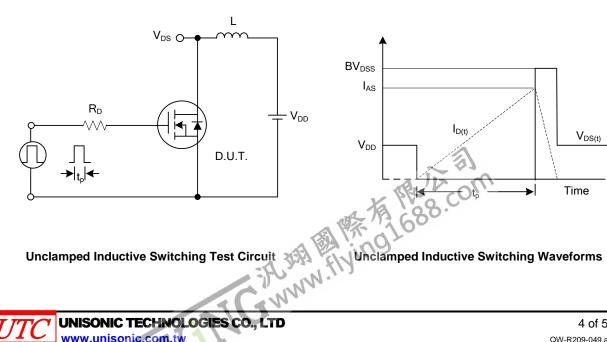
Switching Waveforms

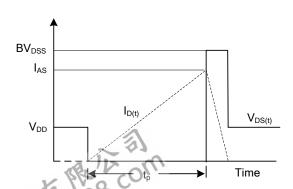




Gate Charge Test Circuit

Gate Charge Waveform





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