

UTC UNISONIC TECHNOLOGIES CO., LTD

UTT8P03

Power MOSFET

-8A, -30V **P-CHANNEL POWER MOSFET**

DESCRIPTION

The UTC UTT8P03 is a P-channel MOSFET, it uses UTC's advanced technology to provide the customers with a minimum on state resistance, etc.

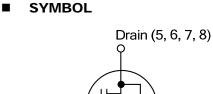
The UTC UTT8P03 is suitable for battery switches, load switches and power management, etc.

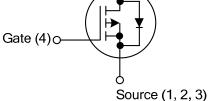
FEATURES

* $R_{DS(ON)}$ < 33 m Ω @ V_{GS} =-10V, I_D =-8.0A

 $R_{DS(ON)} < 51 \text{ m}\Omega @ V_{GS} = -4.5 \text{V}, I_D = -8.0 \text{A}$

* Low R_{DS(ON)}





TO-252

ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing	
Lead Free	Halogen Free			3	Facking		
UTT8P03L-TN3-R	UTT8P03G-TN3-R	TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: G	•				· · · · ·		
Note: Pin Assignment: G: Gate D: Drain S: Source UTT8P03G-TN3-R (1)Packing Type (1) R: Tape Reel (2)Package Type (2)Package Type (2) TN3: TO-252 (3)Green Package (3) G: Halogen Free and Lead Free, L: Lead Free						ad Free	
■ MARKING UTC UTC L: Lead Free G: Halogen Free Data Code 1 Www.unisonic.com.tw Copyright @ 2017 Unisonic Technologies Co. Ltd							
<u>www.unisonic.com.tw</u> Copyright © 2017 Unisonic Technologies Co., Ltd				1 of 5 QW-R210-063.A			
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ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V _{DSS}	-30	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current	I _D	-8	A	
Pulsed Drain Current (t=300µs)	I _{DM}	-32	A	
Avalanche Energy, Single Pulsed (Note 3)	E _{AS}	22	mJ	
Peak Diode Recovery dv/dt (Note4)	dv/dt	1.85	V/ns	
Power Dissipation	PD	41	W	
Junction Temperature	TJ	+150	°C	
Storage Temperature Range	T _{STG}	-50 ~+150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=-0.1mH, I_{AS}=-21A, V_{DD}=-25V, R_G=25Ω, Starting T_J=25°C

4. $I_{SD} \leq -8A$, di/dt $\leq 200A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^{\circ}C$

THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	60	°C/W	
Junction to Case	θ _{JC}	3.04	°C/W	

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

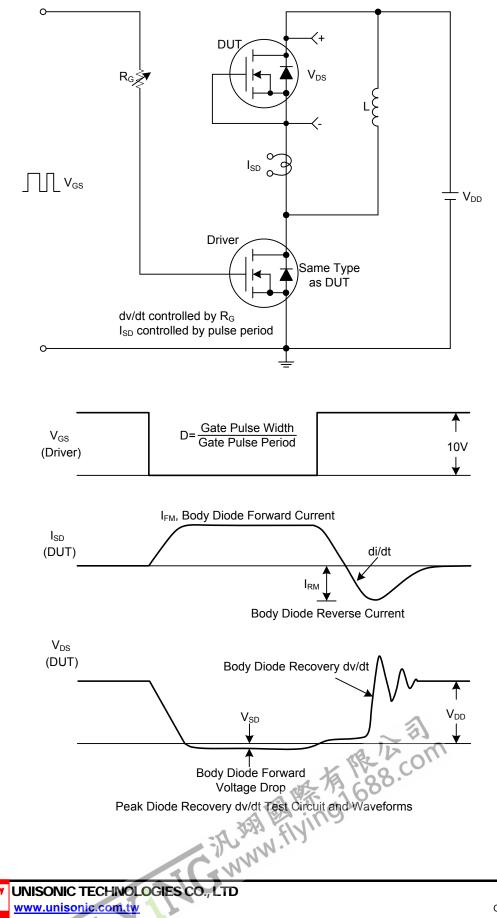
ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =-250μΑ, V _{GS} =0V	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-1	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS					_	
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-State	R _{DS(ON)}	V _{GS} =-10V, I _D =-8.0A			33	mΩ
Resistance (Note 1)		V _{GS} =-4.5V, I _D =-8.0A			51	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	CISS			1200		pF
Output Capacitance	C _{oss}	V _{GS} =0V, V _{DS} =-10V, f=1.0MHz		185		pF
Reverse Transfer Capacitance	C _{RSS}			115		pF
SWITCHING PARAMETERS				-		
Total Gate Charge (Note 1)	Q_{G}	-V _{GS} =-4.5V, V _{DD} =-24V, I _D =-8A		12.5		nC
Gate to Source Charge	Q _{GS}	$I_{G}=1$ mA (Note 1, 2)		3.8		nC
Gate to Drain Charge	Q_{GD}	I_{G} = IIIA (Note 1, 2)		5.7		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}			8		ns
Rise Time	t _R	V _{GS} =-4.5V, V _{DD} =-15V, I _D =-8A		17		ns
Turn-OFF Delay Time	t _{D(OFF)}	R _G =25Ω (Note 1, 2)		70		ns
Fall-Time	t⊨	1		41		ns
SOURCE- DRAIN DIODE RATINGS AN	D CHARACT	ERISTICS				
Continuous Source-Drain Diode Current	ls	TR. CO	D//.		-8	Α
Pulse Diode Forward Current	I _{SM}	10.08.			-32	Α
Body Diode Voltage (Note 1)	V_{SD}	Is=-8A,VGS=0V			-1.5	V
Reverse Recovery Time (Note 1)	trr	V _{GS} =0V, I _S =-8A, dI⊧/dt=-100A/µs		170		nC
Reverse Recovery Charge	Qrr	vgs−uv, isoA, ui⊧/ui100A/µs		250		nS
Notoe: 1. Pulso tost: pulso width ≤ 300 us	duty avala	20/ 11/1				

Notes: 1. Pulse test; pulse width ≤ 300µs, duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

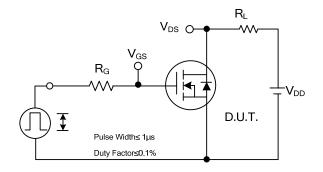
UTT8P03

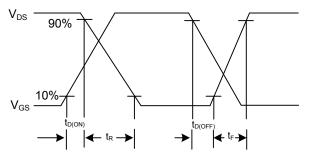
TEST CIRCUITS AND WAVEFORMS



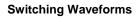
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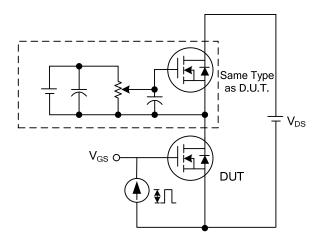
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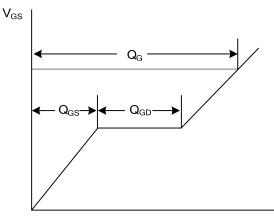






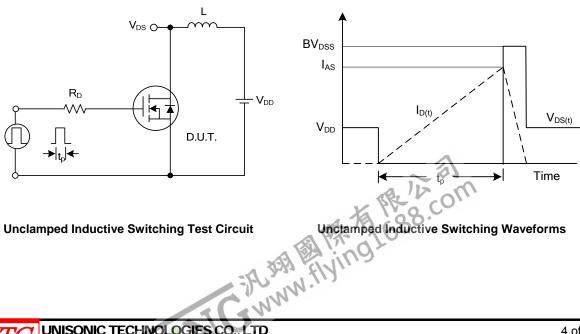


Gate Charge Test Circuit



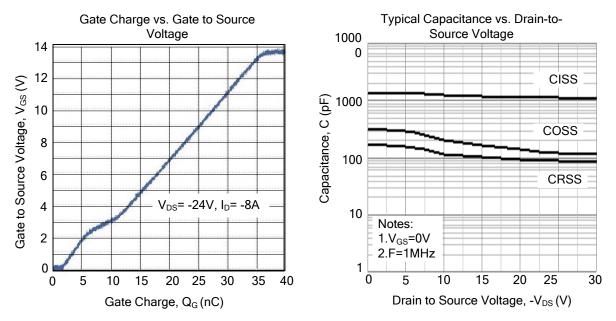
Charge





UTT8P03

TYPICAL CHARACTERISTICS



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