



## UTT9NP03

Advance

Power MOSFET

### 30V DUAL MIDDLE POWER MOSFET (N-CHANNEL/P-CHANNEL)

#### DESCRIPTION

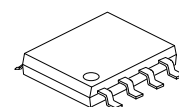
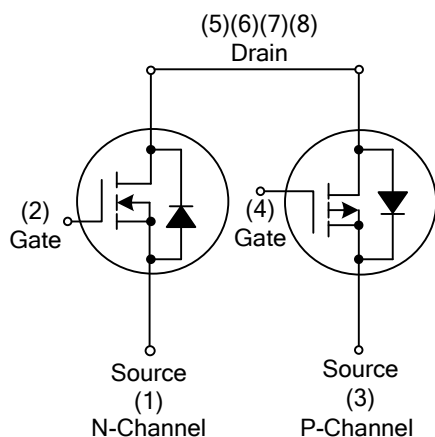
The UTC **UTT9NP03** is a 30V N-Channel & P-Channel middle Power MOSFET, it uses UTC's advanced technology to provide the customers with a minimum on state resistance, etc.

The UTC **UTT9NP03** is suitable for switching.

#### FEATURES

\* Low on-resistance

#### SYMBOL



SOP-8

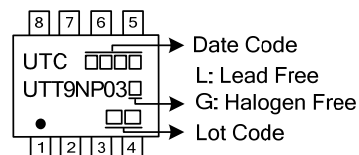
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTT9NP03L-S08-R	UTT9NP03G-S08-R	SOP-8	S	G	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT9NP03G-S08-R		(1) Packing Type	(1) R: Tape Reel
		(2) Package Type	(2) S08: SOP-8
		(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS		UNIT
			TR1	TR2	
Drain-Source Voltage		$V_{\text{DSS}}$	30	-30	V
Gate-Source Voltage		$V_{\text{GSS}}$	$\pm 20$	$\pm 20$	V
Drain Current	Continuous (Note 4)	$I_{\text{D}}$	$\pm 9.0$	$\pm 8.0$	A
	Pulsed (Note 5)	$I_{\text{DP}}$	$\pm 18$	$\pm 18$	A
Avalanche Energy, Single Pulse (Note 3)		$E_{\text{AS}}$	3.5	2.2	mJ
Avalanche Current (Note 3)		$I_{\text{AS}}$	7.0	-5.5	A
Power Dissipation	Total	$P_{\text{D}}$ (Note 4)	2.6		W
		$P_{\text{D}}$ (Note 6)	1.5		W
	Element	$P_{\text{D}}$ (Note 6)	1.25		W
Junction Temperature		$T_{\text{J}}$	150		$^{\circ}\text{C}$
Range of Storage Temperature		$T_{\text{STG}}$	-55~+150		$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by  $T_{\text{J}}$ .

3. N-CHANNEL :  $L=0.1\text{mH}$ ,  $V_{\text{DD}}=15\text{V}$ ,  $R_{\text{G}}=25\ \Omega$ , Starting  $T_{\text{J}} = 25^{\circ}\text{C}$

P-CHANNEL :  $L=0.1\text{mH}$ ,  $V_{\text{DD}}=-15\text{V}$ ,  $R_{\text{G}}=25\ \Omega$ , Starting  $T_{\text{J}} = 25^{\circ}\text{C}$

4.  $P_{\text{W}} \leq 1\text{s}$ , Limited only by maximum temperature allowed.

5.  $P_{\text{W}} \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ .

6. Mounted on a ceramic board.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Note 6)	$\theta_{\text{JA}}$	83.3	$^{\circ}\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

**N-CHANNEL**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA	1.0		2.5	V
Static Drain-Source On-State Resistance (Pulsed)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =9A		12.3	16.0	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =7A		18.2	23.7	mΩ
Transconductance (Pulsed)	g <sub>FS</sub>	V <sub>GS</sub> =5V, I <sub>D</sub> =7A	4.4			S
DYNAMIC PARAMETERS						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1.0MHz		640		pF
Output Capacitance	C <sub>OSS</sub>			110		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			90		pF
SWITCHING PARAMETERS						
Total Gate Charge (Pulsed)	Q <sub>G</sub>	V <sub>GS</sub> =4.5V, V <sub>DD</sub> =15V, I <sub>D</sub> =9A (Note 1, 2)		7.9		nC
Gate to Source Charge (Pulsed)	Q <sub>GS</sub>			3.1		nC
Gate to Drain Charge (Pulsed)	Q <sub>GD</sub>			2.8		nC
Turn-ON Delay Time (Pulsed)	t <sub>D(ON)</sub>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A, R <sub>G</sub> =10Ω, R <sub>L</sub> =3.3Ω (Note 1, 2)		8		ns
Rise Time (Pulsed)	t <sub>R</sub>			19		ns
Turn-OFF Delay Time (Pulsed)	t <sub>D(OFF)</sub>			33		ns
Fall-Time (Pulsed)	t <sub>F</sub>			7		ns
SOURCE TO DRAIN DIODE SPECIFICATIONS						
Body Diode Continuous Forward Current	I <sub>S</sub>	T <sub>A</sub> =25°C			1.0	A
Body Diode Pulse Current (Note 3)	I <sub>SP</sub>				18	A
Forward Voltage (Pulsed)	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			1.2	V

Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

3.  $P_W \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ .

## ■ ELECTRICAL CHARACTERISTICS (Cont.)

## P-CHANNEL

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-1mA	-1.0		-2.5	V
Static Drain-Source On-State Resistance (Pulsed)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A		22.0	28.6	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5.5A		31.0	40.3	mΩ
Transconductance (Pulsed)	g <sub>FS</sub>	V <sub>GS</sub> =-5V, I <sub>D</sub> =-5.5A	5.5			S
DYNAMIC PARAMETERS						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1.0MHz		890		pF
Output Capacitance	C <sub>OSS</sub>			160		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			125		pF
SWITCHING PARAMETERS						
Total Gate Charge (Pulsed)	Q <sub>G</sub>	V <sub>GS</sub> =-4.5V, V <sub>DD</sub> =-15V, I <sub>D</sub> =-8A (Note 1, 2)		9.8		nC
Gate to Source Charge (Pulsed)	Q <sub>GS</sub>			3.0		nC
Gate to Drain Charge (Pulsed)	Q <sub>GD</sub>			3.7		nC
Turn-ON Delay Time (Pulsed)	t <sub>D(ON)</sub>	V <sub>DD</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A, R <sub>G</sub> =10Ω, R <sub>L</sub> =3.8Ω (Note 1, 2)		10		ns
Rise Time (Pulsed)	t <sub>R</sub>			16		ns
Turn-OFF Delay Time (Pulsed)	t <sub>D(OFF)</sub>			55		ns
Fall-Time (Pulsed)	t <sub>F</sub>			22		ns
SOURCE TO DRAIN DIODE SPECIFICATIONS						
Body Diode Continuous Forward Current	I <sub>S</sub>	T <sub>A</sub> =25°C			-1.0	A
Body Diode Pulse Current (Note 2)	I <sub>SP</sub>				-18	A
Forward Voltage (Pulsed)	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1.2	V

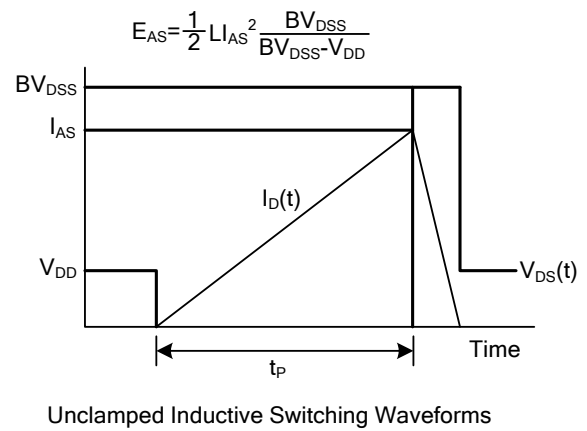
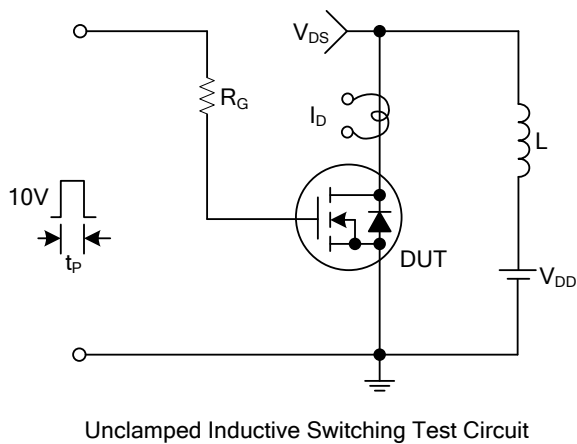
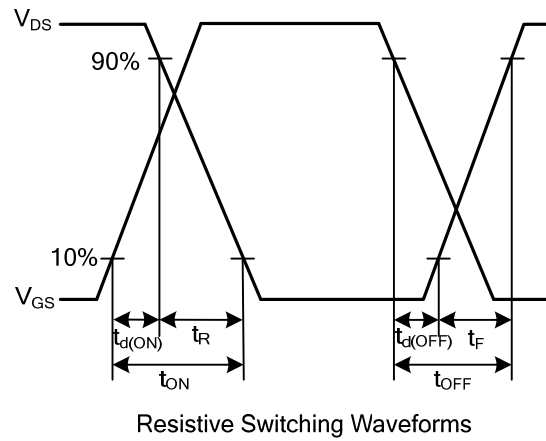
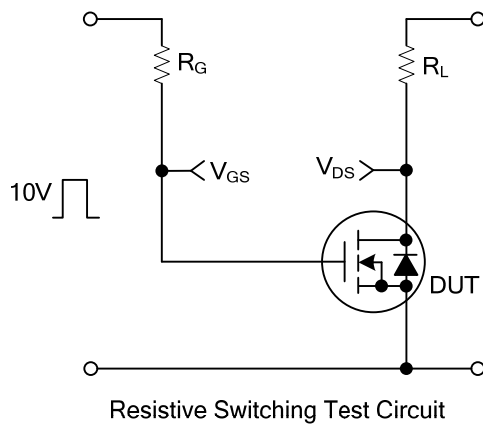
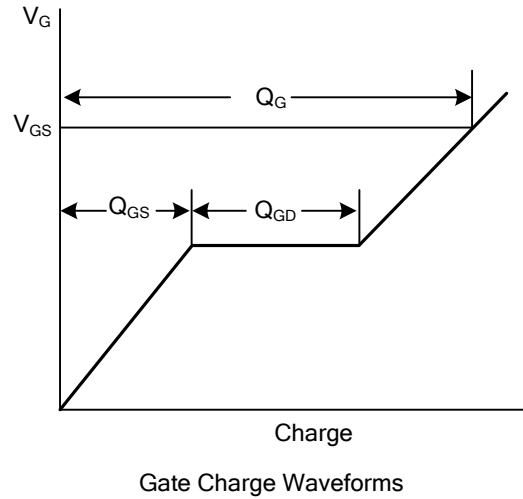
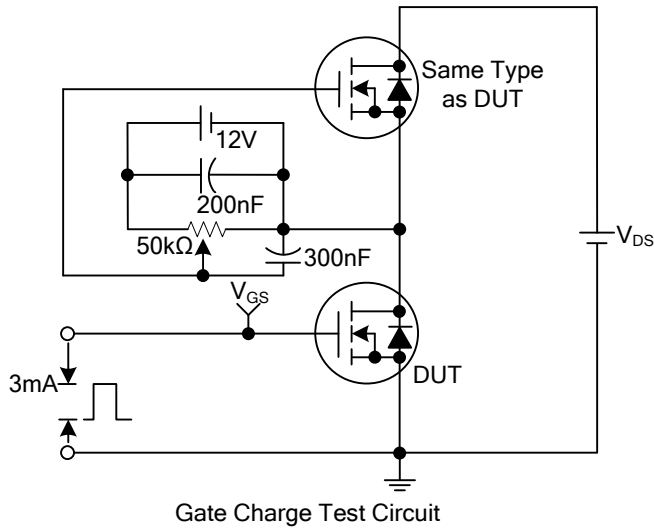
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

3.  $P_W \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ .

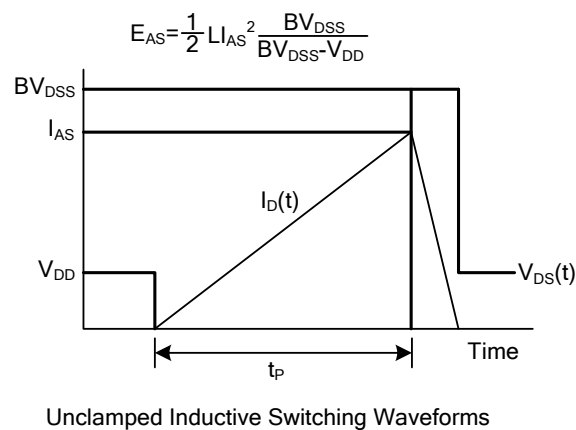
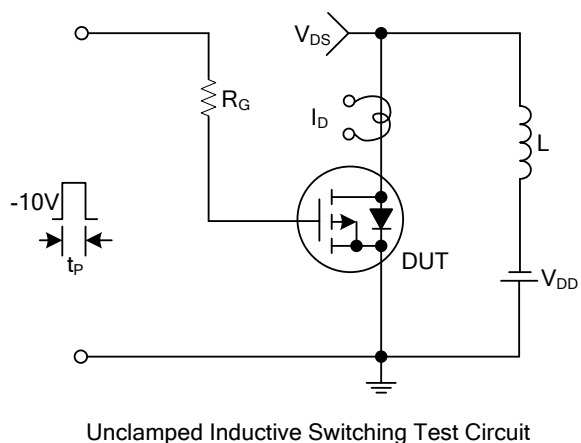
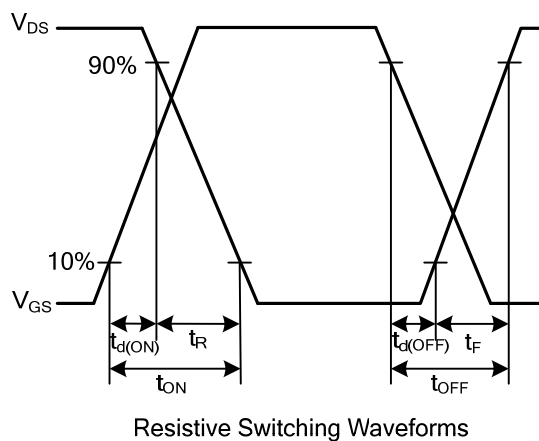
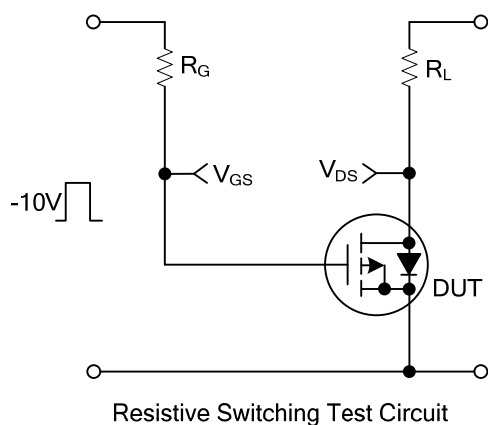
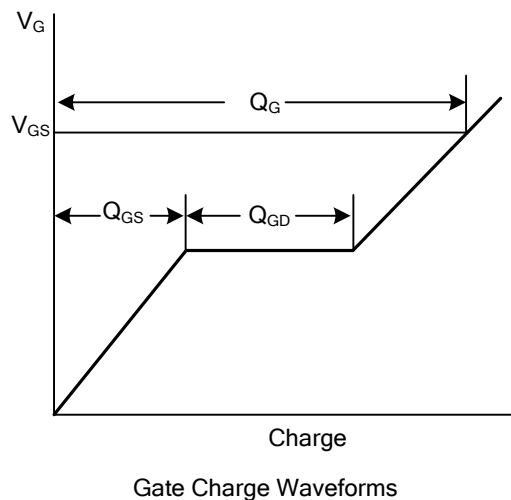
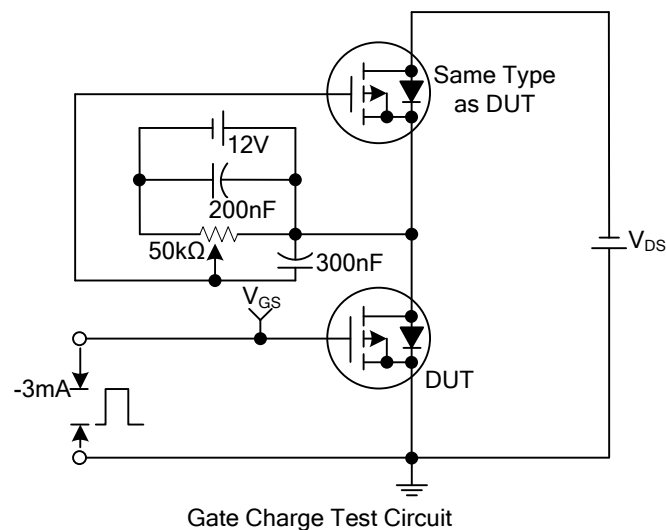
### ■ TEST CIRCUITS AND WAVEFORMS

#### N-CHANNEL



### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

#### P-CHANNEL



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